

SHARP SERVICE MANUAL

CODE: 00ZFO3700ASME



FACSIMILE

MODEL FO-3700

CONTENTS -		
CHAPTER 1. GENERAL DESCRIPTION	CHAPTER 5. CIRCUIT DESCRIPTION	
[1] SpecificationsI-I[2] A look at the machine1-2[3] Operation Panel1-3[4] Transmittable DocumentsI-4[5] InstallationI-5[6] Quick reference guideI-9	[1] Circuit description5-1[2] Control PWB description5-2[3] Description of CCD board5-11[4] TEULIU (with Speech PWB unit) board circuit description5-I 1[5] Description of power supply5-I 4	
CHAPTER 2. ADJUSTMENTS [1] Adjustments 2-I	CHAPTER 6. CIRCUIT SCHEMATICS AND PARTS LAYOUT	
[2] Diagnostics and service soft switches2-2[3] Trouble shooting2-13[4] Error code table2-14	[1] Control circuit/PWB	
CHAPTER 3. MECHANISM BLOCKS [1] General description	[3] Speech circuit/ PWB 6-I 3 [4] Power supply circuit/PWB 6-15 [5] CCD circuit/PWB 6-I 7 [6] Operation panel circuit/PWB 6-I 8	
CHAPTER 4. DIAGRAMS [1] Blockdiagram 4-1	[7] LCD unit circuit/PWB	
[2] Wiring diagram4-2[3] Point to point diagram4-3[4] Connector signal name4-4	CHAPTER 7. OPERATION FLOWCHART [1] Flowchart	
	[2] IC signal name 8-3 PARTS GUIDE	

Parts marked with "A" is important for maintaining the safety of the set. Be sure to replace these parts with specified ones for maintaining the safety and performance of the set.



Caution: Please use the per guage Part No. OJZC214460003 when repairing printes section. The inkjet cartridge consumeable cannot be exposed to the atmosphese for prolong periods.

CHAPTER 1. GENERAL DESCRIPTION

[1] SPECIFICATIONS

Applicable telephone line: Public switched telephone network/

PBX

Compatibility: ITU-TS (CCITT) G3 mode

Configuration: Hatfduplex, desktop transceiver

Compression scheme: Modified Huffman and Sharp special

mode.

Scanning method: Flat-bed, solid-state CCD

Resolution: Horizontal:

8 lines/mm Vertical:

Standard — 3.85 lines/mm
Fine/Halftone — 7.7 lines/mm

Recording system: Thermal ink jet recording/Plain bond

paper

Display: 7 x 5 dots, 1 line by **16-digit** display

Reception modes: Auto/Manual/Answering machine

Modem speed: 9600 bps with automatic **fallback** to

7200, 4800, or 2400 bps

Transmission time*: Approx. **15** seconds

(Sharp special mode)

Effective recording width: 203 mm (average)

Input document size: Automatic feeding:

Width — 148 to 216 mm Length — 128 to 279 mm

Manual feeding:

Width -- 148 to 216 mm

Length - 128 to 1000 mm

Options: Option memory upgrade PWB

FO-1MD: 4Mbit DRAM (4bit x 1M)

-- 2рсе.

FO-2MD (Not recommended)

Effective scanning width: 210 mm max.

Automatic document feeder: Standard (20 sheets max.)

Halftone (grey scale): Standard (64 levels)

contrast control: Automatic/Dark selectable

Copy function: Standard
Telephone function: Standard

(cannot be used if power fails)

Power requirements: 230 ~ 240 V AC, 50 Hz

Operating temperature: 5 to 35°C

Recommend temperate: 15 to 35°C

Power consumption: Stand-by: 13 W

Transmission: 20 W Reception: 23 W copy: 34 w

Dimensions: Width: 393 mm (Without attachment) Depth: 285 mm

Height 212 mm

Weight: Approx. (without attachments) 6.9 kg

★ Based on ITU-TS (CCITT) Test Chart #1 at standard resolution in Sharp special mode, excluding time for protocol signals (i.e., ITU-

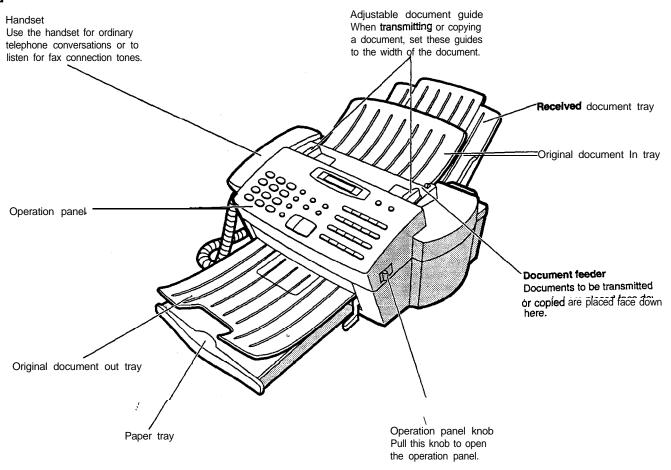
TS phase C time only).

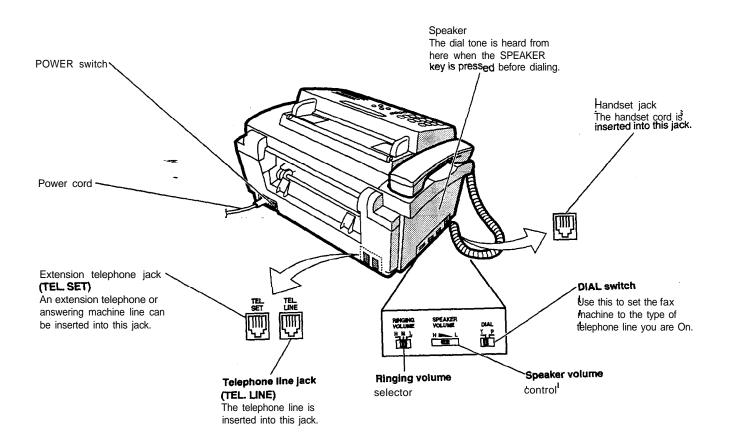
<IMPORTANT PLEASE READ FIRST>

To avoid problems with supplies, plases don't use supplies from other units. Please use new supplies, when supply changes are required.



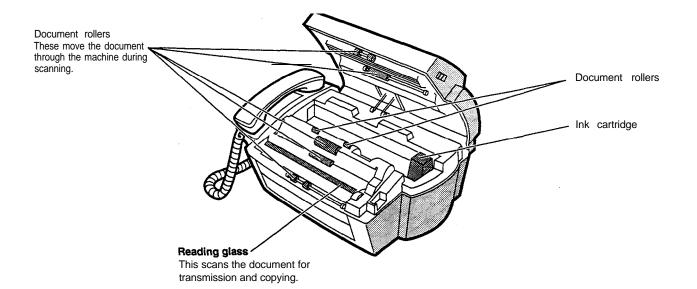
[2] A look at the machine

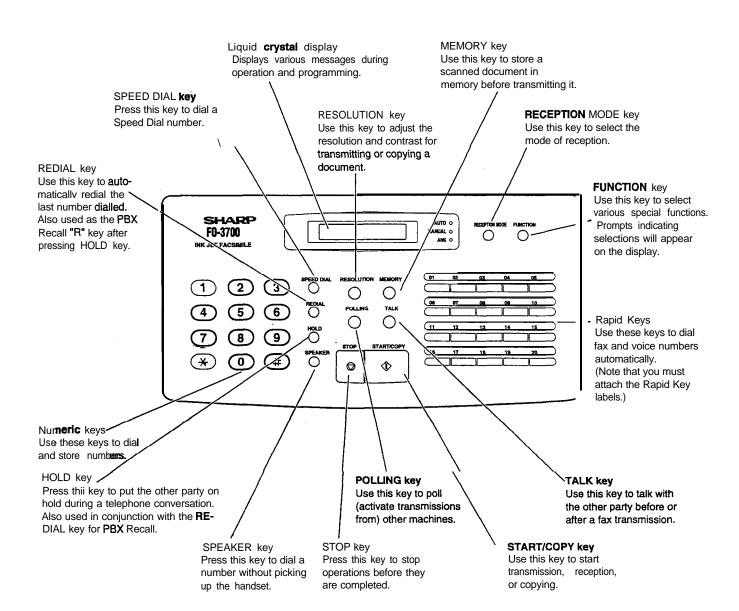






[3] Operation Panel



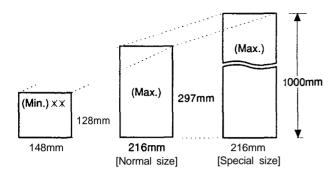




[4] Transmittable Documents

1. Document Sizes

Normal size	width	148 – 216 mm
Normal Size	length	128-279mm



- With special sizes, only one sheet can be fed into the machine at a time. Insert next page into feeder as current page is being scanned.
- ** Use Document carrier sheet for smaller documents.

2. Paper Thickness & Weight

ADF 15	Thickness	0.12 mm	
Normal size	sheets	Weight	52 -104g/m ²
	ADF 20	Thickness	0.06-0.09 mm
sheets	Weiaht	52 -74.3a/m²	
Special size		Thickness	0.12 - 0.20 mm
Special size		Weight	52 – 157 g/m ²

3. Document Types

Normal paper

Documents handwritten in pencil (No. 2 lead or softer), fountain pen, ball point pen, or felt-tipped pen can be transmitted. Documents of normal contrast duplicated by a copying machine can also be transmitted.

. Diazo copy (blueprint)

Diazo copy documents of a normal contrast may be transmitted.

Carbon copy

A carbon copy may be transmitted if its contrast is normal.

4. Cautions on Transmitting Documents

- Documents written in yellow, greenish yellow, or light blue ink cannot be transmitted.
- Ink. glue, and correcting fluid on documents must be dry before the documents can be transmitted.
- All clips, staples and pins must be removed from documents before transmission.
- Patched (taped) documents should be copied first on a copier and then the copies used for transmission.
- All documents should be fanned before insertion into the feeder to prevent possible double feeds.

5. Automatic Document Feeder Capacity

Number of pages that can be placed into the feeder at anytime is as follows:

Normal size: max. 20 sheets (52 - 74.3 g/m²) Special size: single sheet only (manual feed)

OTES: • When you need to send or copy more pages than the feeder limit, place additional pages in feeder when last page in feeder is being scanned.

Place additional pages carefully and gently in feeder.
 If force is used, double-feeding or a document jam may result.

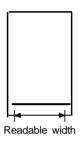
6. Readable Width & Length

The readable width and length of a document are slightly smaller than the actual document size.

Note that characters or graphics outside the effective document scanning range will not be read.

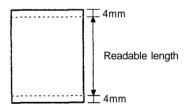
· Readable width

210 mm max.



· Readable length

This is the length of the document sent minus 4 mm from the top and bottom edges.

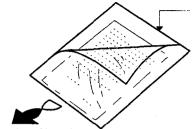




7. Use of Document Carrier Sheet

A document carder sheet must be used for the following documents.

- Those with tears.
- Those smaller than size 148 mm (W) x 128 mm (L).
- Carbon-backed documents



Ensure document is straight in the carrier. Place the document carrier in the document feeder with the clear film side down

Direction of insertion

NOTE: To transmit a carbon-backed document, insert a white sheet of paper between the carbon back of the document and the document carrier.

Those containing an easily separable writing substance (e.g., tracing paper written on with a soft, heavy lead pencil).

NOTES: • When using the document carrier, carefully read the instructions written on the back.

- If the document carrier is dirty, clean it with a soft, moist cloth, and then dry it before using for transmission.
- Do not place more than one document in the carrier at a time.

[5] Installation

1. Site selection

Take the following points into consideration when selecting a site for this model.

ENVIRONMENT

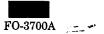
- · The machine must be installed on a level surface.
- Keep the machine away from air conditioners, heaters, direct sunlight, and dust.
- Provide easy access to the front, back, and sides of the machine.
 In particular, keep the area in front of the machine clear, or the original document may jam as it comes out after scanning.
- The ambient temperature should be between 5' and 35'C (Recommend temperate 15 to 35'C).
- The humidity should be between 30% and 85% (without condensation).

ELECTRICITY

A230 - 240 V, 50 Hz, grounded (3-prong) AC outlet is required.

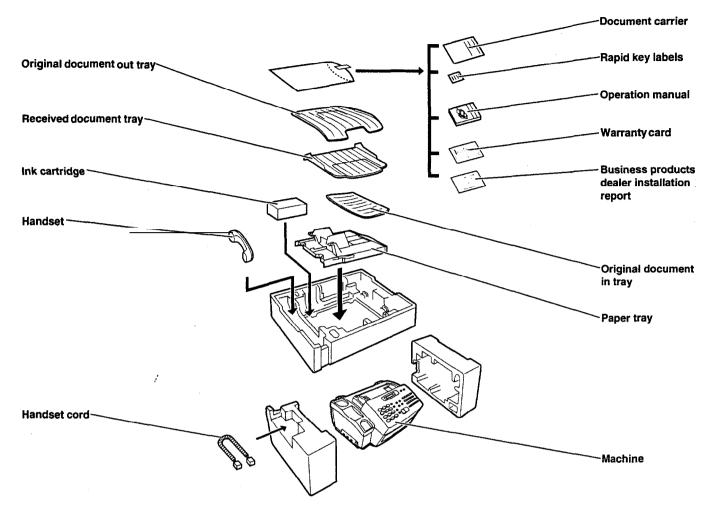
Caution!

- Connection to a power source other than that specified will cause damage to the equipment and is not covered under the warranty.
- If your area experiences a high incidence of lightning or power surges, we recommend that you install a surge protector for the power and telephone lines. Surge protectors can be purchased at most telephone specialty stores.



2. Unpacking

Take the machine out of the box carefully, remove all packing material. Make sure you have all the items listed below.

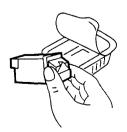


3. Installing the print cartridge

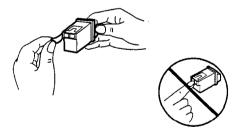
① Pull the operation panel knob on the right side of the machine forward, and open the operation panel.



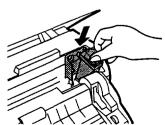
② Open the **print** cartridge container. Grasp the print cartridge by the green **arrow** and remove it from the container.



- ③ Gently remove the tape covering the nozzles on the print cartridge.
 - Be careful not to touch the copper ribbon or the ink nozzles, and do not allow them to touch any surface or object. (Skin oil and dust can cause poor print quality.)

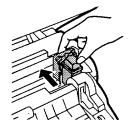


Insert the print cartridge into the cradle with the green arrow on the cartridge top pointing to the green dot on the top of the cradle.





S Push the green arrow forward toward the green dot, 'snapping' the cartridge into place.



6 Close the operation panel.



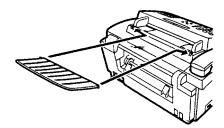
4. Assembly and connections

- ① Connect the handset cord to the handset and the machine as shown.
 - The ends of the handset cord are identical, so they will go into either jack.

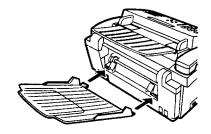
Place the handset on the handset rest.



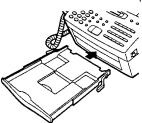
- ② Mount the original document tray.
 - Hold the tray vertically, bend it slightly at the center, and insert the tabs into the holes on the top of the machine as shown.



- 3 Mount the received document tray.
 - Insert the tabs on the tray into the holes on the back of the
 - If desired, flip the tray extender out.



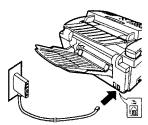
- 4 Mount the paper tray.
 - · Slide the tray in as shown until it clicks into place.



- (5) Mount the original document out tray.
 - Place the original document out tray on the paper tray. Slide it in or out to fit the length of the original.

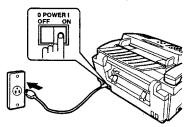


⑥ Insert the small modular plug which terminates the FO80TP adaptor line cord into the jack on the back of the machine marked TEL. LINE. Plug the FO80TP adaptor into the telephone jack on the wall.



Plug the power cord into a 230 ~ 240 V, grounded (3-prong) AC outlet.

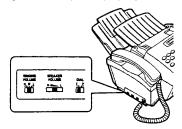
Press the power switch to turn on the power.



- 8 Adjust the volume switches on the left side of the machine:
 - RINGING VOLUME: Set the switch at high (H), medium (M), or low (L) as desired.
 - SPEAKER VOLUME: Adjust the setting from high (H) to low (L) as desired.

Set the **mode** of dialing with the DIAL switch. Select "T" if you are on a touch-tone line, or "P" if you are on a pulse dial (rotary) line.

- Be sure to make the correct setting, or you will not be able to make a call.
- WARNING NOTICE: For all units installed in New Zealand, only the T setting for tone dialing is permitted. The P setting for pulse dialing does not operate properly and must not be used.





5. Loading printing paper

Selecting paper

The inkjet printer in your FO-3700 is designed to work well with most types of plain paper. Bond paper generally produces the best results, and plain paper manufactured for high-quality photocopying is also good. However, variation in paper composition may significantly affect print quality, and you should therefore test paper (printing on both sides) before purchasing large quantities to be assured of the quality you desire.

Size and weight

A4 size paper from 60 to 90 g/m² can be used.

Printing side

Plain paper has a 'print' side which should be used to obtain the best **print** quality. The print side is not visible to the naked eye, so check the label on the end of the package before removing the paper. The print side will be indicated by an arrow, symbol, or wording. Remove the number of sheets you wish to use from the package, and load them in the paper tray print side up as described below.

Do not use paper which is folded, curled, or tom, as it may jam in the **printer**.

Loading paper

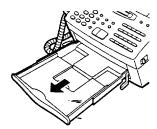
A maximum of 100 sheets of paper can be loaded in the paper tray.

Note: Do not remove the paper tray from the machine to load paper (if the tray is not correctly in the machine when paper is loaded, the paper sensor will not be able to detect the paper).

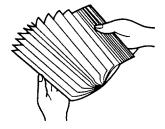
1 Remove the original document out tray.



2 Pull the tray extender out slightly.



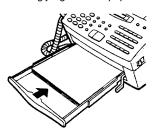
③ Fan the paper, and then tap the edge against a flat surface to even the stack.



- 4 Insert the paper into the tray, print side up.
 - Make sure it fits squarely against the inside of the machine.



- (5) Push the tray extender back in.
 - · Make sure it fits snugly against the paper.



6 Replace the original document out tray.



About the printable area

The area of the page on which the FO-3700 can print is slightly smaller than the page itself. The following dimensions are averages, and there may be slight deviation from these dimensions depending on how the paper is loaded in the tray.

Printing width: 203 mm

Printing length: The length of the page minus 1.5 mm from the top,

and 12 mm from the bottom.

The FO-3700 has been set at the factory to automatically reduce the size of received documents to 92%. This can be configured for 100% reception by user switch. This ensures that data at the extreme edges of A4 size documents is not lost.

Note: If you receive a document which is too long to be printed on one sheet of paper, the remainder will be printed on a second page.

1.0

[6] Quick reference guide

Use this guide as a convenient reminder for operating the machine after you have read the detailed instructions for each procedure in the manual.

Note: Steps which are optional are enclosed in a dotted frame:

Transmitting documents

Normal Dialling	Load document RESOLUTION OF PRESS SPEAKER Dial (press numeric keys) Wait for reception tone ARESOLUTION OF PRESS SPEAKER TARTICOPY THANG UP
Direct Keypad Dialling	Load → AESOLUTION → Dial (press → Improve the provention of t
Rapid Key Dialling	Load Accument Press Rapid Key
Speed Dialling	Load document → RESOLUTION ACCOUNTION OF THE Speed Dial Number (press 2 → number (keys)
Redialling	Load document → RESOLUTION O REDIAL → Wait for reception tone →

Making voice calls

Normal Dialling	Lift handset or → Dial (press → you pressed SPEAKER SPEAKER
Rapid Key Dialling	Lift handset or → Press Rapid → you pressed SPEAKER SPEAKER
Speed Dialling	Lift handset or press SPEAKER SPEED MAL Finter Speed Dial number you pressed SPEAKER SPEAKER
Redialling	PEDMAL → Lift handset

Operations using the FUNCTION key

The following chart shows how **to** access **FUNCTION** key operations. Once you have accessed the desired operation, follow the instructions which appear in the display.

Answering machine settings	FUNCTION # O# X (Frees) until desired itemappears)
Timer operations	FUNCTION 1
Printing out lists	PUNCTION 2 -> ## or **X (Press until desired item appears)
Entry mode settings	FUNCTION 3 -> ## or **X* (Press until desired item appears)
Setting User Switches	PUNCTION 4 → # or X (Press until desired item appears)
Relay Request '	FUNCTION 5 -> ## or **X* (Press until desired item appears)
Broadcasting	FUNCTION 6
Multi-copying	FUNCTION #





CHAPTER 2. ADJUSTMENTS

[1] Adjustments

General

The following adjustments and settings are provided for this model, make adjustments and/or settings as necessary.

1. Adjustments

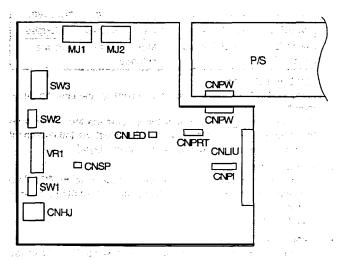
Adjustments of output voltage (FACTORY ONLY)

- 1. Install the power supply unit in the machine.
- 2. Set the recording paper and document.
- 3. When the document is loaded, power is supplied to the output lines. Confirm that outputs are within the limits below.

Output voltage settings

Output	Voltage limits	Foctoryad
+12V	11.4V ~ 12.6V	None
-12v	-12.6V ~ -11.4V	None
+5V	4.75V ~ 5.25V	None
VM (+24V)	23.3V ~ 24.7V	VR1

(Top view)



Connector — No	CNPW
Pin No.	
1	MG
2	MG
3	+24V
4	+24V
5	DG
6	+5V
7	AG
8	+12V
9	-12v

Fig. 1

2. Settings

(1) Telephone/Fax setting

Perform the following adjustments and settings for the telephone and facsimile functions.

a) Ringing volume

The desired telephone ring volume can be selected from among three levels: high, middle, and low.

b) Speaker volume

When the SPEAKER key is pressed, the speaker volume is adjusted with this control.

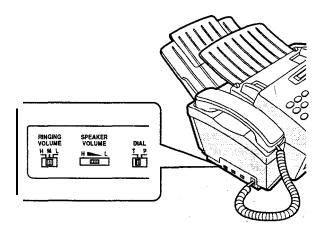
c) Dial mode selector

(tine selector)

Select P(Pulse) or T(Tone) according to the type of the telephone

P: Pulse dial telephone mode (10 PPS)

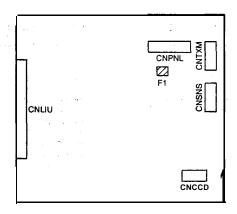
T: Tonedial tone telephone mode (Tone)



3. IC protectors replacement

ICPs (IC Protectors) are installed to protect the motor driver circuit. ICPs protect various ICs and electronic circuits from an overcurrent.

The location of ICPs is shown below:



(1) F1 (ICP-20) is installed in order to protect IC's from an overcurrent generated in the motor drive circuit. If F1 is open, replace it with a new one.

In addition to the replacement of F1, the factor causing F1 to open must also be repaired. If not, F1 will open again.

Replacement parts

ICP-N20 (Sharp code: VHVICPN20//-1)



[2] Diagnostics and service soft switches

1. Diagnostics description

(1) Entering the diagnostic mode

Press the keys in the following sequence $\boxed{FUNC} \rightarrow \boxed{9} \rightarrow \boxed{*} \rightarrow \boxed{8}$ $\rightarrow \boxed{*} \rightarrow \boxed{7}$, and the following display will appear.

ROM1:xxxxxxxx (This code is ROM version name.)

Then press the START/COPY key. Select the desired item by pressing or or rapid key. Press the START/COPY key to go into the selected diagnostic mode.

(2) Diagnostic items

rapid key	contents
01	SOFT SWITCH MODE
02	ALL BLACK PRINT
03	ROM & RAM CHECK
04	AUTO FEEDER MODE
05	AGING MODE
06	PANEL CHECK MODE
07	CHECK PATTERN
0 8	PRODUCT CHECK
09	SIGNALSEND MODE
10	MEMORY CLEAR
11	MEMORY SET MODE
12	FSK SEND MODE
13	ENTRY DATA SEND
14	ENTRY DATA RCV.
15	TEL. NUMBER SET
16	CCD ADJUST MODE
17	PAPER EXIT MODE
18	PRINTER SELF TEST
19	INK TEST MODE

(3) **Description** of diagnostic items

01 SOFT SWITCH MODE

Used to change the soft switch settings.

After setting soft switch 22, the following display will appear.

PRINT SFTSW LIST and 1: YES OTHER: NO

Then 'SOFT SWITCH TABLE' list is printed by pressing 1 key.

₫	SOFT SWIT	ICH TABLE	•
đ	SW01=00000010	SW11=00000110	₫
•	SW02=00001000	SW12=00000000	•
•	SW03=0000000	SW13=00010000	•
₫	SW04-00000000	SW14=00000001	•
•	SW05=00000100	46619000000	*
•	SW06-00000101	SW16=10001000	•
₫	SW07=00001100	SW17=00000000	•
•	SW08=01010010	SW18=10010010	•
•	4664666666	SW19=00010000	•
₫	SW10-00000000	SW20=10000000	₫
*			4

02 AU **BLACK** PRINT

Used to check **the** printer head. All black pattern printable area of page is printed.

03 ROM & RAM CHECK

Used to check the program ROM and the work RAM area of the machine and the printer, and check the status of the printer. The ROM check is used for ROM, and a read/write matching test is used for the **RAM**.

The result of the machine check is given by a number of beeps, and the total result is given by printing the 'ROM & RAM CHECK TABLE' list.

number of beeps

0 beep	NO ERROR
1 beep	ROM ERROR
4 beeps	SRAM ERROR
5 beeps	DRAM ERROR (Standard)
6 beeps	DRAM ERROR (Option)

Description of printer status

ОК	normal
OUT OF INK	The ink is missing.
SENSOR ERROR	The sensor for printer head is abnormal.
NOZZLE CLOGGED 1 to 3	1 to 3 nozzles are clogged.
NOZZLE CLOGGED 4 to 11	4 to 11 nozzies are clogged.
NOZZLE CLOGGED more than 11	More than 11 nozzles are clogged.

04 AUTO FEEDER MODE

Used to check auto feed function by inserting and ejecting documents.

Place documents in the hopper before entering **this mode**, then **press** the **START/COPY** key to start the test. As the document sensor is actuated, the document size is displayed.

05 AGING MODE

If a document is placed in the hopper, a copy is taken first. If no document is present, a sheet of test patterns is printed out every 60 minutes.

(Total 10 sheets)

06 PANEL CHECK MODE

Used to check proper key operation, Each key entry is displayed on the LCD.

The test results will be also printed.

•					
•	PANEL CHE	CK LIST			
* 1	2	3	4	5	
• 2	7	8	9	*	
• 🛮		SPEED DIAL	REDIAL	HOLD	
● IPNGGEN	RESOLUTION	POLLING	MEMORY	TALK	
• START/COPY	RECEPTION	FUNCTION		02	
• 03	04	05	06	07	
• 08	09	00	00	12	
• 13	14	15	16	17	
• 18	19	n 🗀	STOP		
•					AUTO
•	PAREL CHEC	K OK			
•					



07 CHECK PATTERN

Used to check the nozzles of the print head. The following pattern is printed out on the sheet, A total of two sheets will be produced.

1 Frame line:

The frame line shows the print area of a page.

The horizontal ruled line at the lower end is specified with soft SW 18 bit 3. 4, and 5.

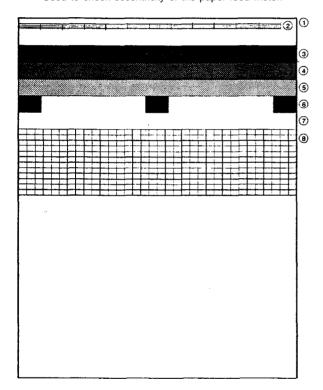
② Nozzle test:

The pattern is used to check for a clog in the 46 nozzles of the print head.

- 3 Vertical stripes 1
- 4 Vertical stripes 2
- S Light grey
- 6 All-black block:

Three all-black blocks for checking print density

- All white
- Paper feed accuracy check pattern: Used to check eccentricity of the paper feed motor.



08 PRODUCT CHECK

(Diags, 07, 06, and 03 take place in succession)

Used to carry out the CHECK PATTERN, PANEL CHECK MODE, and ROM & RAM CHECK CAPITAL **CETIERS** successively.

09 SIGNAL SEND MODE

Pressing the START/COPY | key after entering this mode will transmit modem signals out of the TEL Line, in the following order. Used to check the modem. (Monitor from (This is also audible from the speaker of the machine) the TEL line socket to check signals.)

- [1] No signal (CML signal turn on)
- [2] 9600bps(V.29)
- [3] 7200bps(V.29)
- [4] 4800bps(V.27ter)
- [5] 2400bps(V.27ter)
- [6] 300bps(FLAG)
- [7] 21 00Hz(CED) [8] 11 00Hz(CNG)
- [9] PSEVDC RINGER

10 MEMORY CLEAR

Used to clear the memory. The soft switches and the user switches will be set to initial states. The following will be printed.

* MEMORY CLEARED *

11 FACTORY USE ONLY

12 FSK SEND MODE

Delivers various signals of 300bps in the following data pattern at the level set by the soft switch.

 $00000 \rightarrow 11111 \rightarrow 010101 \rightarrow 11110 \rightarrow 00001$

13.14. Diag #13. #14 are used together.

 Press the Start key on the receive data unit first then the send unit.

13 ENTRY DATA SEND

Used to send the data registered in memory by signals of **2400bps**, and copy the registering contents.

Registering contents

- [1] rapid number
- [2] speed number
- (But date & time is excluded.)
- [4] relay tx data
- [5] soft switch settings

14 ENTRY DATA RCV.

This mode is the reception mode of ENTRY DATA SEND. The received data is registered in **the** memory, and 'TELEPHONE LIST', 'PASSCODE LIST'. 'USER SWITCH LIST'. 'SOFT SWITCH LIST' are printed.

15 TEL. NUMBER SET

The TEL number and name registered in rapid number 01 can be copied to **all** rapid numbers. The first 3 characters of the copied name is changed such as **"RO2"**.

Additionally, the TEL number and name registered in speed number 01 can **be** copied to all speed numbers. The first 3 characters of the copied name is changed such as **"S02"**.

16 CCD ADJUST MODE

Used to execute copy operation. When the STOP key is pressed, **the** unit goes into the waft state to adjust the CCD line alignment. When the START/COPY key is pressed again, the copy operation is resumed.

17 PAPER EXIT MODE

Used to check the recording paper feed function by inserting and ejecting recording paper. This mode will continue until recording paper is out, or the **STOP** key is pressed.

18 PRINTER SELF TEST

Used to check **the** state of the printer. This the check prints the check pattern provided by the printer driver.

19 INK TEST MODE

Used to check the state of the printer. A check pattern as with diagnostic 07 is produced when this mode is entered.



2. Soft switch description

Soft switch list

3W	Bit	Itom	Switch setting and function					Default	Note	
JO.	No.	Item		1			0		Derauit	Note
	1	Line density choice	FINE			STAN	DARD		0	USER
										SWITCH
	2	Reserved							0	
	3	Reserved					_ _		0	
s W	4	Activity report print	Automatic prir full	ntout after memo	ory	No pri	ntout when me	mory is full	0	USER SWITCH
1	5	Reserved							0	
	6	Reserved				-			0	
	7	Reserved		_					1	
	8	Automatic switching mode	Phone/lax aut	omatic switching	g	switching to fax only			0	USER SWITCH
	1	Reserved		_					0	
	2	Reception 4800 BPS fixed	Yes			No			0	
_	3	Reserved	 						0	
S	4	CED tone-signal interval	500 msec			75 ms	ec		0	
W		Line equalizer	Yes			No			1	
2	5		163			-			0	-
	6	Reserved	 						0	+
	7	Reserved				N-	<u> </u>		0	
	8	"NOZZLE CLOGGED" display	Yes			No				<u> </u>
	1	Max.page length for transmit, receive and copy	No limit		·	1.5m	ax.for copy and max. for receiv		0	
	2	Footer print	ON		.:	OFF			0	USER
S	3	Sender's phone number registration	Cannot be ch	anged	Can b	e changed] O [
W 3	4 7	otal communication hours and pages print	No		Yes			0		
	5	Reserved	_				_		0	
	6	Reserved					_		0	
	7	CSI transmission	Not transmitte	d		transmitted			0	
	8	Communication error treatment (reception) in RTN sending	Not transmiss	ion error	Ţ	ransmission error			0	
	1.	Protocol Monitor 1	Printed at con	n.err		Not P	rinted		0	
	2	Dialing pause	4 seconds			2 seconds			0	
	3	Reserved	<u> </u>				_		0	
S W	4	NSF receive acknowledge in G3 transmit mode	Twice				for NSF recept for DIS recept		0	
4	5	Non-modulation carrier in V29 transmission mode	Yes			No			0	
	6	EOL detect timer	25 seconds			5 sec	onds		0	
	7	Protocol Monitor	Yes			No			0	
	8	Line Monitor ~	Yes			No	.,		0	
	-		 	Standard	Lic	ght	Dark	Darker		
	1	Fine and standard contrast	No.1	0		0	1	1	0	
	2	control	No.2	0			0	1	0	1
			140.2	Standard		ght	Dark	Darker	 	
s	_	11-16	No 2	 		911t 0	1	1	- O	
w	3	Halftone contrast control	No.3	0			0	1	0	
5	4		No.4	0		1	·	 	+	+
	1					eak	Strong		٠.	
	5	Halftone MTF correction	No.5	0		0	1 1	1 1	- 0	
	6		No.6	0		1	0	1	1	
	7	Reserved				<u> -</u>			0	
	8	Reserved				<u> </u>			0	



sw	Bit	Item	Switch setting and function							5	, , , ,	
No.	No.	TOTAL			1				0		Default	Note
				transmi- ssion or com.err	cancel or com. error	printe at E/T/M	a	rinted at only trans.	Prints every time	Not printed		USER SWITCH
	1	Communication results printout	No.1	1	1	0		0	0	0	7 0	
S	2	(Transaction report)	No.2	0	0	0		0	1	1] o	
W 6	3		No.3	1	0	0		1	0	1	0	
	4	Sender's information delivery	No				Yes				0	
	5 6 7 8	Remote operation code figures by external TEL(0-9)	-	Binary input 8 4 2 1 (Binary) No. = 5 6 7 8 (Bit No.)								USER SWITCH
	1	H2 mode	OFF				ON				0	
	2	ECM (MMH)	No				Yes				0	
s w	3	ECM mode	No				Yes				0	USER SWITCH
7	4	ECM oct/frame	64 oct	-			256 oc	ct			0	
•	5 6 7 8	Modem signal transmission level (dBm)	Binary No.	input 8 4 2 =5 6 7							1 1 0 0	
	1	Mode signal transmission level (dBm)	ON (O	VER –16dl	Bm)		OFF		/		0	
	2	Reserved			-				_		1	
S	3	Reserved						0				
W	4	Reserved								1		
8	5	Reserved								0		
	6	Reserved		_	_	:					0	
	7	Reserved			_				_		1	
	8	Reserved		_					_		0	
		Dummy ringer rumbling time at			15sec.	30s	ec.	60s	ec.	120sec.		USER
.	l	TEL/FAX automatic switching mode	No		0			1		1	0	SWITCH
1	2		No	.2	0	1)	1	0	
	3	Busy tone detection continuation sound detect time	10sec			,	5sec	1			0	
S	.				2pulse	4pu		6pu	lse	10pulse	1	
W 9	4	Number of Busy tone detection	No		0			1 1		1	0	
9	5	pulses	No		0	1		<u> </u>)	1	1	
ļ	6	Busy tone detection ON/OFF time (Lower limit)	350ms				200ms				0	
	7	Busy tone detection ON/OFF time (Upper limit)	650ms				750ms				0	
	8	Busy tone detection intermittent sound detection	No				Yes	,			0	
					3 sec.	1 s		no b		Not used	1	USER
	1	Beep length select	No		0	C		1		1	0	SWITCH
-	2	_	No	.2	0	1		0		1	0	
-		Remote receive control function	No		<u>-</u>		Yes				0	
s w		Switching to reception when A.M.memory full	After 6	rings				not switc			0	A.M. MODE
10			·	·		9	4 %		10	00%	 -	USER
'	5	Reduce of copy and manual		No.5			0 0			0	SWITCH	
	6	receive	No.6		0 1		0	1.05-				
				footer off			92 % 100%			4	USER	
	_					(91 % 100%			4 _	SWITCH	
		Reduce of auto receive										
,	7 8	Reduce of auto receive		footer on No.7 No.8								0 0



SW	Bit	Item		Switch	setting a	and fun			Default	Note	
No.	Ño.					0					
	1	Busy tone detection frequency		480 Hz	4	440 Hz			0		
	2	Reserved							0		
			50ms 8			80ms 100 ms		120 ms			
S	3	DTMF detection time	No.3	0	1	T	0	1	0		
W	4		No.4	0	0		1	1	0		
11	5	Quiet detect time (When in A.M	Binary input(0	Binary input(0 ~ 10)							
	6	mode)		8 4 2 1 (Binary)							
	7	,		7 8 (Bit N					1	1	
	8			(0:Not detect)						1	
	1	MH fixed	Yes	Yes No							
	2	MMR	No		Y	/es			0		
_	3	F27 mode	No		V	es .			0		
S	4	F70 mode	No			res			0		
W				15)	<u></u>	65			0	A.M. MODE	
12	5	Quiet detect start time	Binary input(0	~ 15) 2 1 (Bina	\				0	A.W. WODE	
	6		No -5 6	7 8 (Bit N	iy) lo\				o		
	8		140. = 5	/ 0 (Dit iv	10.7				Ŏ		
			 	250 ms	500 m	nc	750 ms	1000ms	+	 	
		Datastian time of heats at the	No.1	0	0	1.0	1	1	0		
	1	Detection time of hook state by							-		
	2	external telephone(on → off)	No.2	0	1		0	1	0		
S				250 ms	500 m	ns	750 ms	1000ms	_		
W	3	Detection time of hook state by	No.3	0	0		11	1	0		
13	_ 4	external telephone(off \rightarrow on)	No.4	0	1		0	1	1		
	5	Reserved		_					0		
	6	Reserved							0		
	7	Reserved	 						0		
	8	Reserved	 	-					0		
		.10001704							0		
	1	Reserved							0	 	
	2	Reserved							0	 	
	3	Reserved								 	
S	4	Reserved	ļ						0	ļ	
W	1		V.29			V.27ter			4		
14	1	·	9600	7200)	0 0		2400	_		
	5	Modem speed	0	0				0	0		
	6		0	0	}		0	0	0		
	7		0	1		1 0		0	0	Į.	
	8		1 1			0 0			1	1	
	†	DTMF 3 digits at remote	Yes		N	No					
	1	reception	, , ,						0	-	
	 	DTMFdetection cycletime	58 ms		9	93 ms			0		
	2	off time	8 ms			28 ms		1	}		
	3	A.M.mode CNG detecting	No			Yes	· · · · · · · · · · · · · · · · · · ·		0		
s	4	Protection from echo	Yes			No			0	1	
W	5	CNG detection time	ON 225ms				325ms		0		
15		(Lower limit)	OFF 2150ms				450ms			1	
	-	CNG detection time	ON 775ms				675ms		0		
	6	(Upper limit)	OFF 3850ms				550ms			1	
		(Opper mint)		1 time	2time		3times	4times	+	 	
	_	ONO description	NI			03			┥ .		
	7	CNG detection number of times	No.7	0	0		1	1	1		
	8	(A.M. mode)	No.8	0	1		0	1	0		
	1	CNG detection number of	Twice			Once			1		
		times(TEL/FAX mode)							+	 	
	2	Reserved							0		
	3	Reserved							0		
S		Bassand							0	<u> </u>	
	4	Reserved	1			12 hou			T 4	1	
W	5	Time format	24 hours		7	1∠ nou	rs	1			
	-		24 hours	D.M.Y				JPN	 	D:Day	
W	5	Time format		D.M.Y	M.D.	.Y_	Y.M.D	JPN 1	0	D:Day M:Month	
W	5		No.6	0	M.D. 0	Υ_	Y.M.D 1	1	0	M:Month	
W	5	Time format		 	M.D. 0	Υ_	Y.M.D 1 0				



sw	Bit	lta			Switch	n setting	g and fu	ınction		D (11	
No.	No.	Item			1			0		Default	Note
s W 17	1 2 3 4 5 6 7 8	Off Hook Hold (Answer delay in A.M. n	node)	128	Sinary Input(0 ~ 255) 128 64 32 16 8 4 2 1 (Binary) No. = 1 2 3 4 5 6 7 8 (Bit No.)						
	1	Cut off mode(Copy mod	·	Yes	* '		No			1	USER SWITCH
	2	Cut off mode(Com. mod	Yes			No			0	USER SWITCH	
		Table 19			Letter		4	legal			CHECK
S. W	3	Paper select		No.3	0)	1	1	0	PATTERN
18	4			No.4	• 0	1	t	. 0	1	1	ONLY
10	5	Extended print area		ļ		No			0		
				Blank rows Duplicate last row Smo		Smoo	othing	othing Constant line density Duplication Smoothing			
	6	Vertical scale method	No.6	0	0	()	0	1	0	
	7		No.7	0	0		1	1	0	1	
	8		No.8	0	1	()	1	0	0	
s W	1 2 3 4	Number of rings for auto	receive		~9) 4 2 1 (Bina 2 3 4 (Bit N					0 0 0 1	USER SWITCH
19	5 6 7 8	Automatic switching from to auto receive mode	n manual	Binary input(0 ~ 9) 8 4 2 1 (Binary) No. = 5 6 7 8 (Bit No.) 0:does not change					0 0	USER SWITCH	
s	1 2 3 4	Dummy ringer transmiss (dBm)	sion level	Binary input 8 4 2 1 (Binary) No.=5 8 7 8 (Bit No.)					1 0 0		
20	5	Reserved			_					0	
20	6	Reserved			_					0	
ĺ	7	Remote operation auto	disable	Limit (7 sec)			No lim	it		0	
Ī	. 8	Reserved			_			-		0	

Soft switch setup

Soft **switch** settings stored in memory can be changed by entering data from **the** keyboard. SW1 through SW20 constitutes soft switches. See the soft switch listing on page 2-4 and 2-7 for the function of **SW1** through SW20 settings.

How to make soft switch settings

To enter the softswitch mode, make the following key entries in sequence.

'FUNCTION', "9", "*", "8", "#", "7", 'START/COPY', 'START/COPY' SW1 bii No.1 through No.8 will be displayed.

Press the "#" or "*" key and bring the cursor (blinking pointer) to the bii No. which is to be changed. (The "#" key moves the cursor to the **right,** and the "*" key to the left.)

Press the FUNCTION key to change the setting between 1 and 0. When the cursor is on data No.8 position, press the "#" key to display SW2 data No.1 through No.8. pressing the START/COPY button shifts cursor to the next soft switch

Make settings in the same way as for SW2. Proceed to the settings of SW3 to SW20 in the same manner

When the cursor is on SW20 data No.8 position as shown below, press the "#" key to finalize all settings.

To finish the settings halfway between SW1 and SW20, press the STOP key. in this case, the setting being performed to the SW No. on display will be nullified while settings **performed** to the preceding SW Nos. remain in effect.

Soft switch functional description

SW1 No.1 Line density choice

Used to set the transmission mode which is automatically selected when the MODE key is not pressed. In copy mode, however, the fine mode is automatically selected unless the MODE key is manually set to another mode.

0: Standard

1: Fine

default: 0

SW1 No.2, 3 Resewed

Set to '0'.



SW1 No.4 Activity report print

This soft switch is used to select; whether or not to print out the activity report when the memory is full. An activity report can be printed when the following key entry command is made.

'FUNCTION', "2", "#", 'START/COPY

After producing the activity report, $\mathbf{a}\mathbf{l}\mathbf{l}$ the data in the memory will be cleared.

When the switch function is set to "0" (NO), the data in the memory will be deleted from the oldest as it reaches the maximum memory capacity.

0: NO (first data lost when memory is full)

1: YES (when memory is full)

default: 0

SW1 No.5 ~ 6 Reserved

Set to "0".

SW1 No.7 Reserved

Set to 'l'.

SW1 No.8 Automatic switching mode

This soft switch is used to set the auto **tel/fax** select mode or to set the normal fax mode.

0: Switching to fax only

1: TEL/FAX automatic switching

Default: 0

SW2 No.1 Reserved

Set to "0".

SW2 No.2 Reception 4800 BPS fixed

When line conditions warrant that the reception take place at 4900 BPS repeatedly. It may improve the success of reception to start at 4900 BPS. This improves the receiving document quality and reduces handshake time due to **fallback** during training.

0: NO 1: YES Default: 0

SW2 No.3 Reserved

Set to 0.

SW2 No.4 CED tone-signal interval

For international communication, the **2100Hz** CED tone may act as an echo suppresser switch, causing \boldsymbol{a} communication problem. Though this soft switch is normally set to "0", it should be set to '1' so as to change the timer between CED tone and DIS signal from 75ms to 500ms to eliminate the communication problem caused by echo.

0: 75 ms 1: 500ms Default: 0

SW2 No.5 Line equalizer

Used to set the Line equalizer function.

On **→ 7.2km**

Off \rightarrow 0km 0: Off \rightarrow 0km

1: On → 7.2km

Default: 1

SW2 No.8 ~ 7 Reserved

Set to '0'.

SW2 No.8 "NOZZLE CLOGGED" display select

You can select whether the prompt 'NOZZLE CLOGGED' is displayed or not when nozzie on the printer cartridge dogged.

0: Not display

1: Display

Default: 0

SW3 No.1 MAX.page length for transmit, receive and copy

Used to set the maximum page length.

To avoid possible paper jam, the page length is **nomally** limited to 1 meter for copy or transmit, and 1.5 **metres** for receive.

It is possible to set it to 'No limit to transmit **a-long** document, such as a computer print from, etc. (In this case, the receiver must **also** be set to no limit.)

0: 1 m max. for copy and transmit, 1,5m max. for receive (std)

1: No limit

Default: 0

SW3 No.2 Footer print

When set to "1", the date of reception, the sender's machine No., and the page No.are automatically recorded at the end of reception.

0: off 1: On

Default: 1

SW3 No.3 Sender's phone number registration

Used to make a choice of whether the registered sender's phone number can be changed or not. If the switch is set to 'l', new registration of the senders phone number is disabled to prevent accidental wrong input.

0: Can be changed

1: cannot be changed

Default: 0

SW3 No.4 Total communication hours and pages print

Used to make a choice of whether the total communication time and pages are recorded in the activity report.

0: Yes 1: No Default: 0

SW3 No.5, 6 Resewed

Set to "0".

SW3 No.7 CSI transmission

CSI signal contains the sender's phone number registered in the machine.

0: Transmitted

1: Not transmitted

Default: 0

SW3 No.8 Communication error treatment (reception) in RTN sending

Used to determine communication error treatment when RTN is sent by occurrence of a received image error in **G3** reception. When it is set to 'I', communication error is judged as no error.

0: Transmission error

1: Not transmission error

Default: 0

SW4 No.1 Protocol Monitor 1

If set to "1", protocol is printed at communication error.

0: off 1: On Default: 0

SW4 No.2 Dialing pause

The length of the pause inserted between telephone numbers of direct dial contraction. can be adjusted. Selection of 4 **sec** or 2 **sec pause** is available.

0: 2 seconds

1: 4 seconds

Default: 0

SW4 No.3 Reserved

Set to "0".

SW4 No.4 NSF receive acknowledge in G3 transmit mode

Used to make a choice of whether reception of NSF (DIS) is acknowledged after receiving two **NSFs (DISs)** or receiving one NSF **(two DISs)**.

It may be useful for overseas communication to avoid an echo **sup**-presser problem, if set to 1

0: 1 for NSF reception, 2 for DIS reception

1: 2 times

Default: 0

SW4 No.5 Non-modulation carrier in V29 transmission mode

Though transmission of non-modulated carrier is not required for transmission by the V29 modem according to the **CCITT** Recommendation, it may be **permitted** to send non-modulation carder before the image signal to avoid an echo suppresser problem.

It may be useful for overseas communication to avoid an **echo sup**-presser problem, if set to 1.

0: No

1: Yes

Default: 0



served Reserved 8 Modem speed de mine the initial modem speed. The default is 9600bps. icessary to program it to a slower speed when frequent is encountered, in order to save the time required for scedure. V29 9600BPS 7200BPS . V27ter 4800BPS : 2400BPS at owl No.1 DTMF 3 digits at remote reception 1 to make a choice of whether to use the 3 digits code or 2 digits a for remote receive. 0: No 0× → 00× 1: Yes 1×→ 10× 5¥ → 50× efault: 0

$9 \times \rightarrow 90 \times$ SW15 No.2 DTMF detection cycle/off time

Used to choose the cycle time and off time.

Normally set to "0". When the DTMF signal not detected. You can set to '1'. The DTMF detection is shortened.

0: Cycle time 93ms

Off time 28ms

1: Cycle time 58ms

Off time 8rns

Default: 0

SW15 No.3 A.M mode CNG detecting

Choke is made whether the CNG signal is not detected in A.M (Answering machine) mode.

0: Yes 1. No

Default: 0

SW15 No.4 Protection from echo

Used to protect from echo in reception.

0: No 1: Yes

Default: 0

SW15 No.5 CNG detection time (Lower limit)

Used to determine the lower limit of CNG detect time.

0: ON 325ms

OFF 2450ms

1: ON 225ms

2150ms OFF

Default: 0

SW15 No.6 CNG detection time (Upper limit)

Used to determine the upper limit of CNG detect time.

0: ON 675ms

OFF 3550ms

1: ON 775ms

OFF 3850ms

Default: 0

SW15 No.7, 8 Number of CNG signal detection in A.M mode

Used for detection of CNG in 1 ~ 4 pulses in answering machine mode.

00: 1 time

01.2 times

10: 3times

11: 4 times

Default: 10

SW16 No.1 Number of CNG signal detection at the TEL/FAX automatic switching mode

Used for detection of CNG in one or two pulses in the TEL/FAX automatic switching mode

0: Once

1: Twice

Default: 1

SW16 No.2 ~ 4 Reserved

Set to "0".

SW16 No.5 Time format

Choice is made for the format of time display

0: 12 hours mode (AM/PM)

1: 24hoursmode

Default: 1

SW16 No.6, 7 Date format

Used to set entry of date into activity report and LED format according to specifications of country.

00: day/month/year

01: month/day/year

10: year/month/day

11: year/month/day (Japan mode: month is not English)

Default: 00

SW16 No.8 Vertical resolution

Used to set the Vertical resolution.

0: 300 dpi

1: 7.7 line/mm

Default: 0

SW1 7 No.1 ~ 8 Off hook hold

Used to set 'Off hook hold' time by binary input.

00000000: 0 second

11111111: 255seconds Default: 00000000

SW18 No.1Cut off mode (Copy mode)

When in copy, if the scanned data is out of the range of recording, the operator has one of the choices below using the switch.

0: Continue: Data is printed onto the next page.

1: Cut off: Data scanned out of the limit is cut off.

Default: 1

SW18 No.2 Cut off mode (Com. mode)

When receiving, if the data is out of the range of recording, the operator has one of the choices below using the switch.

0: Continue

1: cutoff

Default: 0

SW18 No.3, 4 Paper select

Used to set the media size. (Letter /A4 / Legal) check pattern only

00: Letter size

01: A4

10: Legal

Default: 01

SW18 No.5 Extended print area

Used to choose of extended print area enable or disable.

0: Disable

1: Enable

Default: 0

SW18 No.6 ~ 8 Vertical scale method

Used to choose of Vertical scale method.

Method used to create the additional rows needed to provide the proper vertical size.

000: Blank rows

001: Duplicate last row

010: Smoothing

011: Constant line density (Duplication)

100: Constant line density (Smoothing)

Default: 010



<scale method=""></scale>	Value	Standard Mode	Fine Mode
	. *	1 A A A A A A A A A A A A A A A A A A A	1 A B 3
Blank rows	0 x 00	4 5 6	4 5 D
	, * v	7	7 <u>E</u> 8 <u>F</u> 9
Duplicate last row	0 x 01	1	1 A 2 B 3 ///B/// 4 C 5 D 6 ///D/// 7 E 8 F
Smoothing	0 x 02	1	1 A 2 B 3 // (B, C) // , 4 C 5 D 6 // (D, E) // , 7 E 8 F 9 // (F, G) // ,
Constant line density using duplication	0×03	1 A 2 /// K//// 3 4 B 5 /// B /// C /// B /// C /// 9	1 A 2 B 3 4 C 5 D 6 7 E 8 F
Constant line density using smoothing	0 x 04	1	1 A B B S S S S S S S S S S S S S S S S S

SW19 No.1 ~ 4 Number of rings for auto receive

When the machine is set in the auto receive mode, the number of rings before **answer** is made **can be** selected. It may be set from one to nine **rings using a** binary number. Since the facsimile telephone could be used as an ordinary telephone if the handset is taken off the hook before connection is made to the facsimile while ringing, it should be programmed to the user's choice. If a facsimile calling beep is heard when the handset is taken off the hook, press the START/COPY key and put the handset on the hook to have the facsimile start receiving. If it is set to above 9, receive rings are automatically set to 1.

0001: 1 time

1001: 9 times Default: 0001

SW19 No.5 ~ 8 Automatic Switching from manual to auto receive mode

Choice is made to after how many rings in the manual receive mode it should be **automatically** changed to the facsimile answer mode or remain in the manual receive mode. Entering a binary number 0 will force the machine to remain in the manual answer mode. If a number between 1 and 9 is entered, the machine will go into the answer mode after the given number of rings. However, it can be used as an ordinary telephone if the handset is taken off the hook before this programmed number has elapsed. Entry of a number above 9 will set the machine to 0.

0000: Does not change

0001: 1 times

1001: 9 times Default: 0000

SW20 No.1 ~ 4 Dummy ringer transmission level (dB)

Pseude-ringer sending level setting 0dBm ~ -15dBm.

0000: **0dBm**

1111: -15dB

Default: 1000 (-8dBm)

SW20 No.5 - 6 Reserved

Set to '0'.

SW20 No.7 Remote operation auto disable

Selection of remote operation (5, st) inhibition after passing a certain time from reception of Remote operation auto disable.

0: Not inhibited.

1: Inhibited automatically after 7 sec.

Default: 0

SW20 No.8 Reserved

Set to '0'.



[3] TROUBLE SHOOTING

Refer to the following actions to troubleshoot any of problems mentioned in I-6.

- [1] A communication error evoked.
- [2] Image distortion produced.
- [3] Unable to do overseas communication
- [4] Communication speed slow liable to failback.
 - Increase the transmission level SOFT SWITCH 7-5 6 7 8 Can be used in case [1] [2] [3]
 - Decrease the transmission level SOFT SWITCH 7-5 6 7 8 Can be used in case [3]

- Apply line equalization SOFT SWITCH 2-5 Can be used in all cases.
- Slow down the transmission speed SOFT SWITCH 14-5 6 7 8 Can be used in case [2] [3]
- Replace the LIU PWB.
 Can be used in all cases.
- Replace the control PWB.
 Can be used in all cases.
- * If transmission problems still exist on the machine, use the following format and check the related matters.

TO:	ATT:		Ref.No. :
CC:	ATT:		Date :
FM:			Dept :
			Sign :
	***** Facsimile communica	tion problem *****	Ref.No.:
From: Mr.	Fax Tel No.	:	Date:
Our customer	Name	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	Tel No.
	Address		Fax No.
	Contact person		Model name
Other party	Name		Tel No.
	Address	~	Fax No.
	Contact person		Model name
Problem mode	Line: Domestic / international	Mode: G3	Phase: A. B. C. D.
		atic reception / Manual reception	
	Autom	atic dialling /Manual dialling / Others	
Frequency:		% ROM version:	
Confirmation item Comment	Our customer A1 A2 C1 C2 E E Our service	D2	Please mark problem with an X No problem is: 0 Al A2 Bl B2 Cl C2 Dl D2 El E Transmission level setting is () dB at our customer Transmission level () dBm Reception level () dBm By level meter at Bl and B2
Counter-measure	tro 63 days and satisfing regard as prof		



[4] Error code table

Transmission errors

E-O	Able to recognize handshake signal, but it has errors.
E-i	Cannot recognize the handshake signal from the receiver side.
E-2	Line disconnected during transmission.
E-3	Line disconnected after modem speed fall-back.
E-4	tine disconnected during multi-page transmission.
E-6	Cannot recognize the handshake signal for next page at receiver side.
E-7	No response from receiver side or 'disconnect signal" is received at transmitter side.

Reception errors

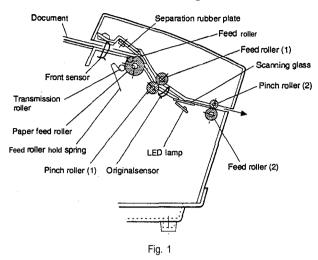
E-O	Able to recoonize handshake signal , but it has errors.
E-I	Line disconnected during reception.
E-2	Cannot recognize the handshake signal from the transmitter side.
E-3	Cannot recognize the last handshake signal from the transmitter side.
E-4	Cannot recognize the handshake signal for next page from the transmitter side in the case of mode change.
E-5	Cannot recoonize the handshake signal for next page from the transmitter side.
E-7	No response from transmitter or 'disconnect signal' is received at receiver side.



CHAPTER 3. MECHANISM BLOCKS

[1] General description

1. Document feed block and diagram



2. Document feed operation

- The document placed in the hopper actuates the front sensor.
 After one second, the pulse motor starts to drive the paper feed roller. The document is automatically taken up into the machine, and stopped at the original sensor.
- After a specified number of pulses are received from the document lead edge being sensed, scanning is started.
- When a specified number of pulses are received from the document rear edge being sensed, scanning is terminated and the document is fed through.
- 4) If the front sensor is active (i.e., another document is in the hopper), when the preceding document scanning is completed and and it is fed out, the next document is taken up into the machine. If the front sensor is not active (i.e., there is no document in the hopper), when the document is fed out, the operation is terminated.

3. Hopper mechanism

3-1. General view

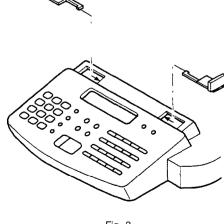


Fig. 2

The hopper is used to align documents with the document guides adjusted to the paper width.

NOTE: Adjust the document guides before and after inserting the document

3-2. Automatic document feed

- Use of the paper feed roller and separation rubber plate ensures error-free transport and separation of documents. The plate spring presses the document to the paper feed roller to assure smooth feeding of the document.
- 2) Document separation method: Separation rubber plate

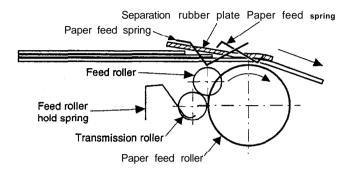


Fig. 3

3-3. Documents applicable for automatic feed

	4x6 series (788mm x 1 1000mm sh		Square meter series			
	Minimum	Maximu	Minimum	Maximu		
Feeder capacity	20 sheets, r	nax.				
Paper weight	45kg	64.3kg	52g/m ²	74.3g/m²		
Paper thickness (ref.)	0.06mm	0.09mm	0.06mm	0.09mm		
Paper size	`	n x 182mm) ~ n x 29 7mm), L		n _x 279mm)		
Feeder caoacitv	15 sheets. r	nax.				
Paper weight	45kg	90kg	52g/m ²	104g/m²		
Paper thickness (ref.)	0.06mm	0.12mm	0.06mm	0.12mm		
Paper size	B6 (128mm x 182mm) ~ A4 (210mm x 297mm), Letter (216mm x 279mm)					
Paper quality	High quality	paper or equ	ivalent			

NOTE: Double-side coated documents and documents on facsimile recording paper should be inserted manually. The document feed quantity may be changed according to the document thickness.

Documents corresponding to a paper weight heavier than 64.3kg $(74.3g/m^2)$ and lighter than 135kg $(157g/m^2)$ are acceptable for manual feed.

Documents heavier than 135kg in terms of the paper weight must be duplicated on a copier to make it operative in the facsimile.

3-4. Loading the documents

- Make sure that the documents are of suitable size and thickness, and free from creases, folds, curls, wet glue, wet ink, clips, staples and pins.
- 2) Place documents face down in the hopper.
 - i) Adjust the document guides to the document size.
 - ii) Align the top edge of documents and gently place them into the hopper. The first page under the stack will be taken up by the feed roller to get ready for transmission.

NOTES: 1) Curled edge of documents, if any, must be straightened

Do not load the documents of different sizes and/or thicknesses together.



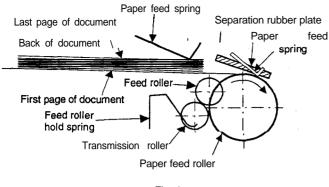


Fig. 4

3-5. Documents requiring use of document carrier

- 1) Documents smaller than B6 (128mm x 182mm).
- 2) Documents thinner than the thickness of 0.06mm.
- 3) Documents containing creases, folds, or **curls**, especially those whose surface is curled (maximum allowable curl is 5mm).
- 4) Documents containing tears.
- 5) Carbon-backed documents. (Insert a white sheet of paper between the carbon back and the document carrier to avoid transfer of carbon to the carder.)
- Documents containing an easily separable writing material (e.g., those written with a lead pencil).
- 7) Transparent documents.
- Folded or glued documents.
 Document in document carrier should be inserted manually into the feeder.

4. Document release

4-1 . Cross section view

(RIGHT SIDE)

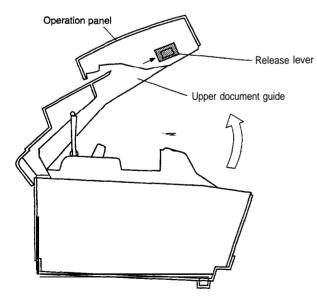


Fig. 5

4-2. General

When the Release Lever is pulled by hand in the direction of arrow A, the latch is released and the upper document guide moves on its axis in the direction of the arrow. The feed rollers, the separation rubber plate, and the pinch rollers become free to make it possible to remove the document.

5. Optical system

(1) General view

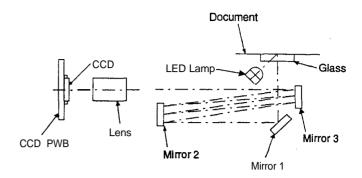


Fig. 6

(2) Composition

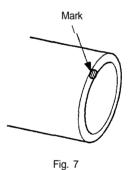
The optical system is composed of the document feed mechanism, the lamp, the reflecting mirrors, the focusing lens, the CCD sensor, and the read process circuit.

5-1. Lamp

The lamp is used to expose the document.

5-2. Lens

The lens is used to **focus** the light reflected from the document on the CCD elements.



5-3. CCD

The CCD (charge coupled device) image sensor consists of a photodiode array which converts the intensity of light reflected from the document surface into a **series** of analog voltages which are then stored in an analog shift register. The series of analog voltages are then converted into a digital equivalent by a **black/white** binary logic circuit.

(Example) Scan signal output waveform

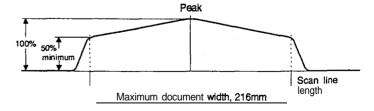


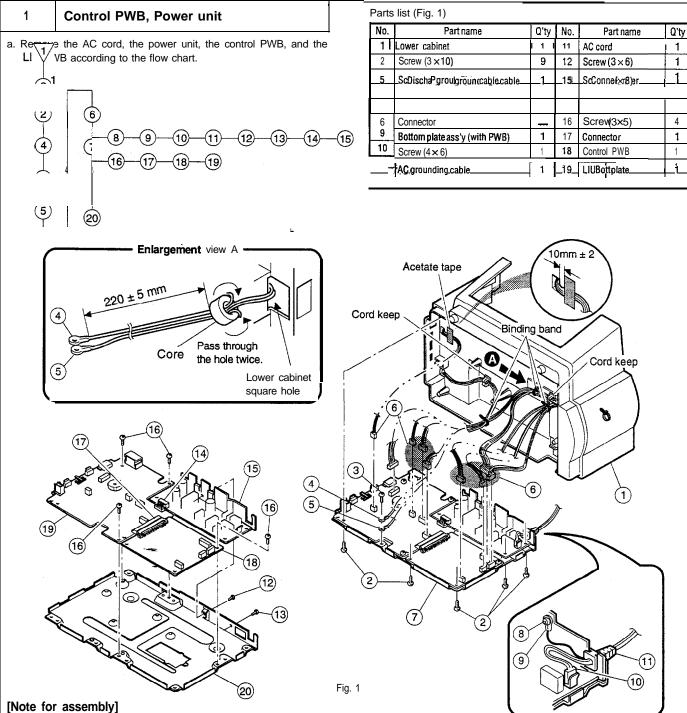
Fig. 8

- The minimum output from the CCD at the maximum scan width of document (216mm) must be more than 50% of the peak value.
- The peak output must be about 150mV under room temperature to avoid CCD saturation.



[2] Disassembly and assembly procedures

- This chapter mainly describes the disassembly procedures. For the assembly procedures, reverse the disassembly procedures.
- Easy and simple disassembly/assembly procedures of some parts and units are omitted. For disassembly and assembly of such parts and units, refer to the Parts List.
- The numbers in the illustration, the parts list and the flowchart in a same section are common to each other.
- . To assure reliability of the product, the disassembly and the assembly procedures should be performed carefully and deliberately.



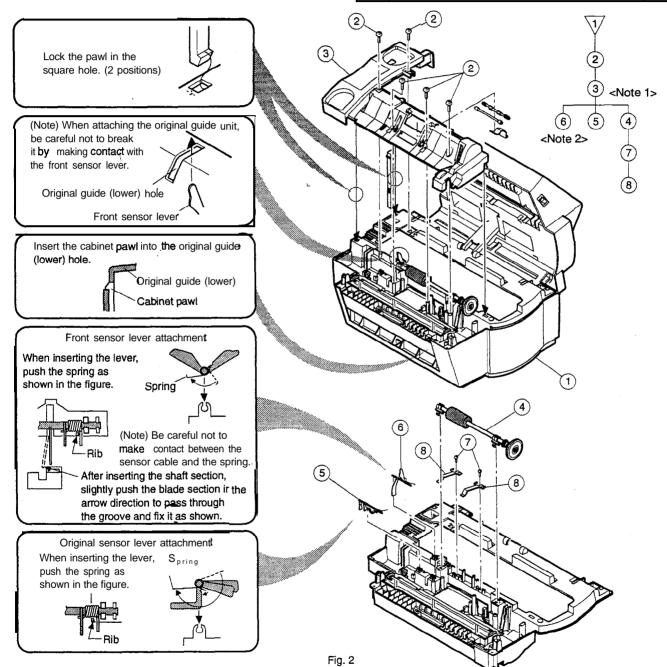
- Insert connectors and securely.
- 2. When setting AC cord (1), note the following points:
- 2. When setting the solid (b), hote the following points.
 - For inserting angle of the AC cord bushing into the mounting angle, refer to the enlargement view of Fig. 1.
 - Fix the AC cable above the AC cord with a screw. (Refer to the enlargement view.)
- 3. When wiring, note the following points:
 - Before binding each cable, wind grounding cables (4) and (5)
 around the core and insert the core into the square hole in
 the lower cabinet until the core is not seen from the outside
 as shown in the enlargement view.
 - Use two bands and two cord keeps for wiring, and fix with acetate tape to eliminate slack in the speaker cable. (Refer to the enlargement view.)



2 Original guide lower, paper feed roller, sensor lever ass'y, etc.

- a. Remove the original guide lower, the paper feed roller, the original sensor lever ass'y, and the front sensor lever ass'y.
 - <Note 1> When removing original guide lower ass'y ③, be careful not to damage the front sensor lever.
 - <Note 2> When removing the front sensor lever ass'y, refer to the enlarged view. Press lever section (a) in the arrow direction to put the lever in the dotted line place. Then turn the lever shaft and remove it upwards.

Parts li	Parts list (Fig. 2)					
No.	Part name	Q'ty				
1	Medium cabinet	1				
2	Screw (3 x 8)	5				
3	Original guide lower ass'y	1				
4	Paper feed roller ass'y	1				
5	Original sensor lever ass'y	1				
8	Front sensor lever ass'y	1				
7	Screw (3 x 8)	2				
8	Pinch pressure spring	2				



[Note for assembly]

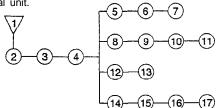
- 1. Be careful to the installing direction of pinch pressure spring **®**.
- 2. When attaching original sensor lever ass'y ⑤ and front sensor lever ass'y ⑥ to the sensor holder, refer to the enlarged view for the spring position and the attachment procedure.
- 3. When attaching original guide lower ass'y $\ensuremath{\mathfrak{G}}$, note the following points:
 - Check that the scanning glass is free from dust, finger prints,
 - Be careful not to damage the front sensor lever.
 - Lock the four pawls and fit them with screws.



3 Optical unit

- Remove the original guide lower ass'y, and the paper feed roller ass'y according to procedure 2-a.
-). Remove the optical unit.

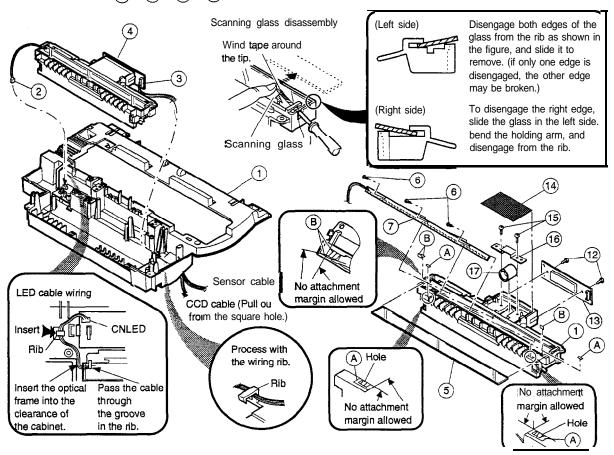
:. Remove the scanning glass, the LED, and the mirror from the optical unit.



Parts list (Fig. 3)

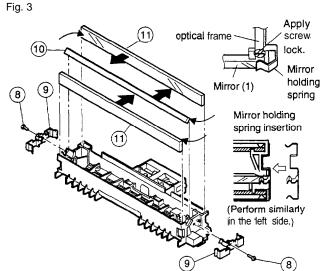
No.	Part name	Q'ty	No.	Part name	Q'ty
1	Medium cabinet	1	10	Mirror (1)	1
2	Connector (LED)	1	11	Mirror (2)	2
3	Connector (CCD)	1	12	Screw (M3 W, red)	2
4	Optical unit	1	13	CCD PWB	1
5	Scanning glass	1	14	Shielding sheet	1
6	Screw (2×6)	3	15	Screw (3×6)	2
7	LED	1	16	Lens holding spring	1
8	Screw (3 × 4)	2	17	Lens	1
9	Mirrorholdingspring	2	l		

<Note> Never disassemble the CCD PWB and the lens except when replacing. If they are disassembled, the optical unit adjustment must be performed.



[Note for assembly]

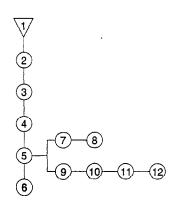
- 1. When attaching mirrors 10 and 10, note the following points:
 - Be careful of the mirror surface direction as shown in the figure.
 - Be careful that the mirror surface is free from finger prints and dust
- Arrange the LED and the CCD cable under the rib. (Refer to the enlarged view.)
- 3. For attachment reference of dust-proof sheet (A) and reflection sheet (B), refer to the enlarged view.





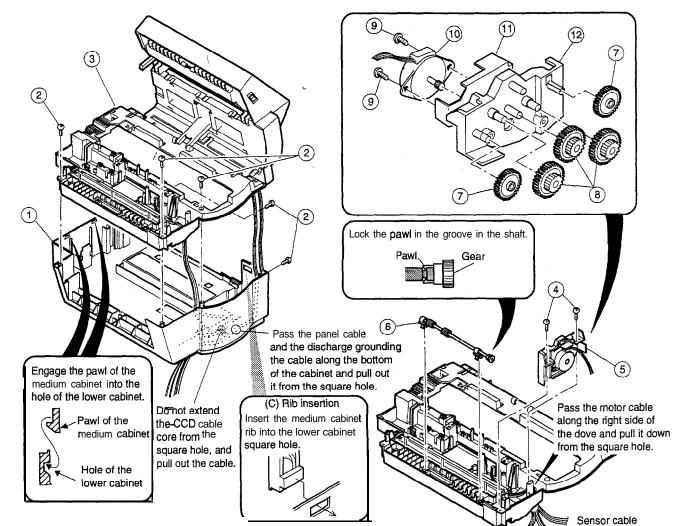
4 Drive system unit

- Remove the PWB section (the control PWB, the LIU PWB, the power unit) according to procedure I-a, and remove the cable from the cord keep.
-). Remove scanner section 3 in Fig. 4 from lower cabinet 1.
 - <Note 1> Be careful not to hang PWBs by the cable.
 - <Note 2> Remove two pawls and one rib. (Refer to the enlarged view.)
- :. Remove the drive system unit and the transport roller.



	(0 /	
No.	Part name	Q'ty
1	Lower cabinet	1
2	Screw	5
3	Scanner section	1_
4	Screw	2
5	Drive system unit	1
6	Transport roller ass'y	1
7	Reduction gear	2
8	Reduction gear	3
9	Screw	2
10	Drive motor	1
11	Motor mounting plate	1
12	Drive frame	1

Parts list (Fig. 4)



[Note for assembly]

- 1. When attaching drive motor (1) to drive frame (18), be careful of the attaching direction. The connector PWB must be in the upper side. (Refer to Fig. 4.)
- 2. When attaching reduction gears ⑦ and ⑧, note the following points:
 - Apply Molykote to the mounting shaft of drive frame

 reduction gear, and the teeth surfaces of reduction gears

 and

 8.
- Lock reduction gear ⑦ to the pawl of the mounting shaft.

CCD cable

Motor cable (Pull out from the square hole.)

- 3. For wiring, observe the notes in Fig. 4.
- When fixing scanner section ③ to the lower cabinet, attach the two pawls and one rib to the lower cabinet securely.

Fig. 4

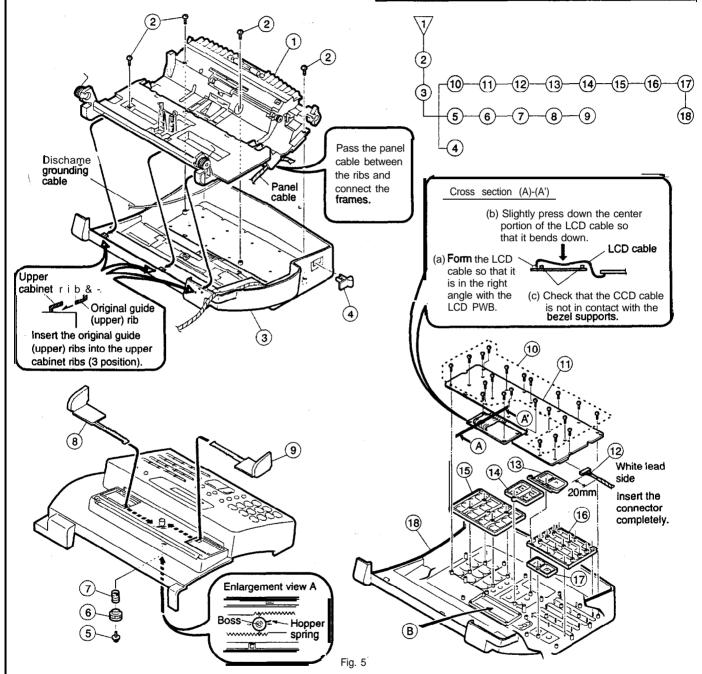


5 Upper cabinet section (Panel, hopper, etc.)

- Remove the scanner section from the lower cabinet according to procedures 4-a, b.
- Remove the upper cabinet section from the upper frame section, and remove the panel PWB, keys, and hoppers.

Parts list	(Fig.	5)
------------	-------	----

No.	Part name	Q'ty	No.	Part name	Q'ty_
1	Upper frame ass'y	1	10	Screw (2×6)	21
2	Screw (3 × 8)	4	11	Panel PWB	1
3	Upper cabinet section	1	12	Panel cable	1
4	Release knob	1	13	Start/stop key	1
5	Screw (3 × 6)	1	14	Auto/manual select key	1
6	Pinion gear	1	15	Dial key	1
7	Hopper spring	1	16	One-touch key	1
8	Hopper guide (R)	1	17	Function key	1
9	Hopper guide (L)	1	18	Upper cabinet	1



[Note for assembly]

- 1. When connecting panel cable ②, insert the white cable as shown in Fig. 5.
- 2. When attaching panel PWB (1), note the following points:
 - Check that the LCD installing section (B) in Fig. 5) of upper cabinet (B) and the glass surface of the LCD are free from dirt.
 - When tightening screws (10), be sure to tighten

- For wiring of the LCD cable, refer to the enlarged view.
- 3. For attaching direction of hopper spring ⑦, refer to the enlarged view
- When attaching upper cabinet section ③ to the upper frame ass'y, put the upper frame ass'y rib under the upper cabinet rib. (Refer lo the enlarged view.)
- 5. For wiring of the panel cable, refer to the note in Fig. 5.

and 🔲



6

Upper frame section (original guide upper section)

- **a.** Remove the scanner section from the lower cabinet according to procedures 4-a, b.
- Remove the upper cabinet section from the upper frame section according to procedure 5-b.
- **>.** Remove the release lever, the original insertion guide, the pinch roller, and the transport roller from me upper frame section.

<Note 1> Use a small screwdriver to remove screw (4) and be careful not to scratch pinch roller (5).

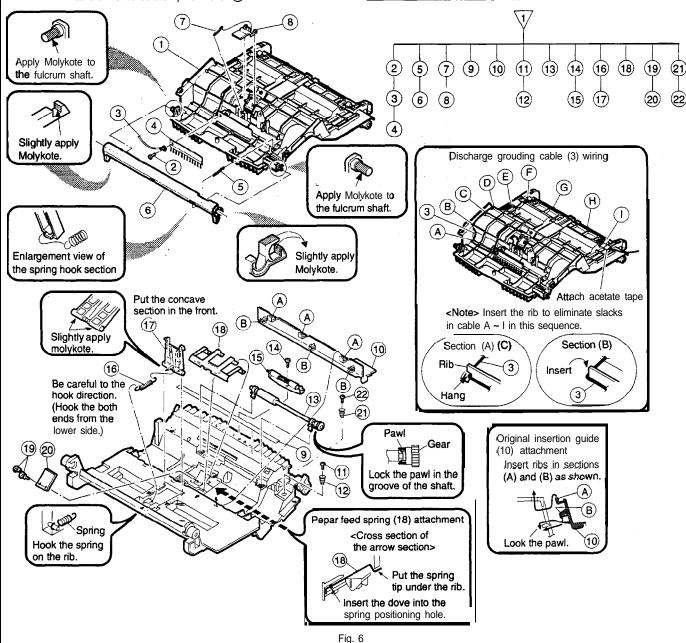
No.	Part name	Q'ty	No.	Part name	Q'ty
1	Upper frame section	1	12	Open/close spring 2	1
2	Screw (3 × 6)	1	13	Transport roller ass'y	1
3	Discharge brush grounding cable	1	14	Screw (3×6)	1
4	Discharge brush	1	15	Pinch roller ass'y	1
5	Release lever return spring	1	16	Open/close spring	1
6	Release lever	1	17	Stopper plate	1
7	Separation pressure spring	1	18	Paper feed spring	1
8	Separation pressure plate	1	19	Step screw	1
9	Back sheet	1	20	Separation rubber plate	1
10	Original insertion guide	1	21	Open close spring	1

1 22 Screw (3 × 6)

1

Parts list (Fig. 6)

11 Screw (3 × 6)



[Note for assembly]

- When attaching paper feed spring (18), be careful not to scratch separation rubber plate (20), and insert securely. (Refer to the enlarged view.)
- When attaching stopper plate ①, apply Molykote to the stopper section (referring to the enlarged view), and place the concave section in the front surface, and attach open/close spring ⑤ as shown in Fig. 6. Be careful not to reverse me spring hook.
- 3. For attaching direction of original insertion guide ①, refer to the enlarged view.
- 4. When attaching release lever (6), apply Molykote to the enlarged view section of Fig. 6, and attach release lever return spring (5) as shown in the enlarged view. Be careful to the spring hook direction.
- For wiring of discharge brush grounding cable ③, refer to the enlarged view.



1

FO-3700A

7 Ink jetter unit

:Note> This section describes the procedures to remove the ink jetter section from the body.

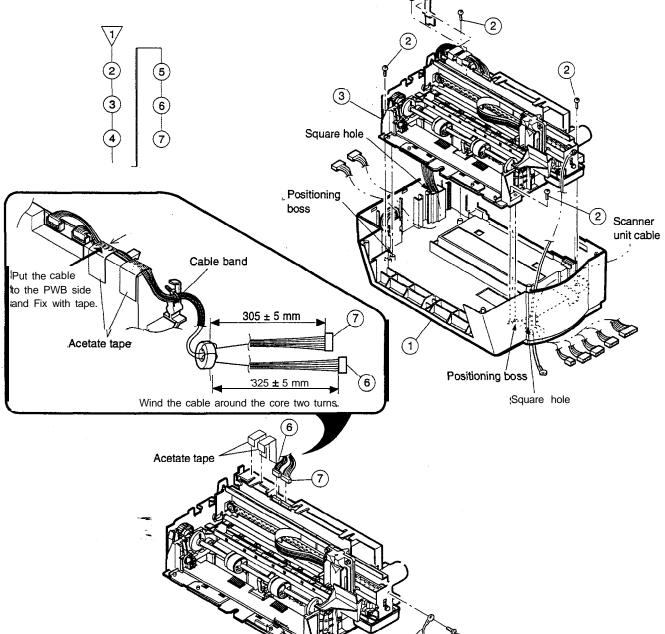
- L. Remove the scanner section from the lower cabinet according to procedures 4-1, b.
-). Remove the ink jetter unit,

<Note> When removing the ink jetter unit from the lower cabinet, be careful not to hang the cables and connec-

Parts list (Pig. 7) No. Part name Q'ty Lower cabinet 1 1 4 2 Screw (3 x 10) 3 Ink jetter unit 1 4 Screw (3 × 5) 1 5 Printer grounding cable 1 6 Printer power cable 1

Printer signal cable

7



[Note for assembly]

- 1. For wiring of printer power cable (6) and printer signal cable (7) and the core attaching position, refer to the enlarged view of Fig.
- 2. Attach printer grounding cable (5) at the angle of 45 degrees. (Refer to Fig. 7.)
- 3. Note for attaching ink jetter unit 3

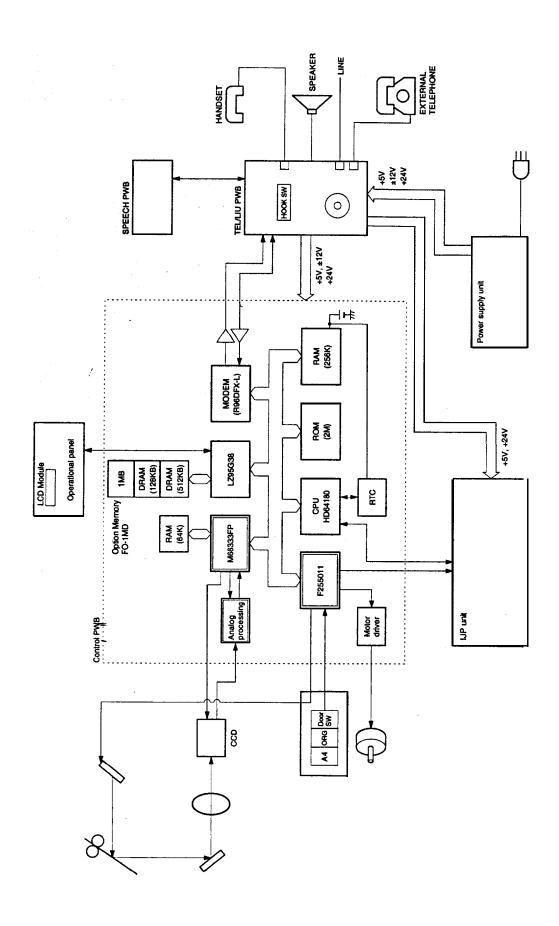
- Before attaching the ink jetter unit, pass the cables connected to the PWB section through the square hole and put them
- Be careful not to pinch the cables, and install the ink jetter unit to the positioning boss of the lower cabinet and fix it with a screw.

Fig. 7

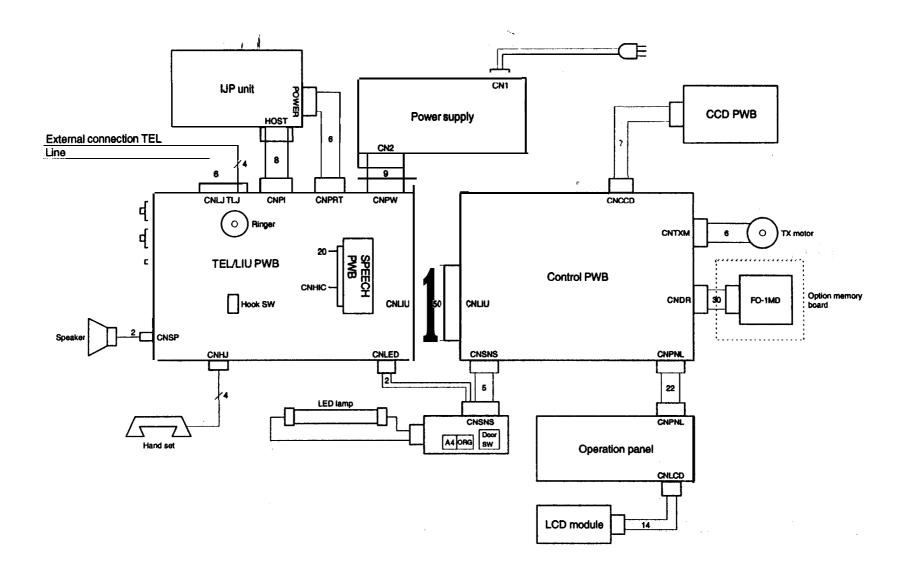


CHAPTER 4. DIAGRAMS

[1] Block diagram



[2] Wiring diagram



[4] Connector signal name [1/2]

Control PWB/Operation panel

(DDK) 128A-050P2B-L

CNLIU	TEL	/LIU F	wB •	Control PWB
-12V		1	2	+12V
AG		3	4	AG
+5V		5	6	+5V
+5V		7	8	DG
DG		9	10	DG
+24V		11	12	+24V
MG		13	14	MG
PAST		15	16	PTX
PRX		17	18	BUSY
SCK		19	20	SI
LEDON		21	22	CI
RNGDE	Ţ	23	24	CML
OHRL		25	26	MPX/QRLY
MPY/MONI	TOR	27	28	MPZ/ERLY
DP		29	30	SIGTX
SIGRX		31	32	SPMUTE
RHS		33	34	HS1
HS2		35	36	TELMUTE/BZCONT
P/T•SD	Г	37	38	BZ
		39	40	
SPARE IN	PUT	41	42	MT OUT
EXHS1		43	44	EXHS2
DT4		45	46	DT3
DT2		47	48	DT1
S-RLY		49	50	

DF11-22DP-2DSA

CNPNL	Cor	Control PWB ◆◆ Panel PWB				
KEN3		1	2	KEN4		
KEN2		3	4	SEN4		
KEN1		5	6	SEN1		
+5V		7	8	SEN2		
GND		9	10	SEN3		
GND		11	12	AUTO		
SEN0		13	14	MAN		
RS		15	16	AM		
RW		17	18	LD7		
E		19	20	LD6		
LD4		21	22	LD5		

(MOLEX) 5532-30A

CNDR	Cont	Control PWB ←► Option memory PWB				
EDRAS)	1	2	EDRA8		
EDRA7	,	3	4	EDRA6		
EDRAS	3	5	6	EDRA4		
EDRA3	3	7	8	EDRA2		
EDRA		9	10	EDRA0		
DRD7		11	12	DRD6		
DRD5		13	14	DRD4		
DRD3		15	16	DRD2		
DRD1		17	18	DRD0		
DRAS1	i	19	20	DRAS2		
DCAS		21	22	DWE		
+5V		23	24	DG		
+5V		25	26	DG		
DG		27	28	DG		
DTYP		29	30	DOPT		

CNDR is mounted only in the FO-3700

CN	LCD Operation panel → LCD module
111	GND
2	+5V
3	vo
4	RS
5	R/W
6	E
7	N.C
8	N.C
9	N.C
10	N.C
11	LD4
12	LD5
13	LD6
14	LD7

(JST) B7B-PH-K-S

טאיווייטיט (וכנ)				
CNCCD		Control PWB → CCD PWB		
1		VO		
2		VCCD		
3	VG			
4	ФТО			
5	Ф2D			
6	Φ1D			
7		ΦRD		

(JST) R6R-PH-K-S

CNTXM		Control PWB ◆ ► TX motor		
1	TPAD			
2	TPBD			
3	TPAD			
4	TPBD			
5	VMT			
6		VMT		

(IST) BEB. DH.K.S

(JSI) B2I	B-PH-K-S	
CNSNS		Control PWB ◆◆ Sensor PWB	
1		+5V	
2	ORGSW		
3	FRTSW		
4	DASW		
5		DG	

(DDK) 128A-050P2B-L								
CNLIU	CNLIU TEL/LIU PWB ←→ Control PWB							
-12V		1	2	+12V	1			
AG		3	4	AG	1			
+57		5	6	+5V]			
+5V		7	8	DG	1			
DG		9	10	DG]			
+24V		11	12	+24V	1			
MG		13	14	MG	1			
PRST		15	16	PTX	1			
PAX		17	18	BUSY]			
SCK		19	20	SI	1			
LEDON		21	22	Ci				
RNGDE	RNGDET		24	CML				
OHRL		25	26	MPX/QRLY]			
MPY/MONI	TOR	27	28	MPZ/ERLY]			
DP		29	30	SIGTX]			
SIGRX		31	32	SPMUTE]			
RHS		33	34	HS1]			
HS2		35	36	TELMUTE/BZCONT	1			
P/T+SD1	ا ً ا	37	38	BZ				
		39	40		1			
SPARE IN	TUS	41	42	MT OUT]			
EXHS1		43	44	EXHS2				
DT4		45	46	DT3				
DT2		47	48	DT1]			
S-RLY		49	50]			

(JST) B8B-PH-K-S

С	NPI	TEL/LIU PWB
1	PRST	
2	PTX	
3	PRX	
4	BUSY	
5		DG
6	SCK	
7	DG	
8	SI	

	T	w	TEL/LIU PWE
1	ı		
	2		A-WIRE
[3	3		8-WIRE
Γ			

(JST) SP-EJ

(001) 01 10		
CNPW		TEL/LIU PWB → Power supply unit
-	MG	
2	MG	
3	+24V	
4	+24V	
5	DG	
6	+5V	
7	AG	
8	+12V	
9	-12V	

(J2	1) SP-	FJ	
CNPW		TEL/LIU PWB ◆◆ Power supply unit	
1	MG		
2	MG		
3	+24V		
4	+24V		
5	DG		
6	+5V		
7	AG		
8	+12V		
9	-12V		

(JST) B6B-PH-K-R

CNPR	TEL/LIU PWB	
1	+24V	
2	+24V	
3	MG	
4	MG	
5	DG	
6	+5V	

CNLJ		TEL/LIU PWB ◆▶ NP
1		_
2	B-WIRE	
3	L1	
4	L2	
5		A-WIRE
a		

SPEECH PWB

MO+ MO-L1 L2

RX+ RX-MUTE TX-N.C.

N.C. VDD мо TO XMUTE DP

CNHIC

(JST) B2B-PH-K-R

CNSP		TELA.NU PWB ◆◆ Speaker
1	SP-	
2	SP+	

(JST) B2B-PH-KS

CN	LED	TEL/LIU PWB →→ LED lump
1	LEDON	
2	+24V	



CHAPTER 5. CIRCUIT DESCRIPTION

[1] Circuit description

1. General description

The compact design of the control PWB is **obtained** by using two gate arrays and high density printing of surface mounting parts. Each PWB is independent according to its function as shown in Fig. 1.

2. PWB configuration

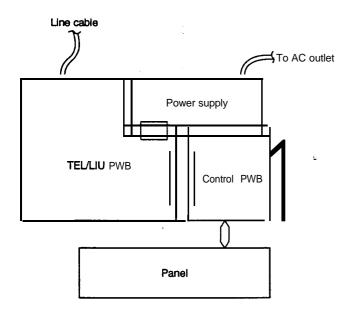


Fig. 1 PWB configuration

1) Control PWB

The control PWB controls peripheral PWB's, mechanical parts, transmission, and performs overall control of the unit.

This machine employs a I-chip modem (R96DFXL) which is installed on the control PWB.

2) TEL/LIU

This PWB controls connection of the telephone line to the unit.

3) Power supply

This provides voltages of +5V, $\pm 12V$, and +24V to the control PWB and the Inkjet Printer.

4) Panel

me panel allows input of the operation panel and LCD display.

5) Option memory board (FO-1 MD)

This unit is the use of an extend memory.

If this unit is **setted**, the memory function is increased.

3. Operational description

Operational descriptions are given below:

Transmission

When a document in loaded in the standby mode, the state of the document sensor is sensed via the gate array A. If the sensor signal was on, the motor is started to bring the document into the standby position. With depression of the START/copy key in the off-hook state, transmission takes place.

Upon depression of the START/copy key, the CML relay is set active which switches the line from the telephone to the modem. Then, the procedure is sent out from the modem and the motor is rotated to move the document down to the scan line. In the Image processor (M66333), the signal scanned by CCD is sent to the internal AD converter to convert the analog signal into binary data. This binary data is transferred from the Image processor to the image buffer and encoded and stored in the transmit buffer of the DRAM. The data is then written to the modem according to interruption by the data transmission request signal from the modem. The **modem** madulates the code data and sends signals through the **TEL/LIU** PWB.

· Receive operation

There are two ways of starting reception, manual and automatic. Depression of the START/copy key in the off-hook mode in the case of the manual receive mode, or CI signal detection by the LIU in the automatic receive mode, causes the CML relay to activate to initiate the receive operation.

First, the CPU controls the procedure signals from the modem to be ready to receive data. When the program goes into phase C, the serial data from the modem is stored in the receive buffer of the RAM. The data in the receive buffer is decoded software-wise to reproduce it as binary image data in the image buffer. The data is DMA transferred to the recording processor within the gate array A **(F255011)** which is then converted from parallel to serial form to be sent to the printer unit. The data is printed by the printer unit.

· Copy operation

To make a copy on this facsimile, the START/copy key is pressed when the machine is ready with a document on the document table and the telephone set is in the on-hook state.

First, depression of the START/copy key advances the document to the first scan line. Similar to the transmitting operation, the image signal from the CCD is converted to a binary signal in the DMA mode via the Image Processor which is then sent to the image buffer of the RAM. Next, the data is transferred to the recording processor in the DMA mode to send the image data to the printer unit which is printed. The copying takes place as the operation is repeated.

[2] Control PWB description

1. General description

Fig. 2 shows the functional blocks of the control PWB, which is composed of 6 blocks.

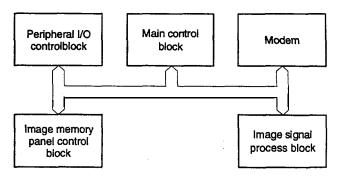


Fig. 2 Control PWB functional block diagram

2. Description of each block

(1) Main control block

The main control block is composed of an 8-bit microprocessor HD64180, ROM (256KByte), SRAM (32KByte), and DRAM (640KByte). Devices are connected to the bus to control the whole unit

1) HD64180 (IC4, main CPU) . . . pin-80 QFP

This is a CMOS 8-bit microprocessor. A high-speed CPU (compatible with **Z80** upper class models) and peripheral functions are incorporated in one chip.

This system allows the following functions.

- Memory Management Unit (MMU)
- DMA controller (2 channel); channel 0: For read data transfer channel 1: For print data transfer
- Timer
- Interruption; As external interrupt.
 - INTO: Modem interrupt.
 - INT1: Peripheral I/O control section interrupt.
 - **INT2:** RTC detection interrupt.

Operating speed is 8MHz.

In addition, 16MHz clock is internally generated with the ceramic

For reset when power is turned on, a LOW signal of about 200msec is supplied to RESET terminal.

2) 27C020 (IC114, main ROM): pin-28 DIP

EPROM of 2MKbit equipped with software for the main CPU.

3) µPD43257 (IC5): pin-28, SOP

Line memory for the main CPU system RAM area.

Memory of recorded data such as daily report and auto dials. When power is turned off, backup is made with a lithium battery.

4) HM514800 (IC11 DRAM): pin-28, SOJ and GM71 CA256A (IC10 DRAM): Pin-26, SOJ

Image memory for cording/recording process.

- Memory for recording pixel data at no paper.
- Memory for ECM

5) F255011 (IC7, gate array A)

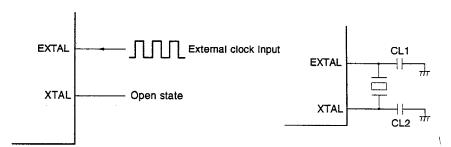
The following functions are incorporated to support the main CPU.

- Printer interface
- Read system control
- Mechanism control
- 1/O port



HD64180 (IC4) terminal descriptions

Classification	Code	Terminal No. (FP-80)		Name and pin function
Power GND	VCC	32	Input	Power supply: Connected to the power source. (+5V)
	VSS	12, 34, 72, 73	Input	Ground: Connected to the power source. (Ground)
KTAL clock	XTAL	74	Input	Connected to a crystal oscillator. Frequency must be two times as great as ϕ clock frequency. When inputting an external clock to EXTAL pin, open XTAL pin.
	EXTAL	76	Input	Connected to a crystal oscillator. Also used as an external clock input pin. The external clock input frequency must be two time as great as ϕ clock frequency.



Recommended circ	cuit configuration with	a crystal oscillator
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	ф	71	Output	System clock: Provides system clock to the peripheral devices.	
Reset	RESET	80	Input	Reset: LOW when the LSI is reset state.	
Address bus	A0-A18 A0-A19 (HD64180R1; FP-80, CP-68) (A18 is commonly used with TOUT.)	8~11 13 15~21 24~29 31, 33	Output (Three-state)	Address bus: The address to make access to the memory space. HIGH only in the following cases: (a) Reset (b) Bus control is transmitted to another device. (When BUSACK = "0" by BUSREQ = "0") A18 is multiplexed with TOUT. The timer control register TOC0 and TOC1 bits determine which output to take.	
Data bus	D0~D7	35~41, 44	Input/Output	Data Bus: 8-bit bidirectional data bus	
Memory I/O nterface signal	RD	70	Output (Three-state)	Read: Shows that the LSI is in read cycle. At that time, the data bus is in output mode.	
	WR	69	Output (Three-state)	Write: Shows that the LSI is in write cycle. At that time, the data bus is in output mode.	
	ME	66	Output (Three-state)	Memory Enable: Shows that read/write operation of the memory is being executed. LOW in the following cases: (a) Command fetch, operant read (b) Memory access in DMA cycle (c) Refresh cycle	
	ĪŌĒ	65	Output (Three-state)	I/O Enable: Shows that I/O read/write operation is being executed. LOW in the following cases: (a) Read/write of data in executing an I/O command (b) I/O access in DMA cycle (c) INT ₀ acknowledge cycle	
	WAIT	77	Input	Wait: Used to extend read/write cycle of I/O or the memory. When this input is LOW at the falling edge of T2, TW state is inserted next to T2. When it is LOW at the falling edge of TW, another TW is inserted again next to the preceding one.	
	E	67	output	Enable: Synchronizing clock for peripheral LSI's of the 6800 system.	
System control signal	BUSREQ	79	Input	Bus Request: Used for other devices to request bus free to this LSI. When driven LOW, the CPU stops execution of commands and drives some parts (RD, WR, ME, IOE) of the address bus, data bus, and memory interface signals HIGH.	
	BUSACK	78	output	Bus acknowledge: Shows that the CPU received BUSREQ signal and freed the bus. When a device which outputted BUSREQ signal receives BUSACK signal, it acknowledges that it has gained bus control	

HD64180 (IC4)

Clasaification	Code	Terminal No.		Name and pin function			n function	
System control signal	HALT	61	output	HALT: LOW when the CPU executes HALT or SLP command, and shows to the outside that the CPU is in HALT mode, SLEEP mode, or SYSTEM STOP mode. Used with ST signal and LIR signal to show the operation status such as the internal DMA operation and the CPU operation mode.			CPU is in HALT mode, mode. Used with ST signal ion status such as the	
	LIR	68	output	Load Instruction Register: Shows that the cycle which operation is the operation code fetch cycle.				
	ST	7	output		nows the ope			
				ST	HALT	LIR	Operation status	
				0	1	0	CPU operation (First operation code fetch cycle)	
				1	1	0	CPU operation (Second, third operation code fetch cycles)	
				1	1	1	CPU operation (Machine cycle other than operation code fetch cycle)	
			٤	1	Not fixed.	0	DMA operation	
				0	0	0	HALT mode	
					0	1	SLEEP mode SYSTEM STOP mode	
System control signal	REF .	64	output	Refresh: When LOW, shows that the CPU is in DRAM refresh cycle. When LOW, refresh addresses are outputted to the lower 8 bits of the address bus (A ₀ -A ₇). Refresh interval is programmable in 10, 20, 40 or 80 state.			ses are outputted to the or A ₇). Refresh interval is	
Interrupt signal	ЙМI	1	Input	Non-Ma&a request te	•	rupt: This is the non-maskable intterrupt		
	ĪNT ₀	4	Input	Interrupt 0: Maskable interrupt level 0 request terminal. In leve 0, there are three operation mode:				
				Operation	on mode		Content	
					()	Command ed.	on the data bus is execu-	
						Command i	is executed from address	
					2	Vector syst	em	
	ĪNT ₁	5	lanut	Interrupt 1	L 2: Mackabi	• interrupt	level 1 and 2 request	
	INT ₂ - 	6	Input Input		Vector syste		lever i and 2 request	
DMA signal	DREQ ₀ (Commonly used with CKAO.) DMA Request for Channel 0: Internal DM channel 0) request terminal. With this sign DMAC can operate in synchronization with devices. The internal DMAC channel 0 su transfer types:		this signal, the internal ation with the external I/O anel 0 supports the following by map I/O CKAO terminal. When DMA 'Between memory and I/O EQo terminal serves as an					
	TEND₀	55	output	Transfer End for Channel 0: Internal DMAC channel 0 transent signal. Driven LOW in synchronization with the last ditransfer write cycle. This terminal is multiplexed with CKA1 terminal. When ASCI control register A channel 1 is set to it serves as TENDo terminal.			nronization with the last data is multiplexed with CKA1	
	DREQ ₁	59	Input	DMA Request for Channel 1: Internal DMAC transfer (to channel 1) request terminal. Channel 1 supports only transfer between memory and I/O.				





HD64180 (IC4)

Classification	Code	Terminal No.		Name and pin function
DMA signal	TEND ₁	60	output	Transfer End for channel 1: Internal DMAC trasnfer (to channel 1) end signal.Driven LOW in synchronization with the last data transfer write cycle.
Serial i/O signal (ASCI channel 0)	TXA ₀	48	output	Transfer Data for Asynchronous SCI Channel 0: ASCI channel 0 transfer data terminal.
	RXA ₀	49	Input	Receive Data for Asynchronous SCI Channel 0: ASCI channel 0 receive data terminal.
	CKA0 (Commonly used with DREQO)	50	Input/Output	Clock for Asynchronous SCI Channel 0: ASCI channel 0 clock input/output terminal. This terminal is multiplexed with transfer request signal DREQO for internal DMAC channel 0. When DMA channel 0 is oeprated in the transfer mode of 'Between memory and I/O*, it cannot be used as a clock output terminal.
	RTS₀	45	Output	Request to Send for Asynchronous SCI Channel 0: One of the ASCI channel 0 modem control signals. The output can be controlled to LOW and HIGH by the program.
	CTS₀	46	Input	Clear To Send for Asynchronous SCI Channel 0: One of the ASCI channel 0 modem control signals. With this input, transmission can be controlled.
Serial I/O signal (ASCI channel 0)	DCD ₀	47	Input ,	Data Carder Detect for Asynchronous SCI Channel 0: One of the ASCI channel 0 modem control signals. With this input, the operation of the receiver section can be reset.
Serial I/O signal (ASCI channel 1)	TXA1	52	Output	Transfer Data for Asynchronous SCI Channel 1: ASCI channel 1 transfer data terminal.
	RXA1	54	Input	Receive Data for Asynchronous SCI Channel 1: ASCI channel 1 receive data terminal.
	CKA1 (Commonly used with TEND _{0.)}	55	Input/Output	Clock for Asynchronous SCI channel 1: ASCI channel 1 clock input/output terminal. This terminal is multiplexed with internal DMAC channel 0 transfer end signal TEND ₀ . When CKA1D bit of the ASCI control register A channel 1 is set to "0", it can be used as a clock input/output terminal.
	CTS ₁ (Commonly used with RXS.)	57	Input	Clear to SEnd for Asynchronous SCI Channel 1: ASCI channel 1 modem control signal. With this input, transmission can be controlled. This terminal is multiplexed with RXS signal described below. ASCI status register channel 1 CTS1E bit is used to select this terminal.
Serial I/O signal	TXS	56	output	Transfer Data for Serial I/O Port: CSI/O serial output terminal.
(CSI/O)	RXS (Commonly used with CTS1.)	57	Input	Receive Data for Serial I/O Port: CSI/O serial input terminal. This terminal is multiplexed with CTS1, and selection is made by the program.
	CKS	58	Input/Output	Clock for Serial I/O Port: Used as CSI/O clock input/output terminal.
Timer	TOUT (Commonly used with AT8.)	31	output	Timer Out: Timer output terminal of timer 1. Multiplexed with A18. Selection is made with TOCO and TOC1 bits of the timer control register.

Common terminal descriptions HD64180 (IC4)

Code	Terminal	No.	Selection method
AI 8/TOUT	31		Al8 is selected immediately after resetting. When either one or both of TOC1 bit and TOCO bit is/are set to '1 ", TOUT is selected. When the both bits are set to "0", Al 8 is selected again.
CKA0/DREQ₀	50		CKAO is selected immediately after resetting. Either one of DM1 bit or SM1 bit of DMAC DMA mode register is '1', CKAO is compulsorily changed to an input terminal though it is set as an output terminal, and CKAO can be used as DREQo terminal.
CKA1/TEND₀	55		CKA1 terminal is selected immediately after resetting. When CKA1 D bit of the ASCI control register A channel 1 is Set to "1", it can be used as TEND0 terminal. When the bit is reset to "0", the terminal returns to CKA1.
RXS/CTS ₁	57		RXS terminal is selected immediately after resettina. When CTS1E bit of ASCI status register channel 1 is set to "1", it can be used as CTS1 terminal. In this case, however, the function of RXS input terminal is not prohibited.



F255011 PJ pin descriptions

Pin	Name	1/0	Description
1	PI697	 	Input port (I/O address 69H)
2	IOSCF	1/0	Input mode: Imput port (I/O address 6BH) Output mode: I/O address C0H ~ FFH selection
3	IOSAB	0	I/O address A0H ~ BFH selection
4	IOS89	0	I/O address 80H ~ 9FH selection
5	ĪŌS7	1/0	Input mode: Input port (I/O address 6BH) Output mode: I/O address 70H ~ 7FH selection
6 7 8 9 10 11 12 13	PO660 PO661 PO662 PO663 PO664 PO665 PO666	0	Output port (I/O address 66H)
14	PO670	0	Output port (I/O address 67H)
15	GND	_	GND
16	V C C	_	Power (+5V)
17 18 19 20 21 22 23	P0671 P0672 P0673 P0674 P0675 P0676	0	Output port (I/O address 67H)
24 25 26 27	GAIN0 GAIN1 GAIN2 GAIN3	0	Read image signal gain control signal
28	AGC		GAIN 3 ~ 0 control signal
29	PTIM	- 1	Transmission motor start timing signal
30	RRDY	0	Data send start ready signal to the read process LSI
31	A-11.7		Data send area signal from the read
	STIM	I	process LSI
32	STIM	1	Data send clock from the read process LSI
			Data send clock from the read process
32	SCLK SVID PHIT	1	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal
32	SCLK SVID PHIT INT		Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal
32 33 34	SCLK SVID PHIT INT RESET	 - -	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal
32 33 34 35	SCLK SVID PHIT INT	 - 	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal
32 33 34 35 36	SCLK SVID PHIT INT RESET	 - - - -	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal
32 33 34 35 36 37	SCLK SVID PHIT INT RESET PHAI	 	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz)
32 33 34 35 36 37 38	SCLK SVID PHIT INT RESET PHAI IORD	 - 0	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read
32 33 34 35 36 37 38 39	SCLK SVID PHIT INT RESET PHAI IORD IOWR	 - 0	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write
32 33 34 35 36 37 38 39 40 41 42	SCLK SVID PHIT INT RESET PHAI IORD IOWR	 - 0	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V)
32 33 34 35 36 37 38 39 40 41 42 43	SCLK SVID PHIT INT RESET PHAI IORD IOWR vcc GND DO D1	 - 0	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V)
32 33 34 35 36 37 38 39 40 41 42 43 44	SCLK SVID PHIT INT RESET PHAI IORD IOWR vcc GND D0 D1 D2	 - 0	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V)
32 33 34 35 36 37 38 39 40 41 42 43 44 45	SCLK SVID PHIT INT RESET PHAI IORD IOWR vcc GND D0 D1 D2 D3	 - 0	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V)
32 33 34 35 36 37 38 39 40 41 42 43 44	SCLK SVID PHIT INT RESET PHAI IORD IOWR vcc GND D0 D1 D2	- - - 0 1 - - -	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V) GND
32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48	SCLK SVID PHIT INT RESET PHAI IORD IOWR VCC GND D0 D1 D2 D3 D4 D5 D6	- - - 0 1 - - -	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V) GND
32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47	SCLK SVID PHIT INT RESET PHAI IORD IOWR vcc GND D0 D1 D2 D3 D4 D5 D6 D7	- - - 0 1 - - -	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V) GND
32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48	SCLK SVID PHIT INT RESET PHAI IORD IOWR V C C GND D0 D1 D2 D3 D4 D5 D6	- - - 0 1 - - -	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V) GND
32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49	SCLK SVID PHIT INT RESET PHAI IORD IOWR vcc GND D0 D1 D2 D3 D4 D5 D6 D7	- - - - - - - - - - - - - - - - - - -	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V) GND CPU data bus
32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50	SCLK SVID PHIT INT RESET PHAI IORD IOWR VCC GND D0 D1 D2 D3 D4 D5 D6 D7 DREQ1	- - - - - - - - - - - - - - - - - - -	Data send clock from the read process LSI Serial image data from the read process LSI CCD shift pulse signal Interruption request signal Reset signal Clock input (8MHz) I/O read I/O write Power (+5V) GND CPU data bus DMA ch.1 reauest signal to CPU

Pin	Name	1/0	Description	
54	A0			
55	A1			
56	A2	[
57	A3	ı	CPU address bus	
58	A4	\	Or o address bus	
59	A5			
60	A6			
61	A7			
62	TPA	0	Transmission motor phase excitement	
63	TPB		control signal	
64	TEST		Test pin	
65	GND		GND	
66	VCC		Power (+5V)	
67 68	TPA TPB	0_	Transmission motor phase excitement control signal	
69	PI6A0			
70	PI6A1			
71	PI6A2			
72	PI6A3	1	Input port (I/O address 6AH)	
73	Pl6A4		input port (# o dddiose e/ ii i)	
74	PI6A5			
75 76	PI6A6			
	PI6A7			
77	RSTP	0	Printer reset signal	
78	BUSY	- 1	Busy signal from the printer	
79	PCLK	0	Record data send clock to the printer	
80	PDATA	0	Serial record data to the printer	
81	Cl1	1	Cl signal	
82	Cl2	1	RINGDET signal	
83	DP	0	Dial pulse generating signal	
84	PO657			
85	PO656		·	
86	PO655	0	Output port (I/O address 65H)	
87	PO654		' ' '	
88 89	PO653 PO652			
90	VCC		Power (+5V)	
91	GND		GND	
92	PO651			
93	PO650	0	Output port (I/O address 65H)	
94	PO690			
95	PO691			
96	PO692			
97	PO693	0	Output port (I/O address 69H)	
98	PO694		,	
99	PO695			
100	PO696			



8) LZ95G38 (IC12 gate array B) . . . 100 pin QFP

The following functions are provided as the main CPU peripheral functions.

- Memory mapper
- WAIT control
- Main CPU timers (3 units)
- DRAM controller
- Panel I/F (LCD controller I/F, key scan)
- Modem I/F (RTC detection)
- Alarm buzzer/busy tone clock frequency division

LZ95G38 pin description

Pin No.	Signal name	1/0	Description	
1	DCLK	T	MODEM DCLK	
2	RXD	T	MODEM serial reception data	
3	ALARM	0	Alarm buzzer clock	
4	BSTONE	0	Busy tone signal	
5	GND			
6	DRWE	0	DRAM write enable signal	
7	CAS	0	DRAM CAS signal	
8	RAS3	0	DRAM RAS signal (3)	
9	RAS2	0	DRAM RAS signal (2)	
10	RAS1	0	DRAM RAS signal (1)	
11	(NU)		Not used.	
12	MA9			
13	MA8			
14	MA7			
15	MA6			
16	MA5	0	DRAM address	
17	MA4			
18	МАЗ			
19	MA2			
20	MA1			
21	MA0			
22	MD7			
23	MD6			
24	MD5			
25	MD4	1/0	DRAM data	
26	MD3			
27	MD2			
28	MD1			
29	MD0			
30	GND]		
31	D7			
32	D6			
33	D5			
34	D4	I/O	Main CPU data bus	
35	D3			
36	D2			
37	D1			
38	D0			
39	Vcc			
40	GND			
41	TEST	!	Test pin	

Pin No.	Signal name	1/0	Description	
42	A19			
43	A18			
44	A17			
45	A16			
46	A15			
47	A14			
48	A13		·	
49	A12			
50	A11			
51	A10	ı	Main CPU address	
52	A9			
53	A8			
54	A7			
55	A6			
56	A5			
57	A4			
58	A3			
59	A2			
60	A2 A1		ı	
61	\ A0		!	
	RD	ı	Main CRU road signal	
62 63	WR	1	Main CPU read signal Main CPU write signal	
64	ĪŌĒ			
			Main CPU I/O enable signal	
65	ME	1	Main CPU memory enable signal	
66	WAITO	0	Main CPU wait signal	
67	REF	1	Main CPU refresh cycle signal	
68	LIR		Main CPU LIR signal	
69	PHI	1	Main CPU system clock	
70	RESET		Reset signal	
71	INT20	0	Interruption request signal	
72	MWR	0	Memory write signal	
73	MS3	0	Memory select (3)	
74	MS2	0	Memory select (2)	
75	MS1	0	Memory select (1)	
76	MS0	0	Memory select (0)	
77	GND			
78	RS	0	LCD controller I/F (Register select signal)	
79	RKS	0	LCD controller I/F (Read/write signal)	
80	E	0	LCD controller I/F (enable signal)	
81	LD7			
82	LD6		_	
83	LD5		·	
84	LD4	1/0	LCD controller I/F (data bus)	
85	LD3		` '	
86	LD2		'	
87	LD1			
88	LD0			
89	Vcc	_		
90	GND	_		
91	KN3			
92	KN2	0	KEY scan signal (decode data)	
93	KN1		ne i scan signal (decode data)	
94	KN0			
95	KN11	0	KEY scan signal	
96	KN10	ī	KEY sense signal	
97	SEN3			
98	SEN2			
99	SEN1		KEY sense signal	
100	SEN0			
			<u> </u>	



(2) Panel control block

The following controls are performed through LZ95G38 according to commands from the main CPU.

- · Operation panel key scanning
- Operation panel LCD display

(3) Peripheral I/O control block

· Recording control block diagram

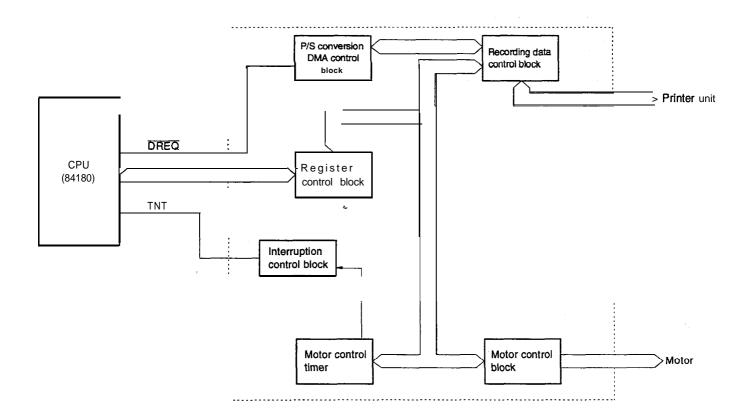


Fig. 3

The recording control block is composed as shown above. The descriptions are given below:

- P/S conversion block, DMA control block, recording data control block
 - The recording data is **transferred** to the printer unit by these blocks. First, the gate array A sends **DREQ** to the CPU. The CPU transfers the recording data to the P/S conversion block by means of DMA. The transferred data is converted into serial data and sent through the recording data control block to the printer unit together with a clock.
- Motor control block
 This block supplies phase output for control of the TX motors. With register setting, it controls phase switching timing of the motor.



(4) Image signal processing block

The image signal processing block is composed of the following:

- ① CCD sensor drive block (M66333FP (IC2)).
- 2 Analog processing block
 - OP amp. (TL084 (IC102)),
 - Analog switch (4053BP (IC107), 4066 (IC103 and IC104))
 - M66333FP (IC2), transistor, etc.
- 3 A/D converter block (M66333FP (IC2) inside).
- Binary coding processing block
 - M66333FP (IC2)
 - LH5266 (IC3)

Descriptions on each block are given below:

1) CCD drive block

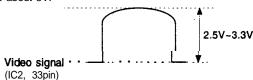
The Clock necessary for CCD drive is generated in M66333.

- **\$1** (Synchronization clock)
- φ2 (= φ1) (Synchronization clock)
- **\phiR** (Output buffer reset clock)
- φT (Transmission clock)

2) Analog processing block

Video signals supplied from the CCD PWB are sample-hold, gain control and clamped to supply **M66333FP**.

The gain control is performed by F255011 (IC7), 7406 (IC105), 4066 (IC104), and TL084 (IC102) to control the max. voltage of video signal to about 3V.



3) A/D Converter

A **7-bit,** high-speed A/D converter M66333 in (IC2 80 pin QFP) is used to supply A/D converted digital **video** signals to the binary coding processing block.

4) Binary coding processing block

Digital video signals incorporates various algorithms required for binary coding, and RAM (IC3) which stores data necessary for processing are converted into binary data, and P/S converted, and DMA-transmitted to the line memory of the main CPU.

The algorithms for binary coding in the processing block are as follows:

- · Shading correction
- Focus correction
- Auto contrast process
- Intermediate Half-tone expression process (error dispersion process/image area separation)

(5) Modem (R96DFXL) block

INTRODUCTION

The Rockwell **R96DFXL MONOFAX** modem is a synchronous 9600 bits per second (bps) half-duplex modem with error detection and DTMF reception. It has low power consumption and requires only a single **+5VDC** power supply. The modem is housed in a single VLSI device package.

The modem can operate over the public switched telephone network (PSTN) through line terminations provided by a data access arrangement (DAA).

The **R96DFXL** is designed for use in Group 3 facsimile machines. The modem satisfies the requirements specified in **CCITT** recommendations V.29. V.27 ter. V.21 Channel 2 and T.4, and meets the binary signaling requirements of **T.30**.

The modem can operate at 9600, 7200, 4800, 2400, or 300 bps, and also includes the V.27 ter short training sequence option.

The modem can also perform HDLC framing according to **T.30** at **9600, 7200, 4800, 2400,** or300 bps.

The modem features a programmable DTMF receiver and three programmable tone detectors which operate concurrently with the V.21 channel 2 receiver.

The voice mode allows the host computer to efficiently transmit and receive audio signals and messages.

The modem is available in either a **100-pin** plastic quad flat pack (PQFP) or a **64-pin** quad in-line package (QUIP).

General purpose input/output (GPIO) pins are available for host assignment in the 1 00-pin PQFP.

The modem's small size, single voltage supply, and low power consumption allow the design of compact system enclosures for use in both office and home environments.

MONOFAX is a registered trademark of Rockwell International.

FEATURES

- Group 3 facsimile transmission/reception
 - CCITT V.29, V.27 ter, T.30, V.21 Channel 2, T.4
 - HDLC Framing at all speeds
- V.27 ter short train
- Concurrent DTMF, FSK, and tone reception
- Voice mode transmission/reception
- Halfduplex (2-Wire)
- Programmable maximum transmit level:
 0 dBm to -15 dBm
- Programmable transmit analog attentuation:
- 0 dB to 14 dB in 2 dB steps
- Receive dynamic range: 0 dBm to -43 dBm
- Programmable dual tone generation
- Programmable tone detection
- Programmable turn-on and turn-off thresholds
- Programmable interface memory interrupt
- Diagnostic capability
 - Allows telephone line quality monitoring
- Equalization
 - Automatic adaptive equalizer
 - Fixed digital compromise equalizer
- DTE interface: two alternate ports
 - Selectable microprocessor bus (6500 or 8085)
 - CCITT V.24 (EIA-232-D compatible) interface
- TTL and CMOS compatible
- Low power consumption: 275 mW (typical)
- Single Package
 - 100-pin PQFP
 - 64-pin QUIP
- Single +5VDC power supply
- Software compatible with R96MFX, R96EFX, R96DFX, and R96VFX modems



R96DFXL Hardware Interface Signals

Pin Signals - 100-Pin PQFP

Din No.	Cianal Name	VO T
Pin No.	Signal Name	I/O Type
1	GPO3	IA/OB
2	GPO4	IA/OB
3	GPO5	IA/OB
4	GPO6	IA/OB
5	GPO7	IA/OB
6	OVD2	GND
7	0VD2	GND
8	D7	IA/OB
9	D6	IA/OB
10	D5	IA/OB
11	D4	IA/OB
12	D3	IA/OB
13	D2	IA/OB
14	D1	IA/OB
15	DÒ	IA/OB
16	OVD2	GND
17	OVA	GND
18	RAMPIN	R
		_ n
19	NC	
20	NC	٤
21	OVA	GND
22	+5VD2	PWR
23	0VD1	GND
24	SWGAINI	R
25	ECLKIN1	R
26	SYNCIN1	R
27	NC NC	* 1
28	NC	
29	NC	
30	OVA	GND
31	NC	
32	NC	
33	NC	
34	DAIN	R
35	ADOUT	R
36	BYPASS	ic
37	RCVI	R
38	TXLOSS3	IC
39	TXLOSS2	IC
40	TXLOSS1	IC
41	NC	
42	NC ~	
43	OVA	GND
44	TXOUT	AA
45	RXIN	AB
46	+5VA	PWR
47	OVA	GND
48	AGD	R
49	AOUT	R
50	0VD1	GND
51	NC	
52	ĪRQ	ос
53	WRITE-R/W	IA
54	CS	IA
55	READ-ø2	IA
56	RS4	IA
57	RS3	IA IA
58	RS2	IA
59	RS1	IA

Pin No.	Signal Name	I/O Type
60	RSO	IA
61	GP13	IA/OB
62	NC	
63	GP11	IA/OB
64	RTS	IA
65	EN85	R
66	OVD2	GND
67	PORI	ID
68	XTLI	R
69	XTLO	, R
70	XCLK	OD
71	YCLK	OD
72	+5VD1	PWR
73	DCLKI	R
74	SYNCIN2	R
75	GP16	IA/OB
76	GP17	IA/OB
77	OVD2	GND
78	CTS	OA
79	TXD	IA
80	0VD2	GND
81	OVD2	GND
82	DCLK	OA
83	EYESYNC	OA
84	EYECLKX	OA
85	EYECLK	OA
86	EYEX	ÒΑ
87	ADIN	R
88	DAOUT	R
89	OVD2	GND
90	EYEY	OA
91	GP21	IA/OB
92	OVD2	GND
93	GP20	IA/OB
94	GP19	IA/OB
95	RXD	OA
96	RLSD	OA
97	OVD2	GND
98	RCVO	R
99	SWGAINO	R
100	GPO2	IA/OB

Notes:

- 1. NC = No connection; leave pin disconnected (open).
- 2. I/O Type: Digital signals: see Table 9;
- Analog signals: see Table 10.

 3. R = Required modem inter-connection; no connection to host equipment.



[3] Description of CCD board

The CCD board picks up optical information from the document, **converts** it into an electrical (analog) signal and transfers it to the control board.

(1) Block diagram

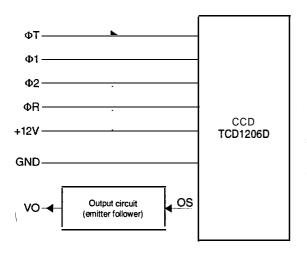


Fig. 4

[4] TEL/LIU (with Speech PWB unit) board circuit description

1.General

Telephone interface circuitry for this facsimile is all mounted on one circuit board unit and is interfaced with the telephone line and facsimile circuits via connectors.

Connection to the switched telecommunication network is canted out by means as of the magnetic relay. Power required for the control of the unit is supplied from the power supply unit of the facsimile +24V, +12V, -12V, +5V.

2. Circuit general description

This board is composed of the following blocks.

- (1) Surge protection block
- (2) Hook detection block(Polarity inversion detection block)
- (3) Reception control block --
- (4) Transmission control block
- (5) CI signal detection block
- (6) Speaker output voice select block
- (7) Speaker amplifier block
- (8) Polarity guard block
- (9) Tone ringer block
- (10) Hook control block
- (11) Dialer control block
- (12) Dial control block
- (13) 4-bit control block
- (14) External TEL hook detection block

Speech PWB unit (IC3)

- (15) Dial pulse transmission block
- (16) Communication circuit block

(2) Description of blocks

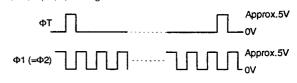
1. CCD

The TCD1206D is a highly sensitive charged coupled image sensor that consists of 2160 picture elements.

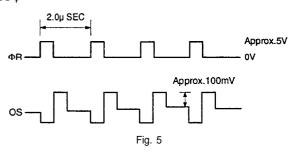
Receiving four drive signals $(\phi T, \phi 2, \phi 1, \phi R)$ from the control board, the transferred photoelectric analog signal OS is impedance converted, and the signal VO, is supplied to the control board.

2. Waveforms

1. $\phi 1$, $\phi 2$ (= $\overline{\phi 1}$)... signals within the control board.



2. OS ¢



3. Each block description

(1) Surge protection block

This is composed of arrester (AR1), Used to prevent the LIU block from damage caused by a surge voltage occurring across lines, the **3-pole** type is used.

(2)Hook detection block(Polarity inversion detection

This is composed of IC11 and its peripheral circuits and detects inversion of porarity. When the DC circuit is formed, either $\overline{\text{HS1}}$ or HS2 turns LOW.

(3) Reception control block

This is composed of IC5, IC6, IC8 and its peripheral circuits and controls reception signals.

(4) Transmission control block

This is composed of IC5 its peripheral circuits and controls transmission signals.

(5) CI signal detection block

The CI signal detection block consists of R2, D6, ZD14. PC5 and its peripheral circuits. The CI(Calling Indicate) signal is detected by the half wave rectifier circuit consisting of D6 and ZD14. The photocoupler PC5 is driven during the half cycle rectified by D6. The photocoupler delives current to IC1 when it is turned on during these half cycles. IC1 inverts the signal present at its input to create CI signal.

(6) Speaker output voice select block

This is composed of IC7 and its peripheral circuits. The speaker amplifier input signal is selected as shown in the table below.

MONITOR	BZCONT	Speaker output voice
0	1	Buzzer, key sound
0	0	Line signal
1	0	Modem send signal



(7) Speaker amplifier block

This is composed of IC9 and its peripheral circuits.

The signal is inputted to amplifier IC through volume and amplified and sent to the speaker. The speaker volume is adjustable by the volume.

(8) Polarity guard block

This is bridge rectifier **REC1** and has a function to protect the telephone set against inversion in the line.

(9) Tone ringer block

This is composed of **IC10** and its peripheral circuits. When a call signal is **inputted**, the toner ringer block makes the piezo-electric buzzer ring.

The speake sound volume is varied in by the slide switch.

(10) Hook control block

This is composed of the hook SW, the on-hook relay (OHRLY relay), and its driver

When OHRLY is high, H relay is on to close the line regardless of the hook SW state.

(11) Dialer control block

This is composed of IC101 and its peripheral circuits, and supplies current to the dialer circuit.

(12) Dial control block

This is composed of IC3 and its peripheral circuits. Signal from the selection signal according to **4-bit** signal from the CPU is outputted to the pulse transmission block DP and the telephone line block (PB) according to slide SW1 setting (Tone or Pulse)

(13) Cbit control block

This block is composed of phto couplers (PC1) is used to supply 4 bit data from the CPU through the photo coupler to the **dial** IC.

(14) External TEL hook detection block

This is composed of IC7 and its peripheral circuits.

When the telephone connected to the external TEL terminal is picked up to form the DC circuit, either **EXHS1** or **EXHS2** turns LOW.

(In Speech PWB unit)

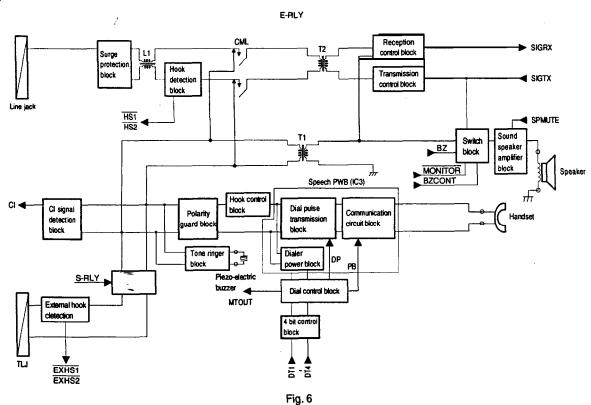
(15) Dial pulse transmission block

This is composed of **IC3** and its peripheral circuits, and has the switching function for pulse dial transmission and the switching function of on-book/off-book

(16) Communication circuit block

This is composed of IC3 and its peripheral circuits. It has functions of reception amplifier, transmission amplifier, AGC circuit, and other function necessary for communication.

Block diagram



5-12



Speech Network IC

TA31065

Pin No.	Pin Name	Pin function	
1	VL	[Line current input pin] This pin is connected to the positive output of the diode bridge circuit.	
2	TOI	Send output current flowing pin] This oin is connected through the 56 Ohm resistor to the VI oin (1).	
3	тоо	[Send output current output pin] This pin is connected through the 15 Ohm resistor to the GND pin (2). Since most of line currents are outputted from this pin, the allowable power of the 15 Ohm resistor which is connected between this pin and the GND pin must be determined by taking the expected max. line current into consideration.	
4	I NC	-	
5	AC BIAS	[AC signal reference voltage pin] When an AC signal is inputted to this pin through the capacitor (for preventing against DC), the signal is transmitted to the line.	
6	MFI	[DTMF or external input signal input pin] A signal inputted this pin is outputted to the V _L pin ① only when the MUTE pin ⑪ is LOW,	
7	TPO	[Send input amp output pin] This pin is negatively fed back to the TPI1 oin (8).	
8	TPI1	[Send input amp reverse input pin] This pin receives negative feedback from the TPO pin ⑦.	
9	TP12	[Send input amp reverse input pin] This pin is DC-biased from the REF pin (5) through the resistor.	
10	NC		
11	MUTE	[NUTE pin] Pin for selection between send signals and MFI input signals in the transmitter system. Pin for selection between send signals and BTI input signals in the receiver system.	
12	GND	[Ground pin] This pin is connected to the negative output of the diode bridge.	
13	UP	[AC impedance control pin] When this pin is connected directly to the GND pin ② or through a resistor, the DC potential of the V _L pin ① can be increased to max. 1.5V (TYP) with the same line current.	
14	PADC	[Pad control pin] When this pin is connected to the GND pin ② or the Vcc pin ② through a resistor, the operating current of gain control (auto pad) by the line current can be controlled.	
15	REF	[Internal reference output pin] This pin voltage serves as the reference voltage of the internal pre-amp.	
16	NC		
17	RPI2	[Reception input amp non-reverse input pin] This pin is directly biased from the REF pin (5) through the resistor.	
18	RPI1	[Reception input amp reverse input pin] This oin receives neoative feedback from the PRO oin (19).	
19	RPO	[Reception input amp output pin] This pin is negatively fed back to the RPI 1 pin (8).	
20	ВТІ	[Dial_confirmation sound (Beep tone, DTMF), monitor sound pinput pin] A signal inputted to this pin is outputted to the RO1 and R02 pin ② and ③ only when the MUTE pin ① is LOW.	
21	NC	-	
22	R02	[Reception output pin, reverse side] This is the output oin to the receiver.	
23	RO1	[Reception output pin, non-reverse side] This is the output pin to the receiver.	
24	vcc	[Internal power voltage pin] Power for the internal pre-amp.	



[5] Description of Power Supply

1. Block diagram

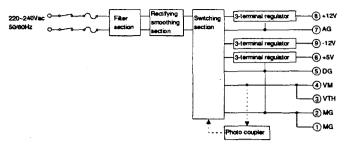


Fig. 1. Block diagram

2. General description

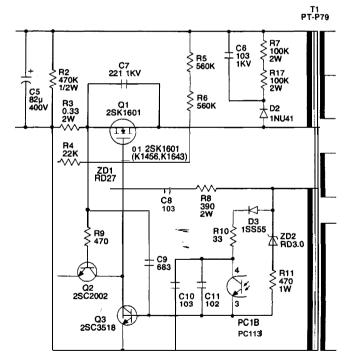
The input voltage is **187V** AC - 276V AC conforming to UL standards. The outputs are +24V(VM), +5V, +12V, -12V. The overvoltage protection **function** for protection of the load in case of power abnormality and the overcurrent protection function for protection of the power supply itself from overload are added,

3. Operational description

When 230 ~ 246 V/AC power is supplied, it is passed through a noise filter to the rectifier section where it is smoothed to about 300V then supplied to the **invertor** section. The **invertor** section employs **one**-transistor ON/OFF self-excited **invertor** (R.C.C. system) and a stable DC voltage is supplied to the secondary side.

The operation of each section is described below:

3.1. Invertor section



When the voltage across R4 reaches the gate ON voltage through R5, R6, and R4, Q1 begins to turn ON, flowing a current from pin1 of T1 primary winding to pin3. Then a voltage is generated from pin5 of auxiliary winding to pin4, turning Q1 gate ON completely. The drain current increases linearly to store energy in the primary winding. However, voltage across R3 turns ON Q2 thorough R9 when the drain current reaches a certain level. As a result, Q1 gate voltage falls below the threshold voltage (about 4V) of the gate and Q1 turns off. Simultaneously when Q1 turns off, the energy stored in the primary winding is induced in the secondary winding to bias the rectifier diodes D4, D5, D6, and D7 forwardly, smoothing each output capacitor. Thus a DC voltage is obtained.

3.2. Control section

(1) +24V (VM) control

A voltage is generated in the secondary side by repeated operations of 3.1, and the output in the secondary side is divided by R14. VR1, and R15 to be inputted to Q4. The divided voltage is adjusted to about 6.2V by VR1. Q4 always monitors the divided output voltage. When the output voltage exceeds +24V, the divided voltage also exceeds 6.2V and Q4 judges it as an increase in the output voltage. Then photocoupler PC1 is lit through R13 to turn on the transistor in the light receiving side, supplying a current to the base of Q3, turning off Q1. (The current which is to be passed through the additional line. R8. and C8 to R4 is bypassed by Q3). Resultantly, ON time of Q1 is shortened and the energy stored in the primary winding is decreased, limiting the increase in the output voltage. When the output voltage begins to decrease, the light quantity of PC1 is decreased to lengthen the ON time to Q1. As a result the energy in the primary winding is increased to compensate for the decrease in the output voltage.

The negative feedback control is repeated to stabilize the output voltage.

(2) +5V, +12V, -12V control

The outputs of **+5V**, **+12V**, and -12V are stabilized by the threeterminal regulator **ICs** (IC3, **IC1**, and IC2). The overcurrent protection function protects the regulator **ICs** themselves.

3.3. Overcurrent protection function

When the output current in the secondary side increases to become an overcurrent or short R3/R4 detects the drain current to turn on Q2, The gate voltage of Q1 is controlled to shorten ON time of Q1 to protect the circuit from the overcurrent.

3.4. Overvoltage protection function

When the output voltage is abnormally increased and ZD5 zener voltage exceeds about 30V, ZD5 is shorted to operate the same procedure as the overcurrent protection function. To reset, turn off the AC switch, remove the cause, and replace ZD5 with a new one.

3.5. High temperature protection circuit

Thermal fuse F3 included in the power circuit is blown when the surface temperature of rectifier D1 exceeds about 115 degrees Centigrade. (about 239-F)

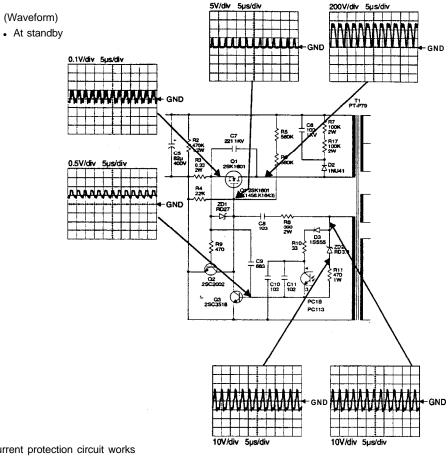


3.6 Rush current limiting circuit

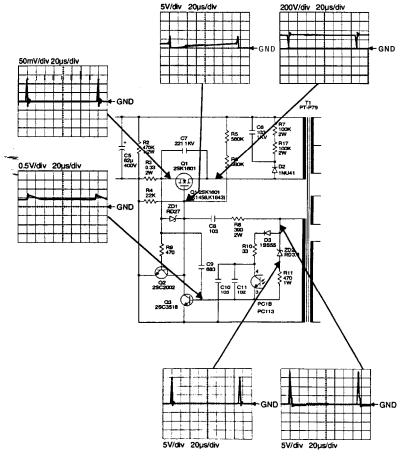
When AC power is supplied, a rush current flowing through capacitor C5 may blow fuse F1/F2 and damage the circuit. To prevent this, the power thermistor TH1 is provided to limit the rush current.

3.7. Line filter

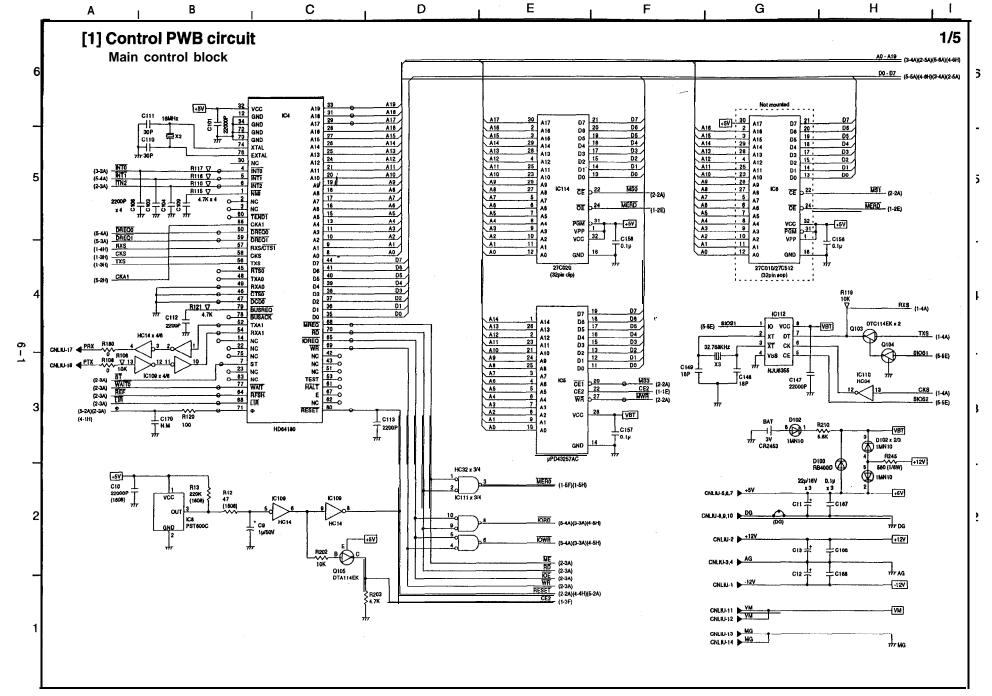
To protect against external noises and noises generated in the power circuit, the line filter is composed of L1, L2, CI, C2, C3, and C4 to reduce noises.



· When the overcurrent protection circuit works



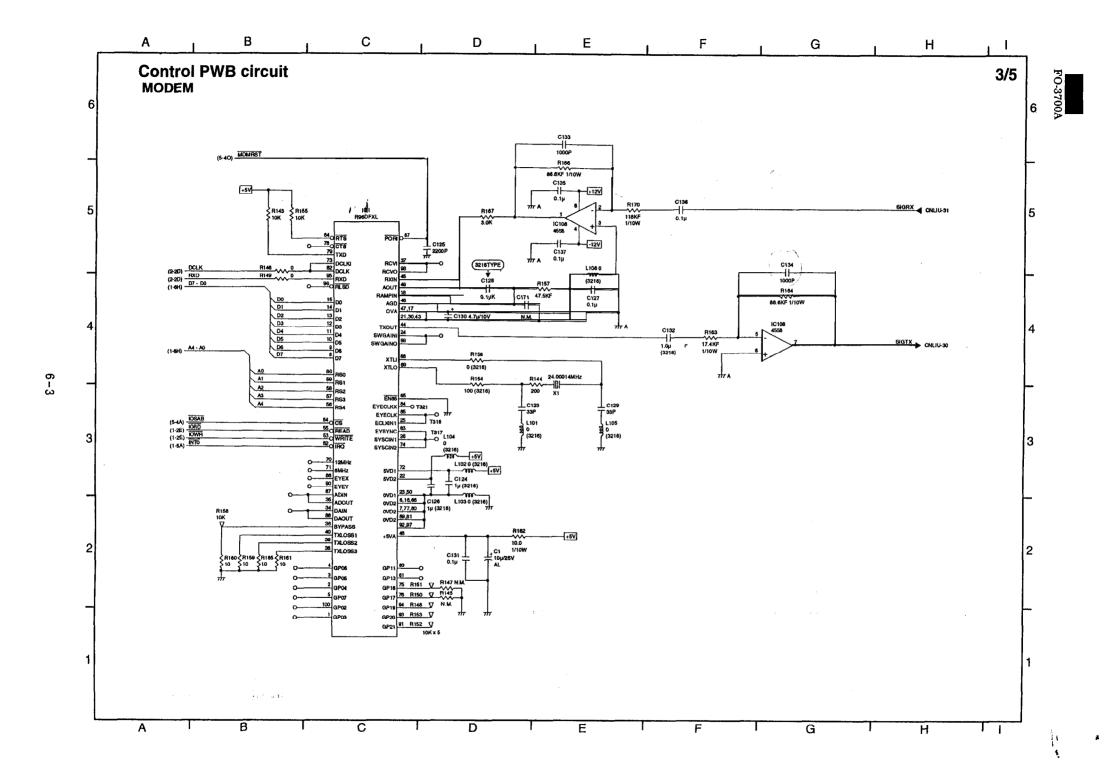
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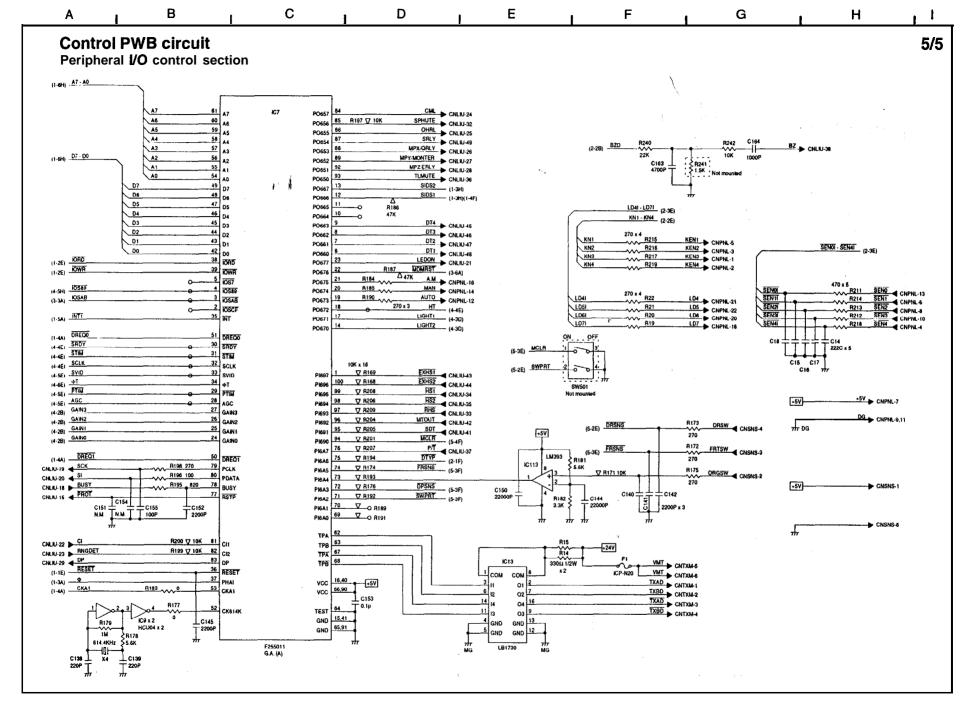


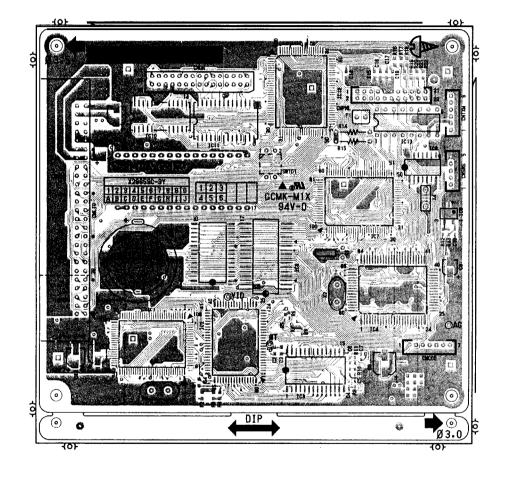
CIRCUIT SCHEMATICS **AND PARTS LAYOUT**

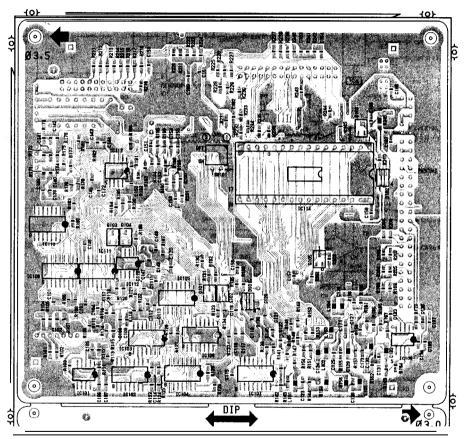
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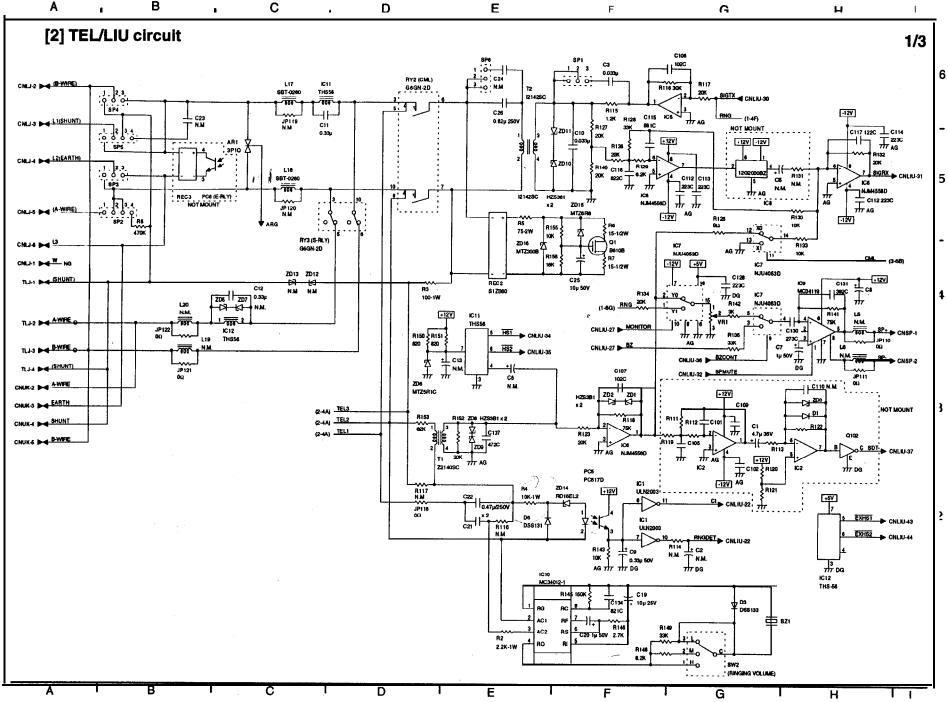
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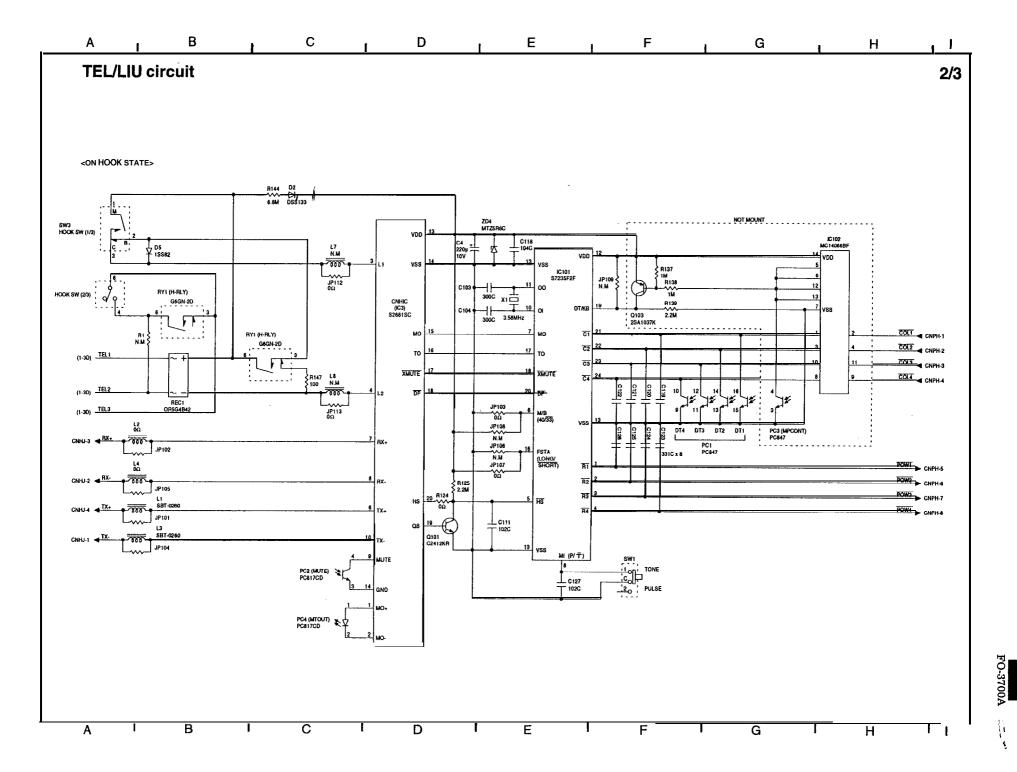


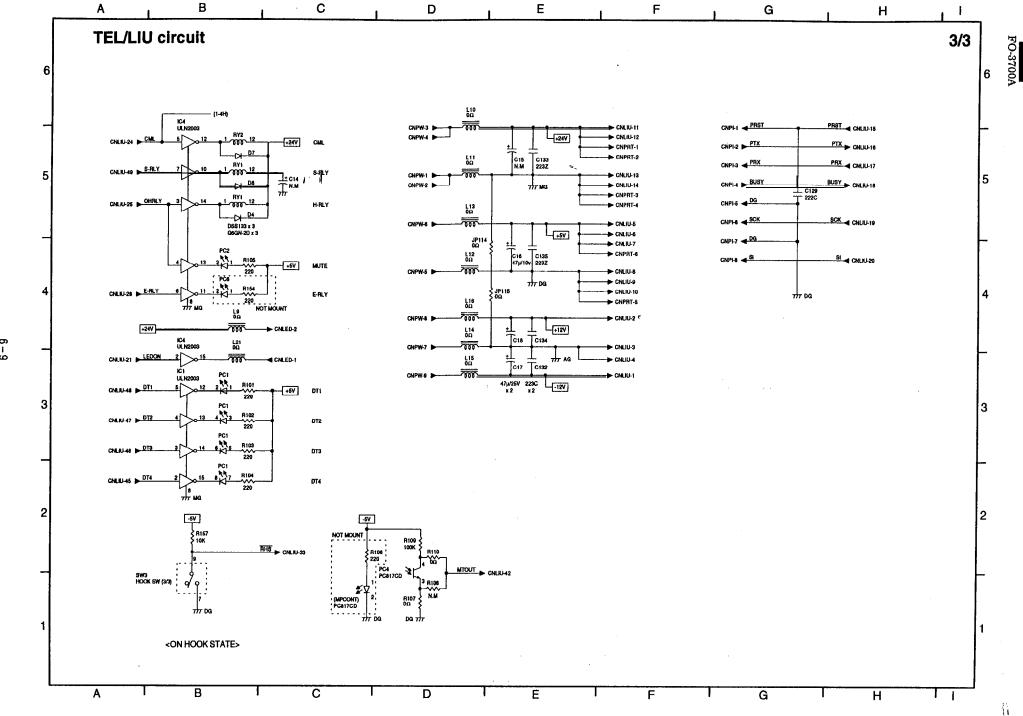




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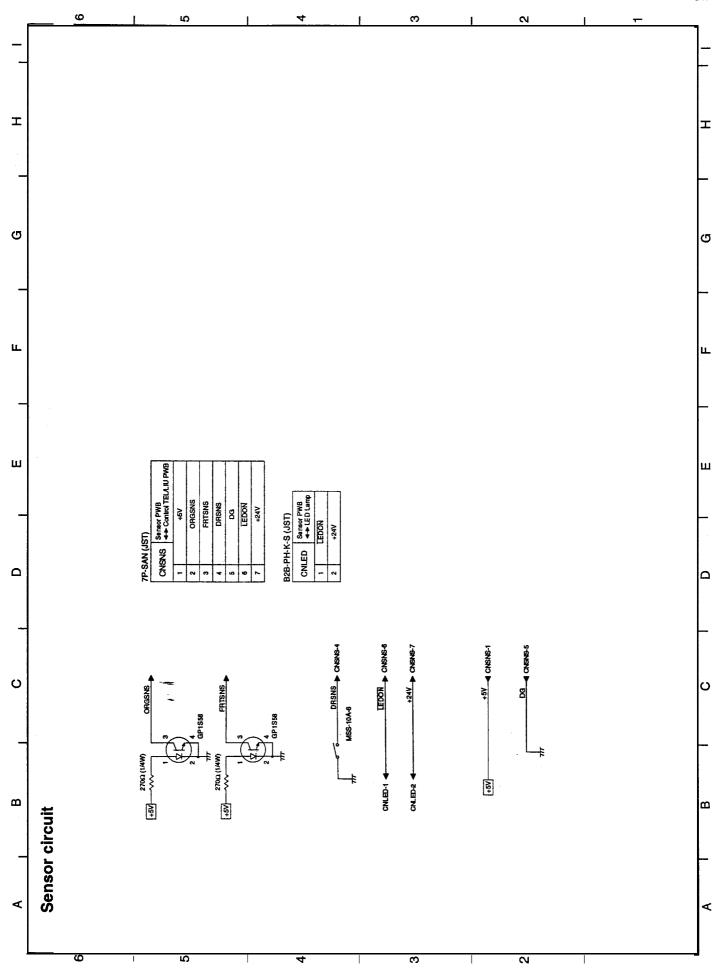
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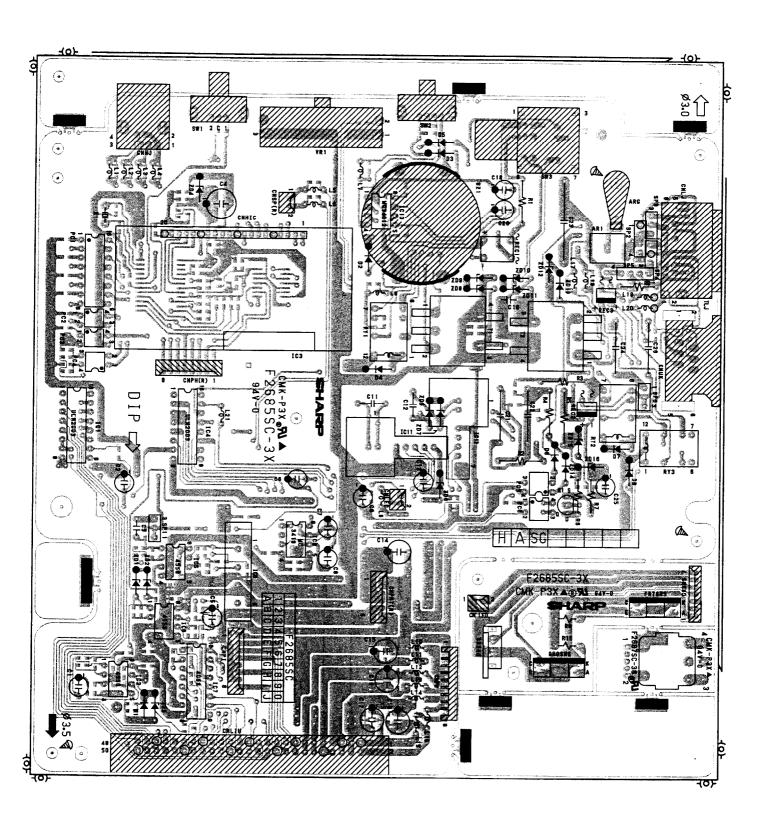


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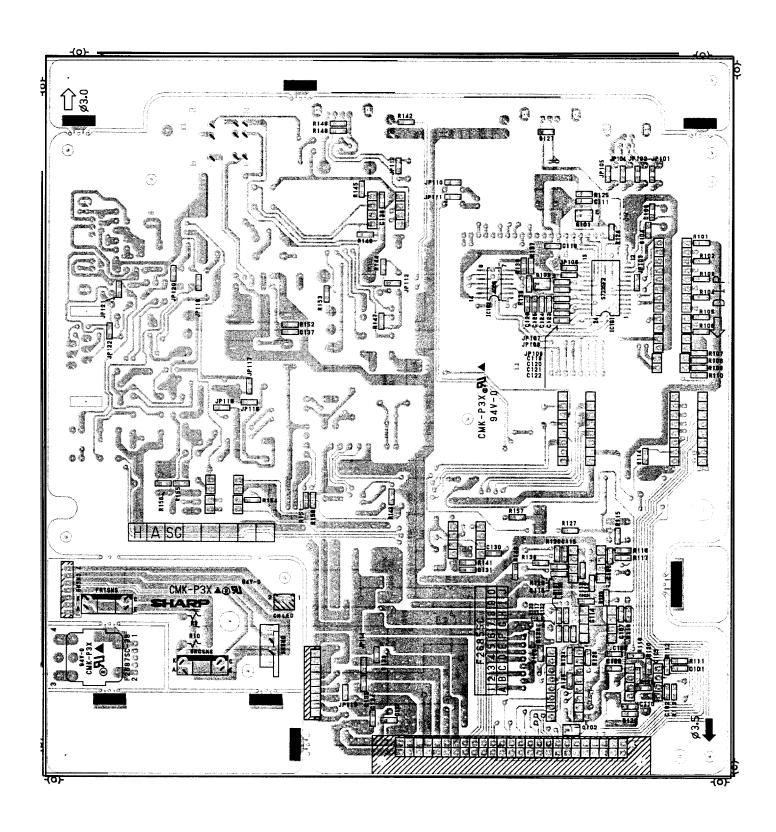


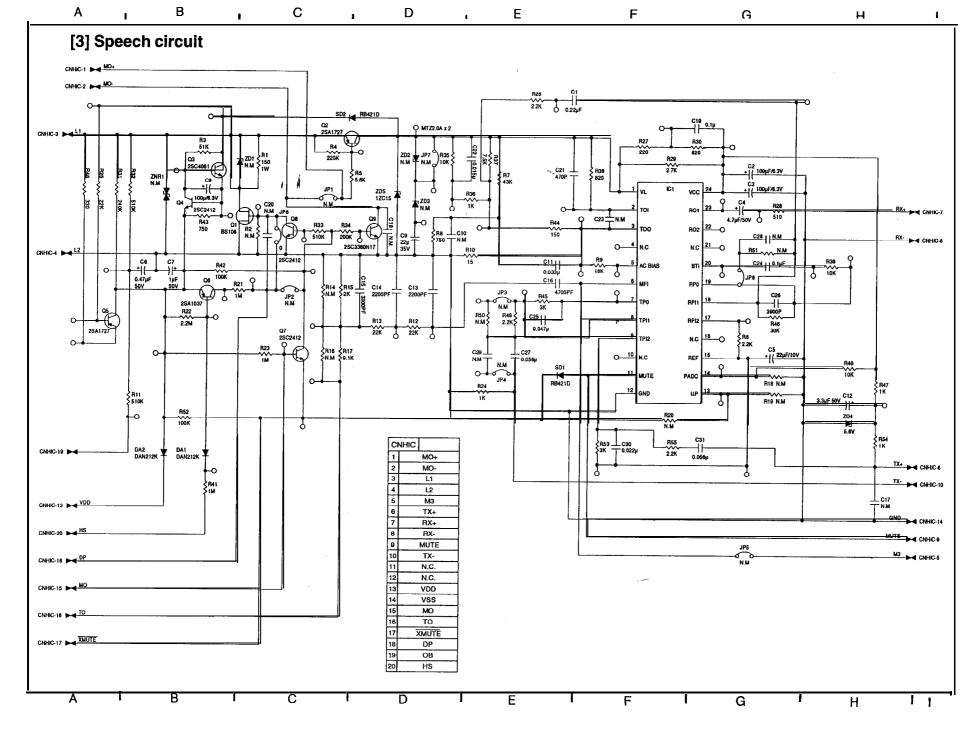
TEL/LIU PWB parts layout [Top side]





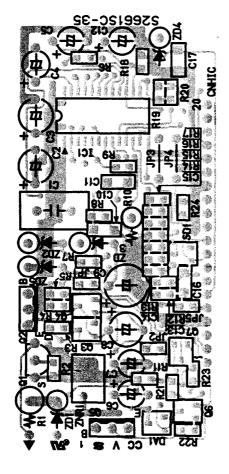
TEL/LIU PWB parts layout [Bottom side]



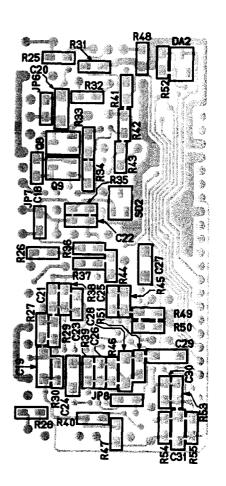


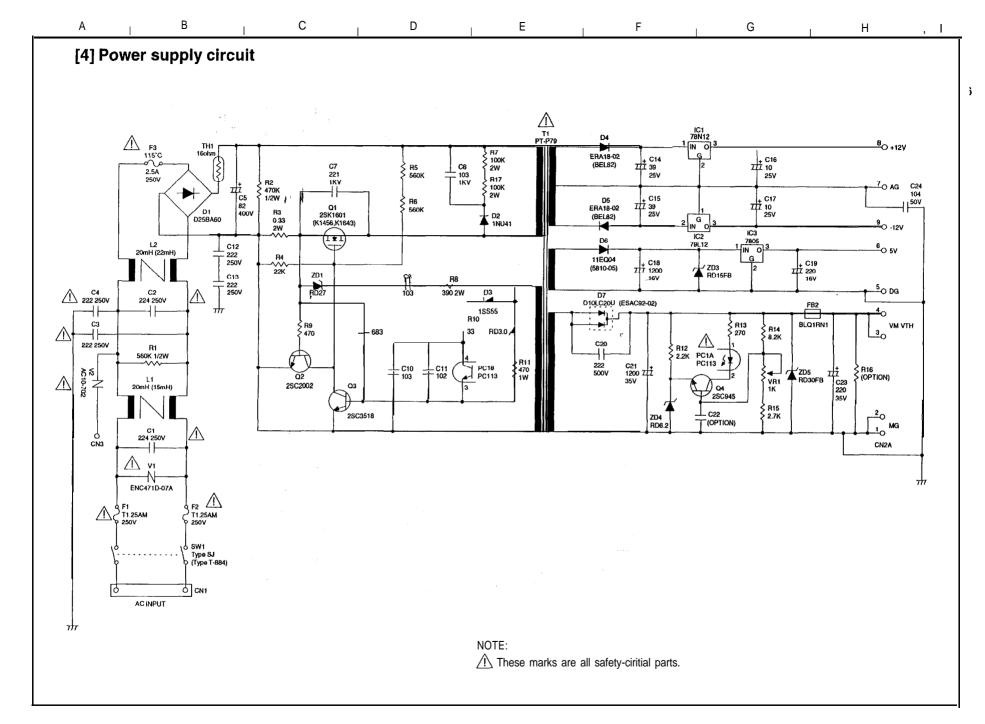


[Top side]



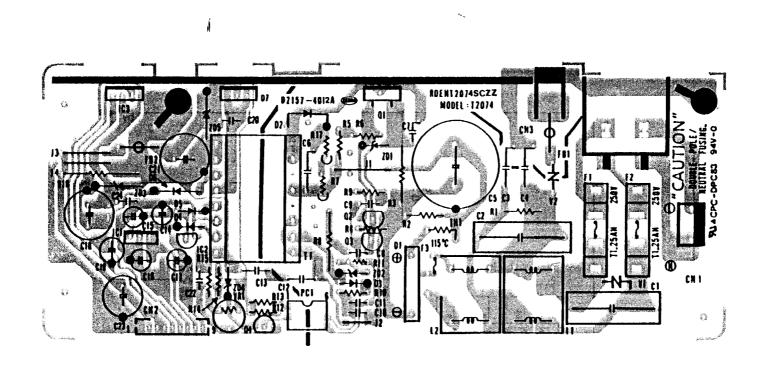
[Bottom side]

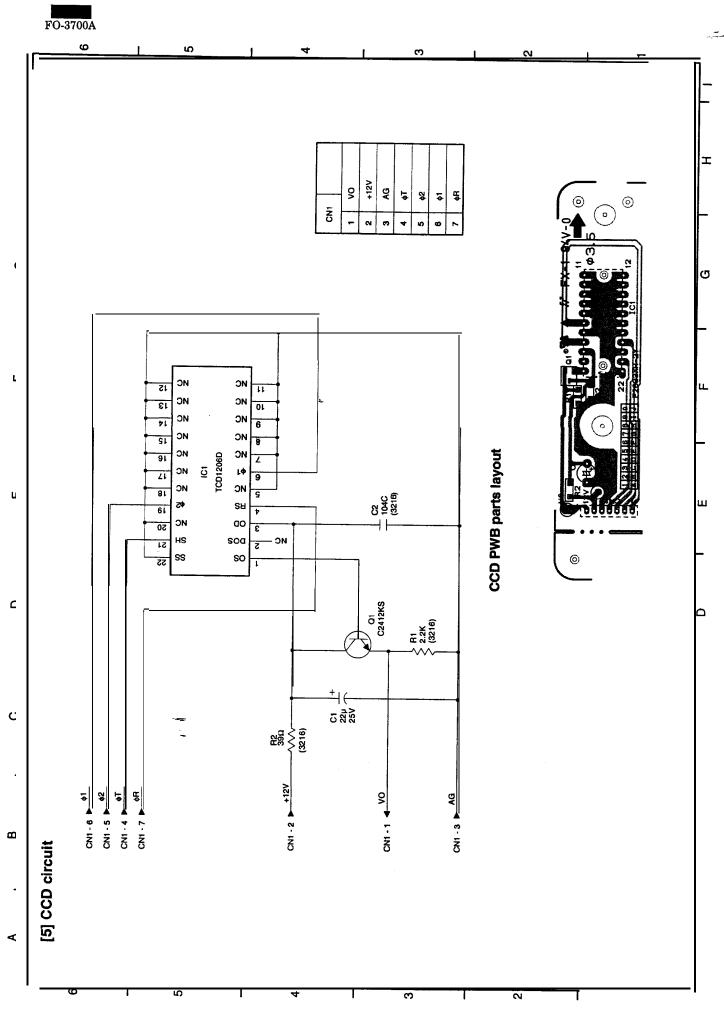


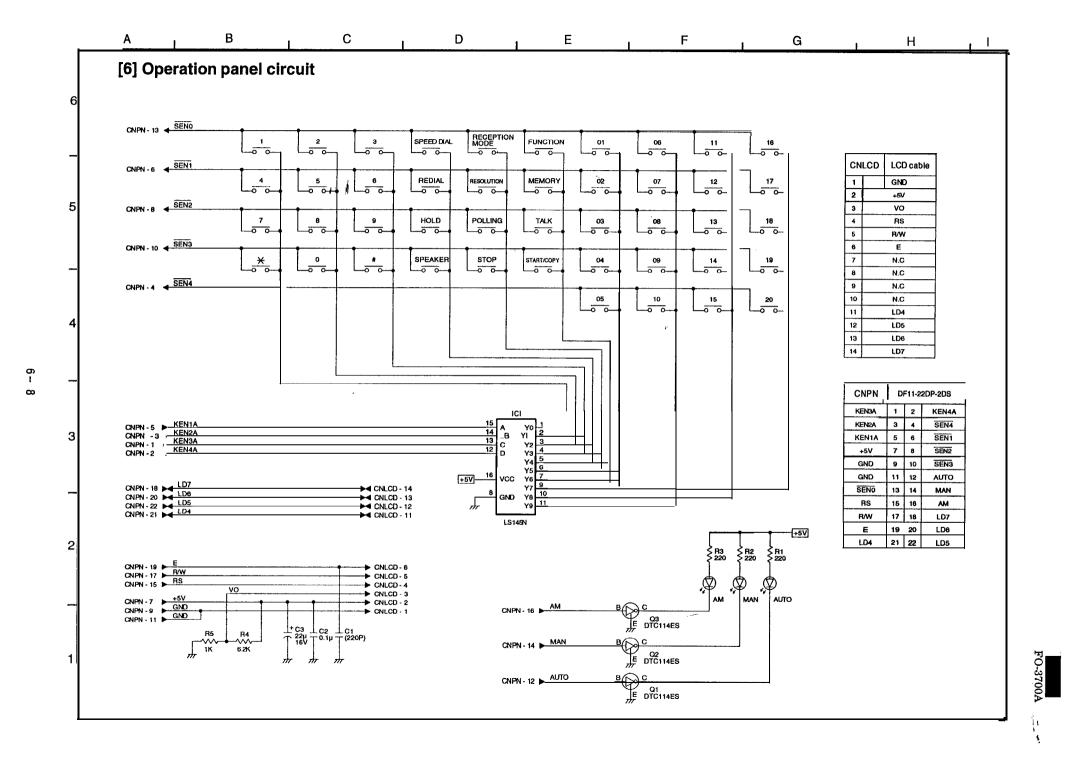


6 – 15

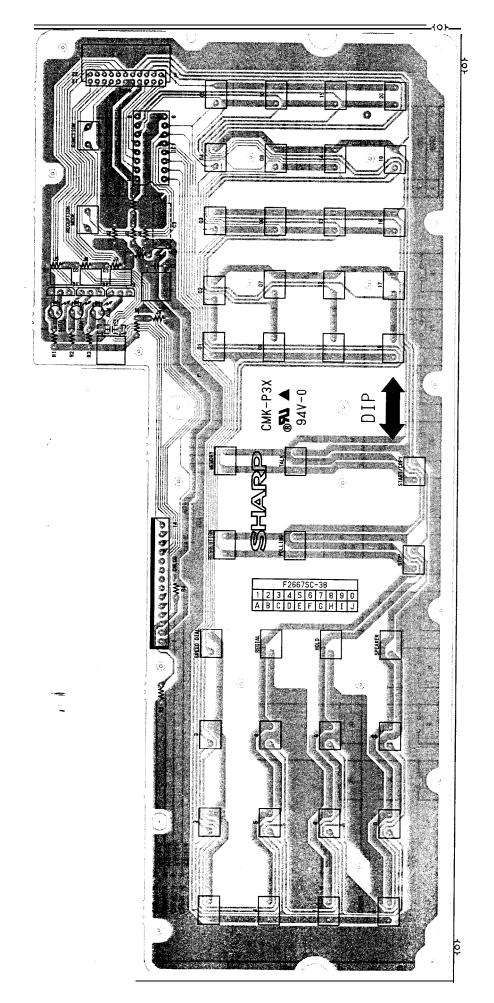
Power supply PWB parts layout

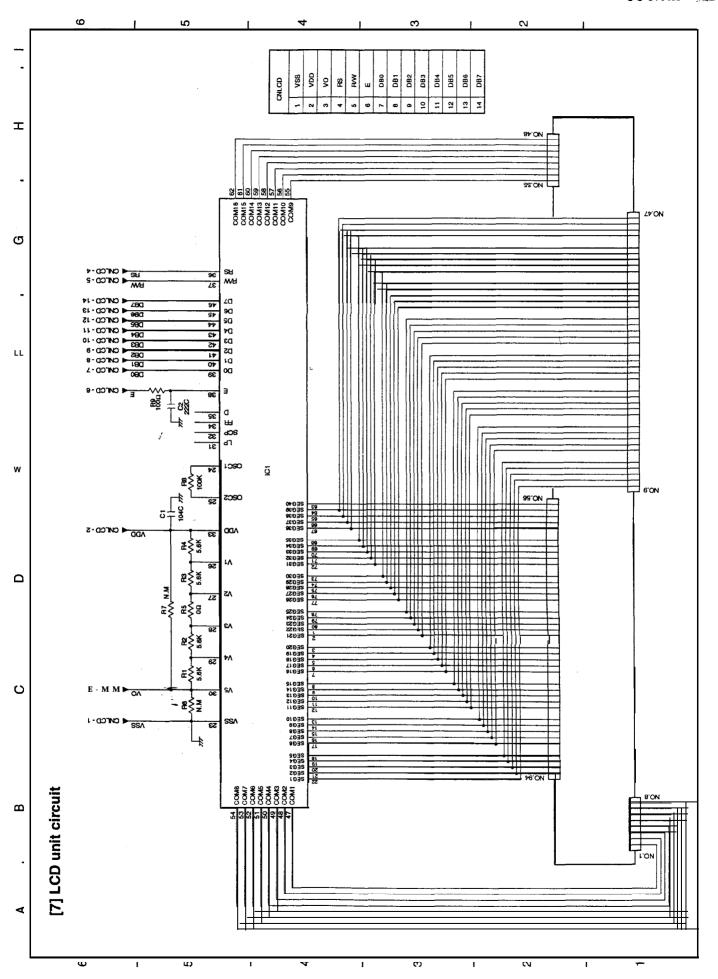


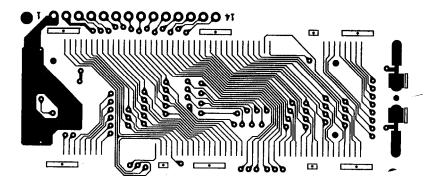


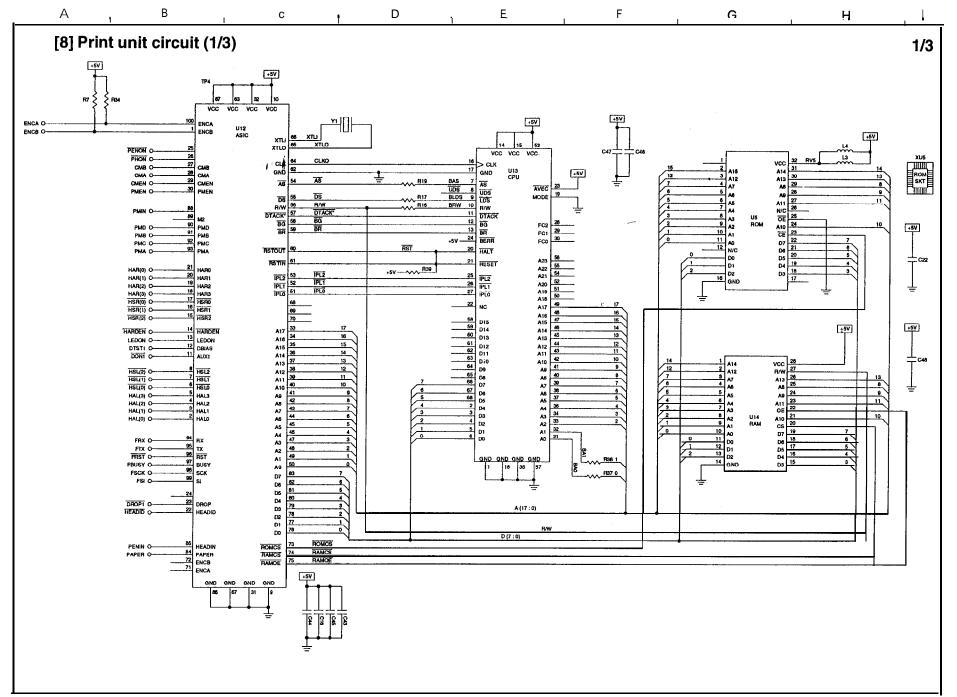




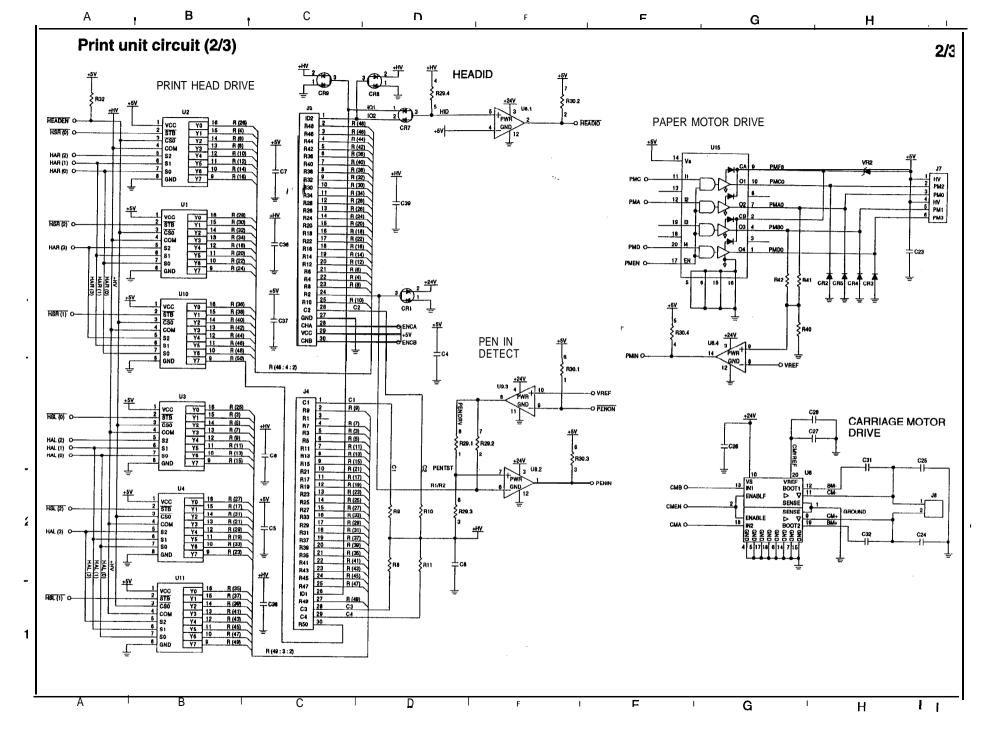






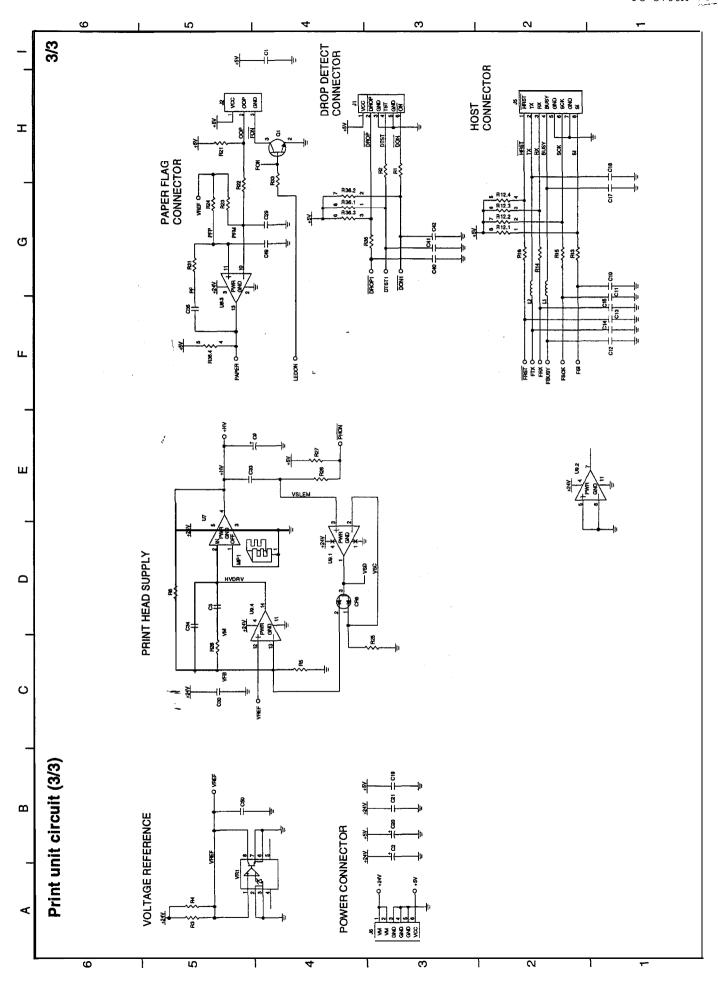


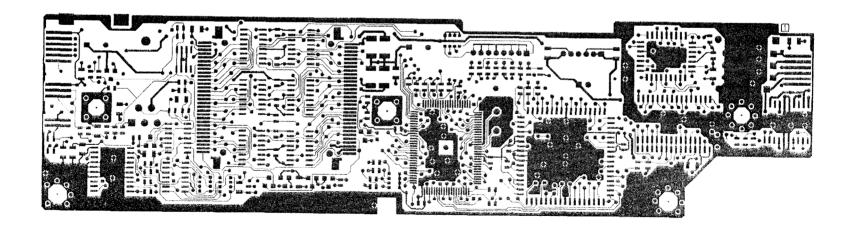




6-2

1

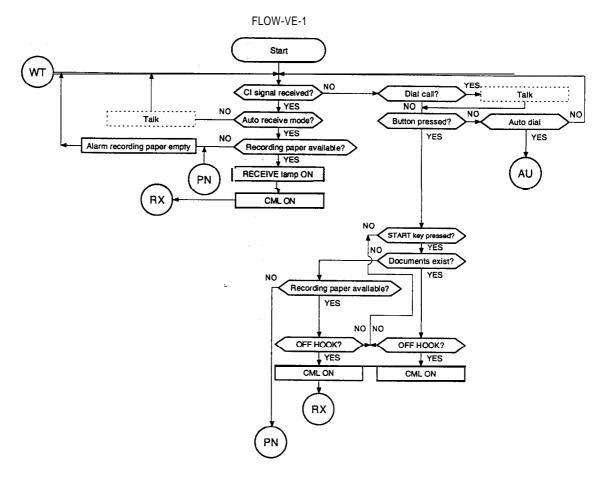


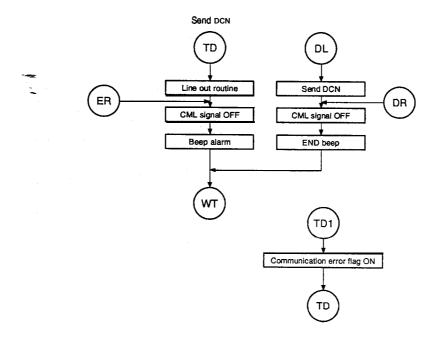


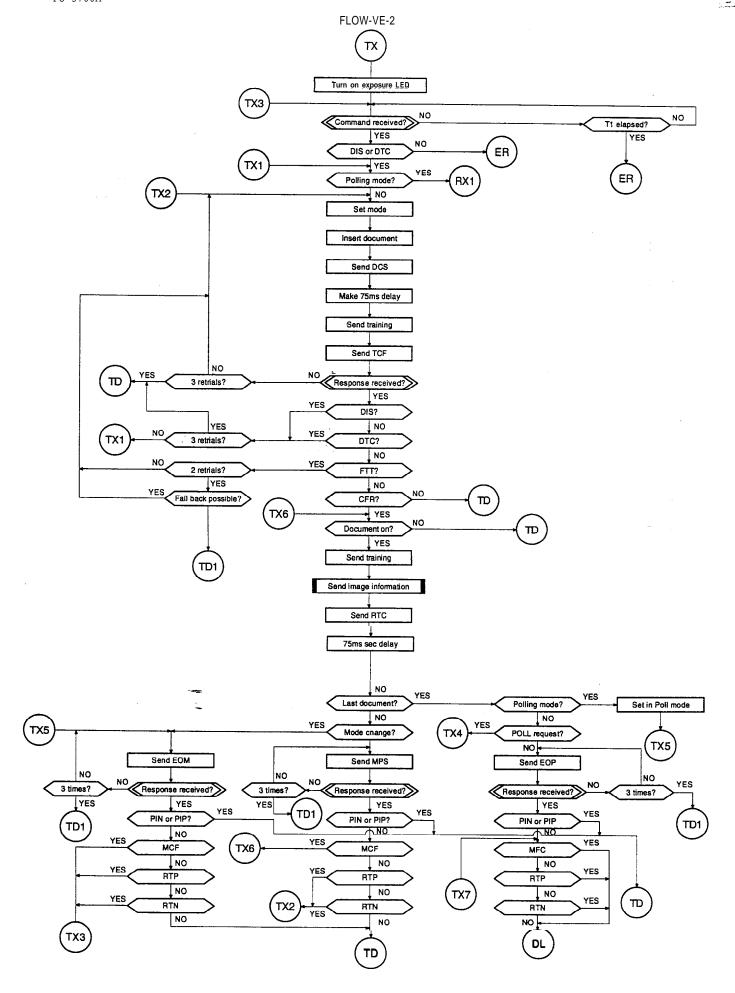


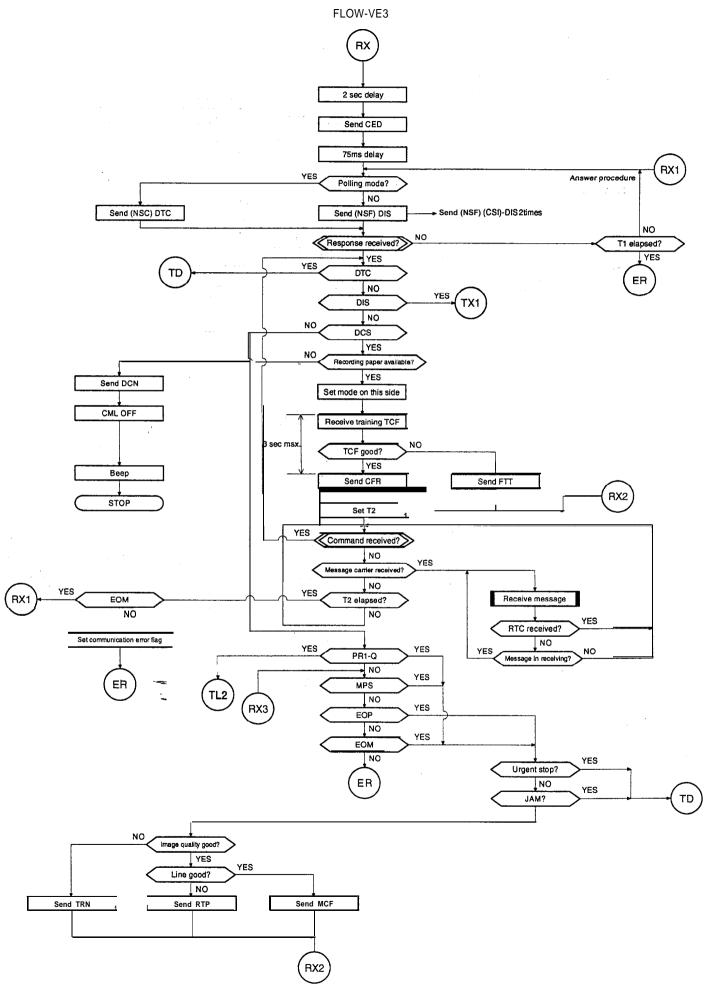
CHAPTER 7. OPERATION FLOWCHART

[1] Flow chart

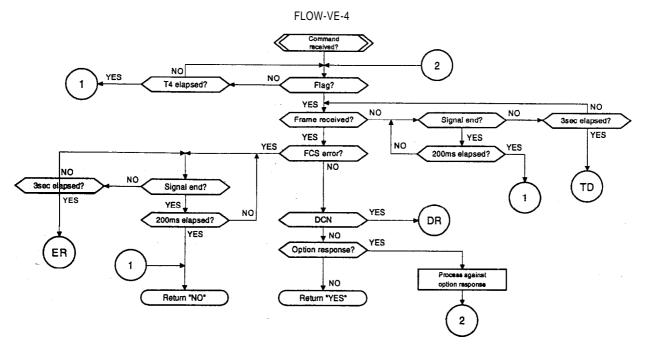


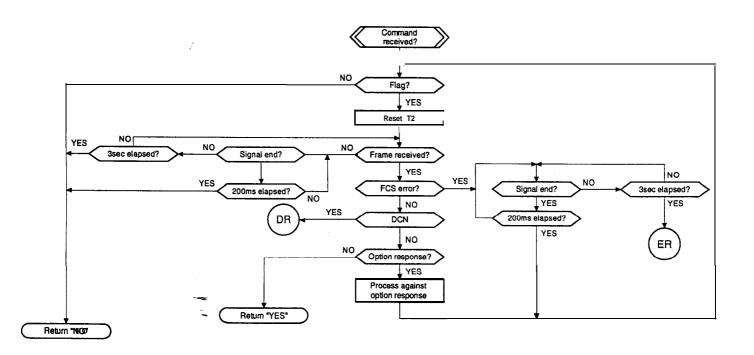


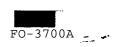




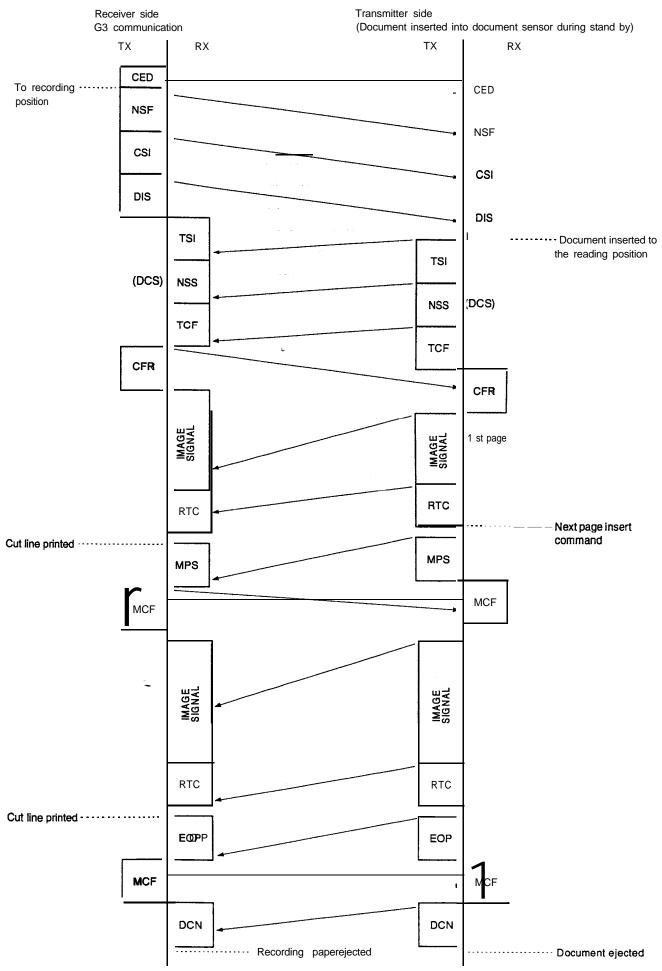






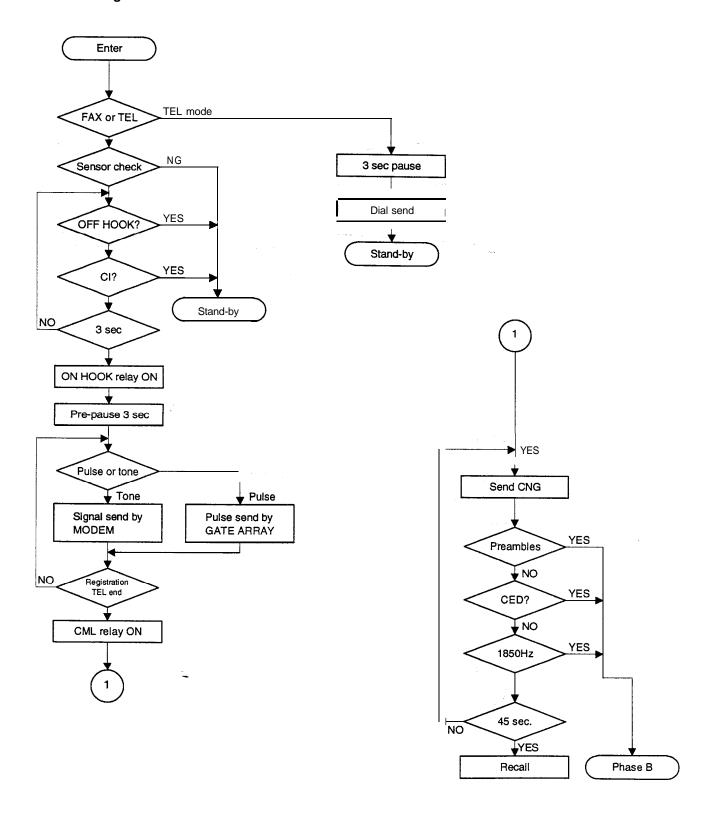


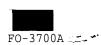
FLOW-VE-11



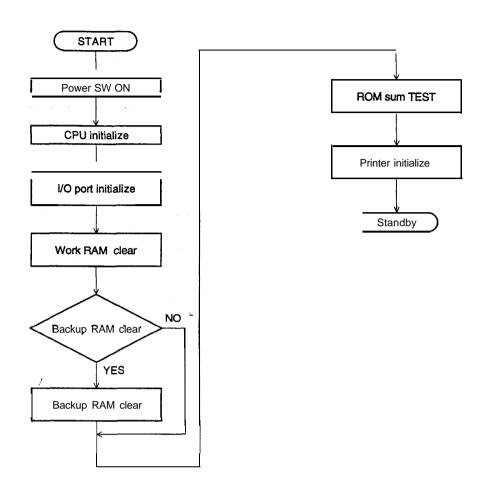


Auto dial sending





[2] Power on sequence



CHAPTER 8. OTHERS

[1] Service tools

1. List

NO.	PARTS CODE	DESCRIPTION		PRICE RANK
1	CPWBS2683SC03	Extension board unit	1	ВХ

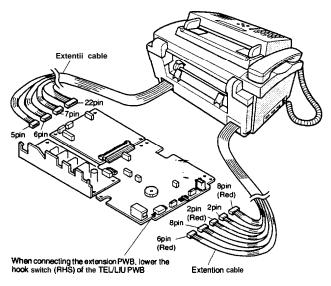
2. Description

2-1. Extension board unit

FO-3700 series extension PWB unit connection

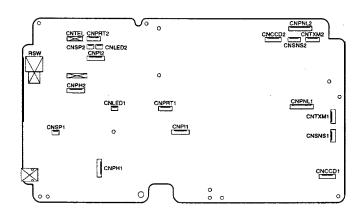
- 1) Remove the bottom ass'y from the body.
- 2) Connect the cables from the body with the extension PWB unit connectors (CNCCD1, CNSNS1, CNTXM1, CNPNL1, CNPI 1, CNPRT1, CNLED1, CNSP1, CNPH1) similarly with the control PWB and the TEL/LIU PWB unit. Fix the grounding cable to the bottom PWB with a screw.
 - (Note) When connecting the cables, check the color of the cables and the color of the connectors.
- 3) Pass the five cables which are provided for the extension PWB through "TEL LINE" and 'TEL SET' holes in the lower cabinet. Pass the four cables which are in the opposite side to the hook switch through the AC cord hole and the power switch hole, and fix the extension PWB.
- 4) Connect the extension cables which are in the rear of the body where the extension PWB has been installed with the **bottom** ass'y (the control PWB, the TEULIU PWB) as follows:

Extention PWB connection diagram



Cable parts code	Pin	Color	Connector	Remark
QCNW-4196SCZZ	22 Pin		CNPNL	
QCNW-4197SCZZ	7 Pin		CNCCD	Control
QCNW-4198SCZZ	6 Pin		CNTXM	PWB
QCNW-4199SCZZ	5 Pin		CNSNS	
QCNW-4200SCZZ	6 Pin	Red	CNPRT	
QCNW-4201SCZZ	8 Pin		CNPI	TEL/LIU
QCNW-4202SCZZ	2 Pin	Red	CNLED	PWB
QCNW-4203SCZZ	2 Pin		CNSP	
QCNW-4217SCZZ	8 Pin	Red	CNPH	

Extension board unit





NO.	PARTS CODE	DESCRIPTION	Q'TY	PRICE RANK
1	QCNW-4196SCZZ	CABLE (CNPNL)	1	AU
2	QCNW-4197SCZZ	CABLE (CNCCD)	1	AK
3	QCNW-4198SCZZ	CABLE (CNTXM)	1	AH
4	QCNW-4199SCZZ	CABLE (CNSNS)	1	AH
5	QCNW-4200SCZZ	CABLE (CNPRT)	1	AK
6	QCNW-4201SCZZ	CABLE (CNPI)	1	AL
7	QCNW-4202SCZZ	CABLE (CNLED)	1	AE
8	QCNW-4203SCZZ	CABLE (CNSP)	1	AE
9	QCNW-4217SCZZ	CABLE (CNPH)	1	AK
10	QCNCM7014SC0G	CONNECTOR 7pin (CNCCD1, CNCCD2)	2	AB
11	QCNCM7014SC0E	CONNECTOR 5pin (CNSNS1, CNSNS2)	2	AB
12	QCNCM7014SC0F	CONNECTOR 6pin (CNTXM1, CNTXM2)	2	AB
13	QCNCM2389SC2B	CONNECTOR 22pin (CNPNL1, CNPNL2)	2	AE
14	QCNCM7014SC0H	CONNECTOR 8pin (CNPI1, CNPI2)	2	AB
15	QCNCM705FAF02	CONNECTOR 6pin (CNPRT1, CNPRT2)	2	AB
16	QCNCM7014SC0B	CONNECTOR 2pin (CNLED1, CNLED2)	2	AD
17	QCNCM2401SC0H	CONNECTOR 8pin (CNPH1, CNPH2)	2	AC
18	QCNCM2401SC0B	CONNECTOR 2pin (CNSP1, CNSP2)	2	AA
19	LPLTM2684SCZZ	Bottom plate	1	AR
20	XHBSD30P05000	Screw, 3 × 5mm	3	AA
21	QPWBS2683SCZZ	EXTENSION BOARD (WITHOUT PARTS)	1	BV

List of jigs used for disassembly and assembly of the ink jet printer

NO.	PARTS CODE	DESCRIPTION	a n	PRICE RANK
1	0 J Z C 2 1 4 4 6 0 0 0 3	Pen garage	1	BK
2	UKOGD2031SCZZ	Torx screwdriver (M2.5 x 5)	1	BQ
3	UKOGD2032SCZZ	Torx screwdriver (M3 x 8)	1	BQ
4	UKOGM2026SCZZ	Optical adjustment plate	1	BP

2-2. Scan optical system adjustment

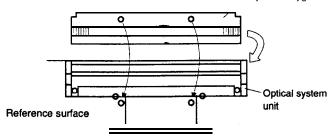
(1) Outline

The adjustment procedures of the scan optical system are described below:

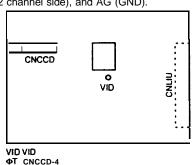
(2) Adjustment procedures

- 1 Fully open the upper cabinet, remove fixing screws of the recording paper tray, and remove the recording paper tray. In order to perform focus adjustment, remove the optical system unit from the frame.
- 2 Install the scan adjustment jig to the optical system unit so that the pattern surface is in the lower side.
- 3 Fit the pin of the optical system adjustment jig with the hole in the optical system frame.

Scan adjustment jig



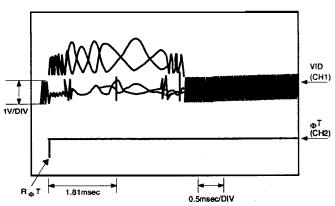
4 Use an oscilloscope to connect the control PWB VID (1 channel side), ϕT (2 channel side), and AG (GND).

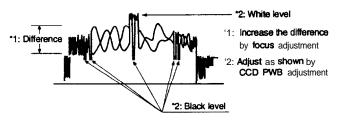


AG CNCCD-3

(5) Supply power to the main body to light the LED in the LED array lighting mode Loosen the two red screws of the CCD to obtain the CCD position so that the following waveform is obtained.

CCD waveform





[CCD waveform model]

6 By adjusting the CCD PWB as shown above, focus is adjusted and scan line is aligned.

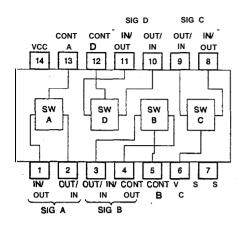
After completion of the CCD adjustment, tighten the two red screws and apply screw lock.

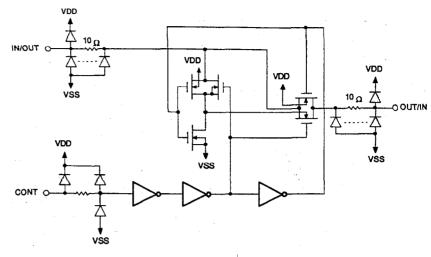


[2] IC signal name

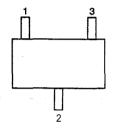
VHIMC14066BF Pin Arrangement

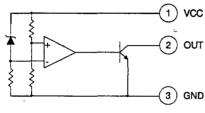
Equivalent circuit (One of the four blocks)



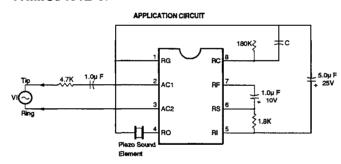


VHIPST600CMT1



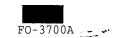


VHIMC34012-1P

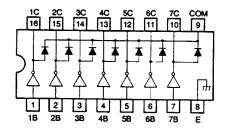


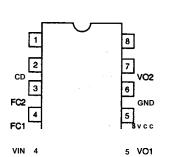
APPLICATION CIRCUIT PERFORMANCE

Characteristic	Typical Value	Units
Output Tone Frequencies MC3401 2-I Warble Frequency	832/1 040 13	HZ
Output Voltage (VI>60Vms,20Hz)	20	VP-P
Output Duty Cycle	50	%
Ringing Start Input Voltage (20Hz)	36	Vns
Ringing Stop Input Voltage (20Hz)	28	Vrms
Maximum AC Input Voltage (<68Hz)	150	Vrms
Impedance When Ringing VI=40Vrms,15Hz VI=130Vrms,23Hz	20 10	К
Impedance When Not Ringing VI=10Vrms,24Hz VI=2.5Vrms,24Hz VI=10Vrms,5.0Hz VI=3.0Vrms,200~3200Hz	28 >1.o 55 >1.o	ΚΩ ΜΩ ΚΩ ΜΩ
Maximum Transient Input Voltage (T<2.0 ms)	1500	V



VHIULN2003AN/

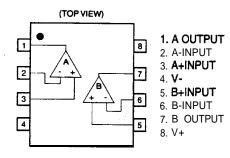




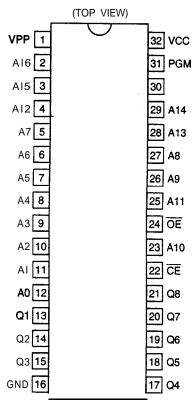
VHIMC34012-1P

RG 1			8 RC
AC1 2			7 RF
AC2 f	-	2	63 RS
RO 4			5 RI
			_

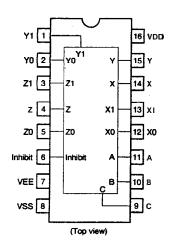
VHINJM4558D-1



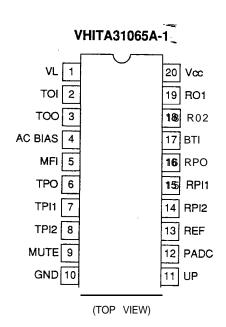
2M EP-ROM (VHI27C02012TI)

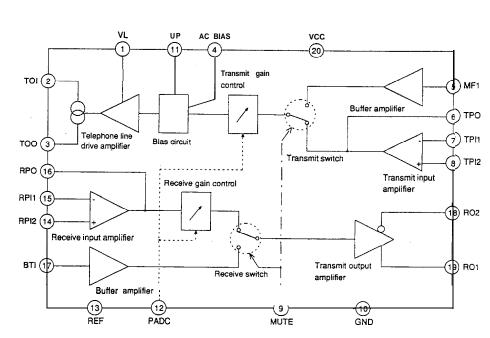


VHINJU4053D-1



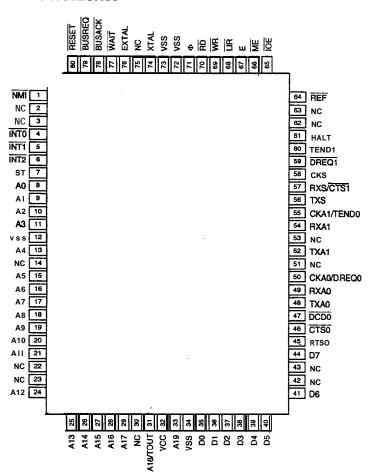
Pin name	Signal
A0~A17	Address input
CE	Chip enable
ŌĒ	Output enabel
PGM	Program
VPP	+12V ~ -13V programming power
GND	Ground
Q1~Q8	Data output
V C C	+5V power



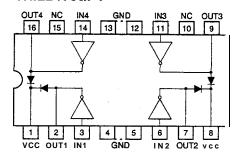




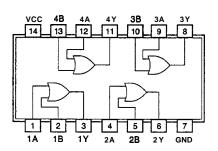
VHI64180ZSR08



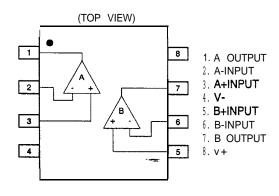
VHILB1730//-1

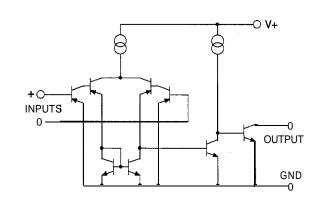


VHIMC74HC32F-

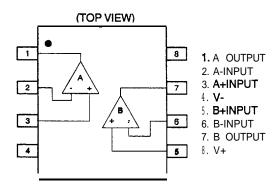


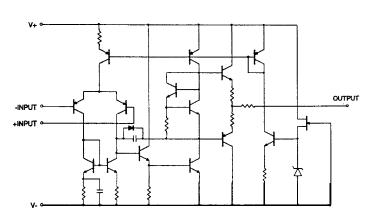
VHILM393PS/-S





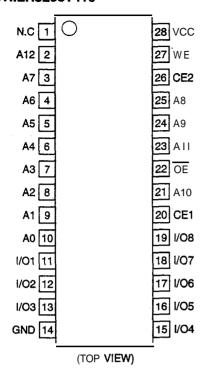
VHINJM4558MF-





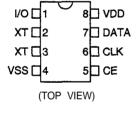


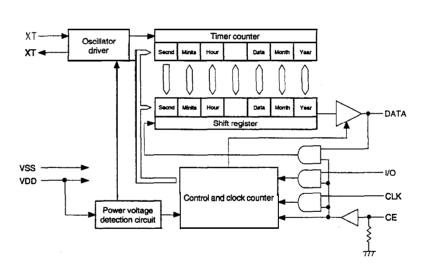
VHILH5268T410



Pin name	Signal
A0~A12	Address input
CE1/CE2	Chip enable
WE	Write enable
ŌĒ	Output enable
I/O1~I/O8	Data I/O
VCC	Power source
GND	Ground
N.C.	Non connection

VHINJU6355E-1

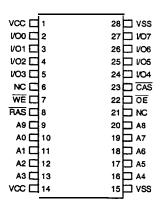




ю	Function		Desci	iption	l	
1	νō	DATA pin I/O select pin "H": Data input "L": Tata output When,however, CE pin is in "L", DATA pin is in high impedance				
2	XT XT	Crystal oscillator connection pin (f= 32.768KHz) For the capacity of Cg and Cd, refer to the series composition table				
5	CE	Chip enable input pin (built-in pull-down resistor) "H": DATA pin allows data input/output "L": DATA pin is in high impedance				
6	CLK	Clock input pin: Data are inputted or output in synchronization with this clock When, however, CE pin in "L", DATA pin is in high impedance				
		Serial timer data I/O pin	1/0	CE	DATA pin	
7	DATA		H L H L	HLL	Input Output High impedance High impedance	
8	VDD VSS	Power pin +5V Power pin GND				

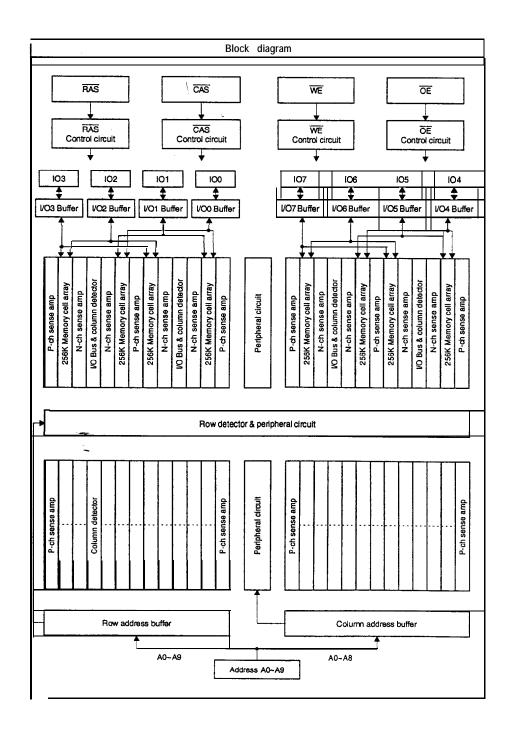


VHIHM514800J8

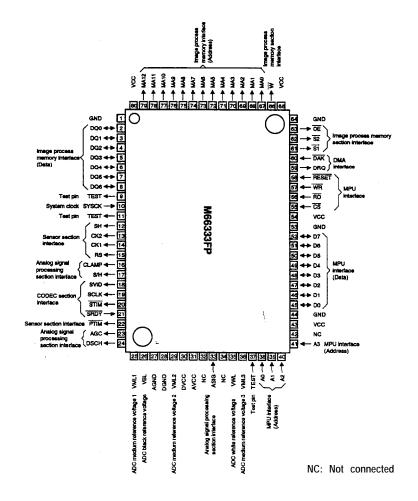


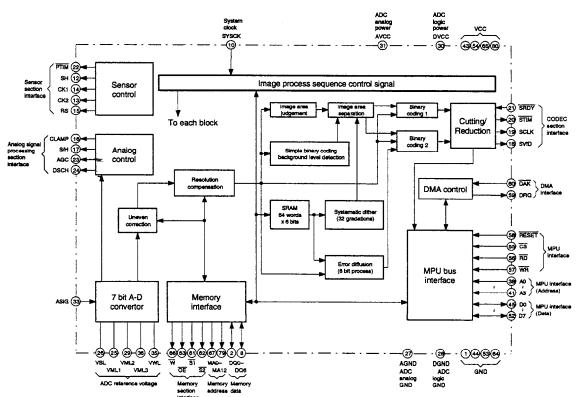
Pin descrpt					
Pin	Pin name	7			
AO-A9	Address input (Low/Refresh AO-A3 Column AO-A3)				
1/00~1/07	Data I/O	1			
RAS	Low address strobe]			

Pin name
Column address strobe
Read/Write input
Output enable
Power (+5V)
Connection



VHIM66333FP-1





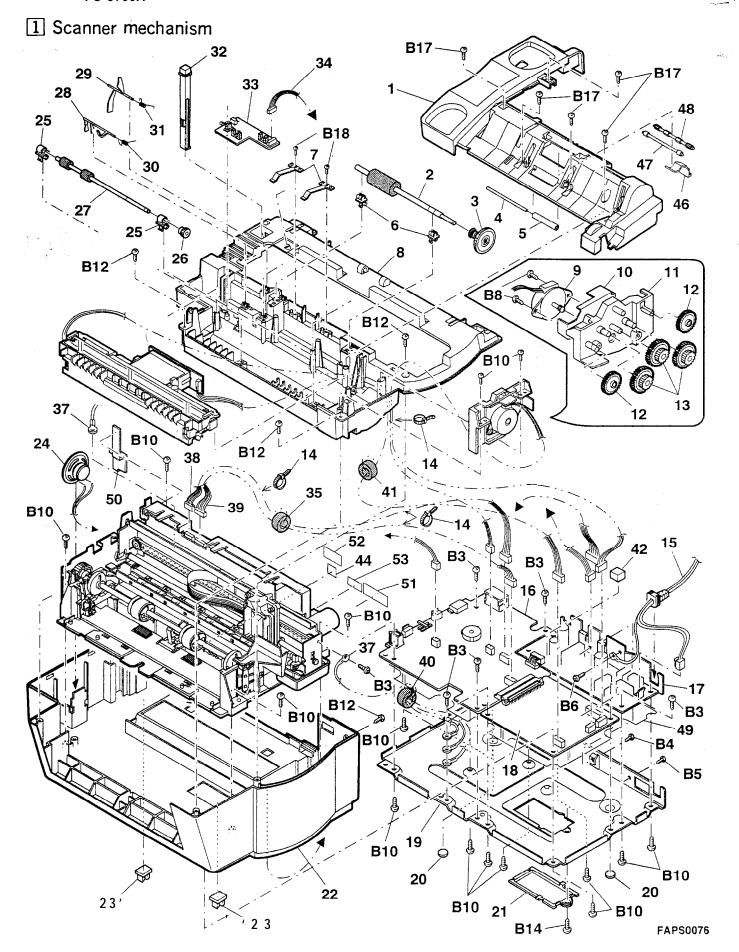
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SHARP PARTS GUIDE

MODEL FO-3700

CONTENTS 1 Scanner mechanism 7 Power supply PWB unit 2 Upper cabinet, document TEL-Liu PWB unit guide upper 9 Panel PWB unit 3 Print mechanism 10 Sensor PWB unit Optical unit 11 CCD PWB unit 5 Packing material & Accessories 50 Hardware parts 6 Control-PWB unit Index

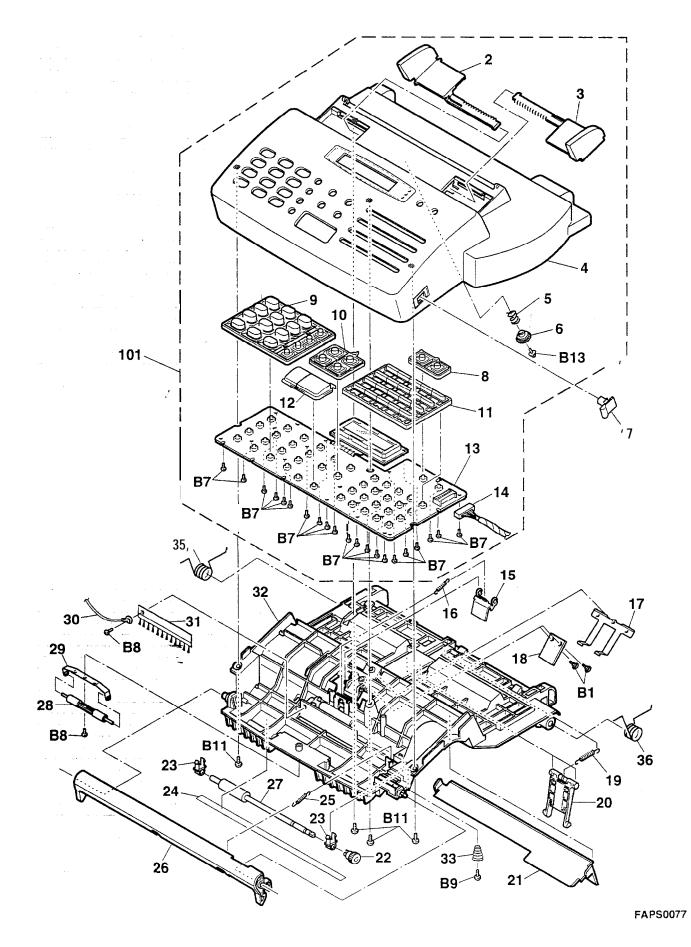
Because parts marked with " \triangle " is indispensable for the machine safety maintenance and operation, it must be replaced with the parts specific to the product specification.



FO-3700A

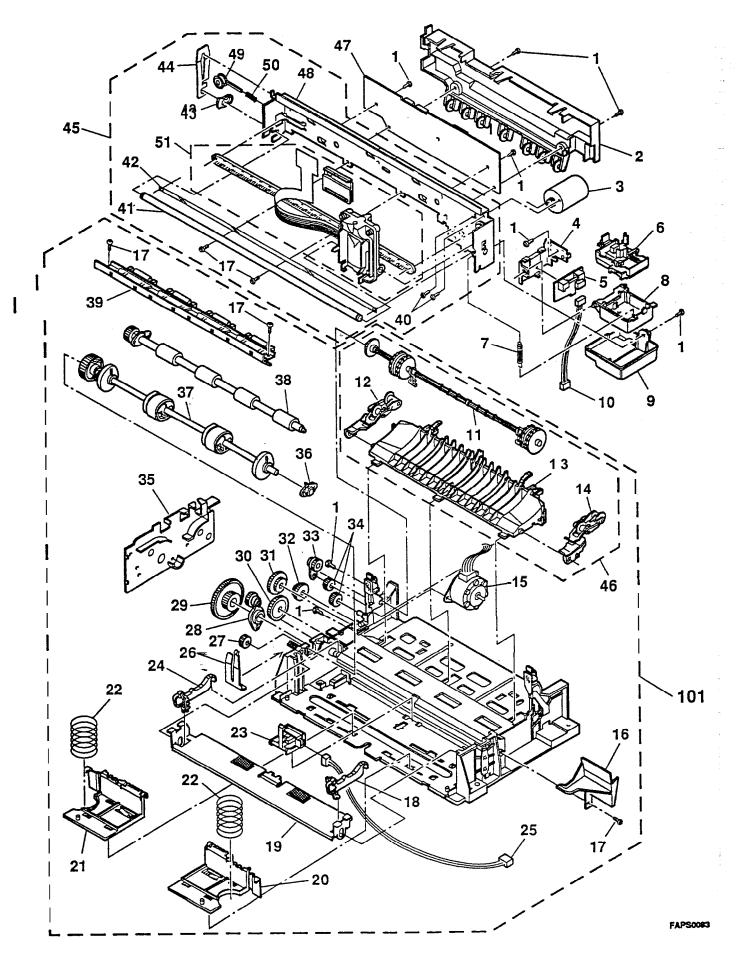
SANT	10.	PARTS CODE	PRICE	NEW	PART	DESCRIPTION
2 N R Ö L R 2 2 5 S C 2 Z AM C C Paper feed roller 3 N G E R H 2 2 5 S C 2 Z A E C Pinch roller shaft C Paper feed gear 542 4 N S F T 7 2 2 4 9 S C 2 Z AS C Pinch roller shaft C Paper feed gear 542 4 N S F T 7 2 2 4 9 S C 2 Z AS C Pinch roller shaft C Pinch roller shaft C Paper feed gear 542 4 N S F T 7 2 2 4 9 S C 2 Z AD C Pinch roller shaft	1	PG I DM 2 3 9 8 SCZD				
3 N N G R H 2 2 3 7 S C 2 Z A E C Paper feed gear 542 4 N S F T 2 2 4 3 S C 2 Z A S C C Pinch roller sheft 5 N N R O L P 2 2 2 S C 2 Z A D C C Pinch roller 7 M S P R P 2 6 3 S C 2 Z A D C C Pinch roller 9 M O T 2 2 2 3 S C 2 Z A C C Pinch roller 9 M O T 2 2 3 S C 2 Z A C C Pinch roller 10 L P L T M 2 6 3 S C 2 Z A C C Pinch roller 11 L P L L T P 2 6 7 S C 2 Z A C C Pinch roller 12 N G E R R 2 2 1 D X H 2 Z A C C C Pinch roller 13 N G E R R 2 2 1 D X H 2 Z A C C C Restrict roller 14 L B N D 1 2 0 0 5 S C 2 Z A C C C Restrict roller 15 Q A C C L 7 5 C A S C 2 Z A C C C Restrict roller 16 D C F K L 3 4 5 A S C 2 Z B Y N E T E L - Lu P WB unit 17 N D C F K C 3 6 F S C 2 Z A C C C Restrict roller 18 D C E K C 3 6 F S C 2 Z A C C C Restrict roller 19 L D C F K C 3 6 F S C 2 Z A C C C Restrict roller 10 L C F C C S C C S C C C Restrict roller 10 L C F C C S C C C C Restrict roller 10 L C F C C S C C C C C C C C C C C C C C C	2	NROLR2285SCZZ		l '`		
5 N R O L P 2 2 3 S C Z Z A D C Pinch roller 6 N B R G P 2 1 3 S K H Z Z A D C Bearing 7 M S P R P 2 6 5 3 S C Z Z A C I C Pinch spring 1 8 G C A B C 2 2 3 3 S C Z Z B B A I D Middle cabinet 9 R M O T Z 2 1 0 9 S C Z Z A X B Transfer motor 10 L P L T W 2 6 8 T S C Z A E C C Heat sink 11 L P L T P 2 6 7 T S C Z A H C Driving plate 12 N G E R H 2 2 1 0 X H Z A C C I Gle gear 1 13 N G E K H 2 2 1 0 X H Z A C C C I Ged cearn [G T 100M] 13 N G E K H 2 2 1 0 X H Z A C C C I Ged cearn [G T 100M] 14 C S C C C C C C C C C C C C C C C C C	3 1	NGERH2257SCZZ			С	
C N B R G P 2 1 3 × H 2 Z A D C Bearing				ļ		
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37 Q C N W - 4 1 7 3 S C Z Z A C C PS earth cable 38 Q C N W - 4 1 6 8 S C Z Z A G C PS cable 39 Q C N W - 4 1 6 8 S C Z Z A G C PS signal cable 40 R C O R F 2 0 8 4 S C Z Z A G C C Core 41 R C O R F 2 0 6 3 S C Z Z A E C C Core (TRA20) 42 P S P A Z 2 1 8 1 S C Z Z A B C C PWB spacer 44 P S H E Z 2 8 8 0 X H Z Z A K C Jack sheet 46 M S P R P 2 6 6 7 S C Z Z A C C Feed roller fixed spring 47 N R O L R 2 2 8 8 S C Z Z A H C Feed roller 48 N R O L P 2 2 8 9 S C Z Z A E C Transmission roller 49 P S H E Z 2 9 8 0 S C Z Z A M C C Insulator 50 P S H E Z 3 0 0 2 S C Z Z A B N D N Z Iabel 51 T L A B S 3 4 2 1 S C Z Z A B N D N Z Iabel 53 T L A B Z 3 4 1 8 S C Z Z A B N D TEL explanation label						
39 Q C N W - 4 1 6 8 S C Z Z AG C C Core 40 R C O R F 2 0 8 4 S C Z Z AG C C Core 41 R C O R F 2 0 6 3 S C Z Z AE C C Core (TRA20) 42 P S P A Z 2 1 8 1 S C Z Z AB C PWB spacer 44 P S H E Z 2 8 8 0 X H Z Z AK C Jack sheet 46 M S P R P 2 6 6 7 S C Z Z AC C Feed roller fixed spring 47 N R O L R 2 2 8 8 S C Z Z AH C C Feed roller fixed spring 48 N R O L P 2 2 8 9 S C Z Z AH C C Transmission roller 49 P S H E Z 2 9 8 0 S C Z Z AM C Insulator 50 P S H E Z 3 0 0 2 S C Z Z AD C C Conceal sheet 51 T L A B S 3 4 2 0 S C Z Z AB N D AUSTEL label 53 T L A B Z 3 4 1 8 S C Z Z AB N D TEL explanation label	37 (QCNW-4173SCZZ				
40 R C Ô R F 2 0 8 4 S C Z Z A G C Core 41 R C Ô R F 2 0 6 3 S C Z Z A E C Core (TRA20) 42 P S P A Z 2 1 8 1 S C Z Z A B C PWB spacer 44 P S H E Z 2 8 8 0 X H Z Z A K C Jack sheet 46 M S P R P 2 6 6 7 S C Z Z A C C Feed roller fixed spring 47 N R Ô L R 2 2 8 8 S C Z Z A H C Feed roller fixed spring 48 N R Ô L P 2 2 8 9 S C Z Z A E C Transmission roller 49 P S H E Z 2 9 8 0 S C Z Z A M C C Insulator 50 P S H E Z 3 0 0 2 S C Z Z A B N D AUSTEL label 51 T L A B S 3 4 2 1 S C Z Z A B N D NZ label 52 T L A B Z 3 4 1 8 S C Z Z A B N D TEL explanation label	38 (QCNW-4167SCZZ				
41 R C O R F 2 0 6 3 S C Z Z A B C C Core (TRA20) 42 P S P A Z 2 1 8 1 S C Z Z A B C PWB spacer 44 P S H E Z 2 8 8 0 X H Z Z A K C Jack sheet 45 M S P R P 2 6 6 7 S C Z Z A C C Feed roller fixed spring 47 N R O L R 2 2 8 8 S C Z Z A H C Feed roller 48 N R O L P 2 2 8 9 S C Z Z A M C Insulator 49 P S H E Z 2 9 8 0 S C Z Z A M C Insulator 50 P S H E Z 3 0 0 2 S C Z Z A D C C Corecal sheet 51 T L A B S 3 4 2 1 S C Z Z A B N D A UST EL label 52 T L A B S 3 4 2 1 S C Z Z A B N D TEL explanation label	39 (QCNW-4168SCZZ				
42 P S P A Z 2 1 8 1 S C Z Z A B C PWB spacer 44 P S H E Z 2 8 8 0 X H Z Z A K C Jack sheet 46 M S P R P 2 6 6 7 S C Z Z A C C Feed roller fixed spring 47 N R O L R 2 2 8 8 S C Z Z A H C Feed roller 48 N R O L P 2 2 8 9 S C Z Z A E C Transmission roller 49 P S H E Z 2 9 8 0 S C Z Z A M C Insulator 50 P S H E Z 3 0 0 2 S C Z Z A D C Conceal sheet 51 T L A B S 3 4 2 0 S C Z Z A B N D AUSTEL label 52 T L A B S 3 4 2 1 S C Z Z A B N D T EL explanation label	40	R C O R F 2 0 8 4 S C 2 Z				
44 P S H E Z 2 8 8 0 X H Z Z	42 [PSPA721815C77				
46 M S P R P 2 6 6 7 S C Z Z A C C Feed roller fixed spring 47 N R O L R 2 2 8 8 S C Z Z A H C Feed roller 48 N R O L P 2 2 8 9 S C Z Z A E C Transmission roller 49 P S H E Z 2 9 8 0 S C Z Z A M C Insulator 50 P S H E Z 3 0 0 2 S C Z Z A B N D AUSTEL label 51 T L A B S 3 4 2 0 S C Z Z A B N D NZ label 52 T L A B S 3 4 2 1 S C Z Z A B N D TEL explanation label	44 I	P S H E Z 2 8 8 0 X H Z Z				
48 N R Ô L P 2 2 8 9 S C Z Z A E C Transmission roller 49 P S H E Z 2 9 8 0 S C Z Z A M C Insulator 50 P S H E Z 3 0 0 2 S C Z Z A D C Conceal sheet 51 T L A B S 3 4 2 0 S C Z Z A B N D AUSTEL label 52 T L A B S 3 4 2 1 S C Z Z A B N D NZ label 53 T L A B Z 3 4 1 8 S C Z Z A B N D TEL explanation label	46 N	MSPRP2667SCZZ				
49 P S H E Z 2 9 8 0 S C Z Z A D C Conceal sheet 50 P S H E Z 3 0 0 2 S C Z Z A D C Conceal sheet 51 T L A B S 3 4 2 0 S C Z Z A B N D AUSTEL label 52 T L A B S 3 4 2 1 S C Z Z A B N D NZ label 53 T L A B Z 3 4 1 8 S C Z Z A B N D TEL explanation label	47 1	N R O L R 2 2 8 8 S C Z Z				
50 P S H E Z 3 0 0 2 S C Z Z A B N D AUSTEL label 51 T L A B S 3 4 2 0 S C Z Z A B N D NZ label 52 T L A B Z 3 4 1 8 S C Z Z A B N D TEL explanation label						
51 T L A B S 3 4 2 0 S C Z Z A B N D AUSTEL label 52 T L A B S 3 4 2 1 S C Z Z A B N D NZ label 53 T L A B Z 3 4 1 8 S C Z Z A B N D TEL explanation label				 		
52 T L A B S 3 4 2 1 S C Z Z A B N D NZ label 53 T L A B Z 3 4 1 8 S C Z Z A B N D TEL explanation label	51	T L A B S 3 4 2 0 S C Z Z		N		
53 T L A B Z 3 4 1 8 S C Z Z A 8 N D TEL explanation label	52	T L A B S 3 4 2 1 S C Z Z				
	53	T L A B Z 3 4 1 8 S C Z Z				
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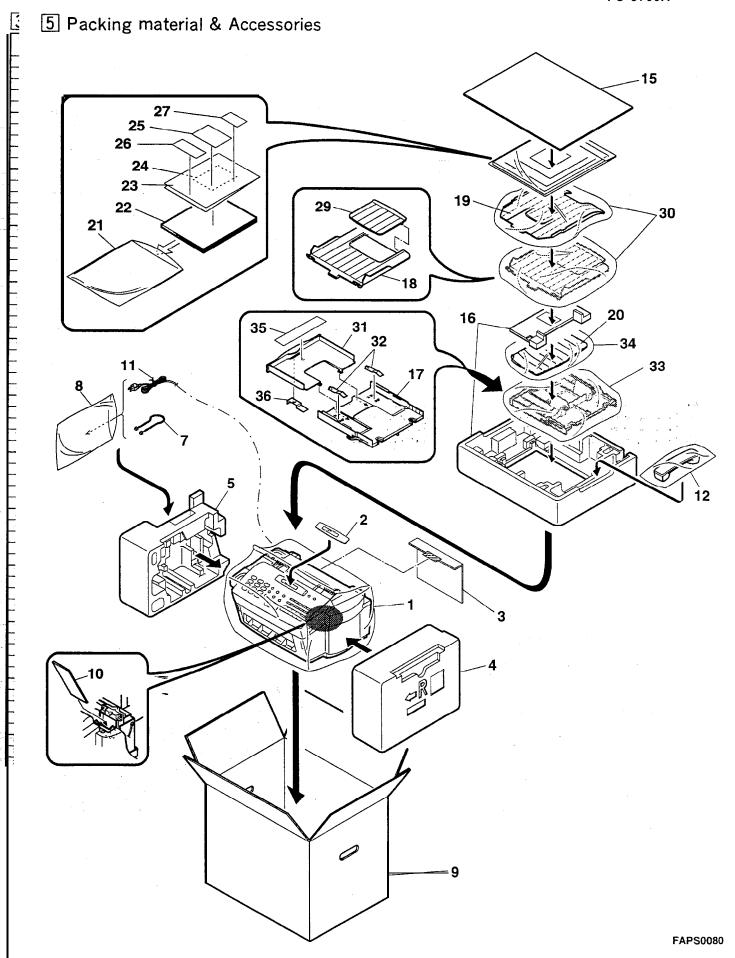
2 Upper cabinet, document guide upper



	Uppercabinet, document guide upper
_	opper capinet, document guide upper

	NO.	PARTS CODE	PRICE	NEW	PART	DESCRIPTION
Λ		PGIDM2397SCLB	AE	MARK		Hopper guide L
Δ	3	PG i DM 2 3 9 7 SCRB	ΑE		č	Hopper guide R
Δ	4	G C A B A 2 2 3 2 S C Z D	AX	N		Upper cabinet
l	<u>5</u>	M S P R C 2 6 6 0 S C Z Z N G E R P 2 2 0 6 X H Z Z	AB			Hopper spring Pinion gear
	7	JKNBP2063SCZB	AD			Release knob
	8	JBTN-2116SCZB	AC		С	Function key
	9	JBTN-2085XHZC	AR		С	12 key
H	10	JBTN-2117SCZB JBTN-2086XHZC	A C			A/M changing key
1	12	JBTN-2115SCZB	AG			Direct key Start/stop key
ı	13	DCEKP335ASC01	BD		E	Panel PWB unit
Δ	14	QCNW-4170SCZZ	AT		С	Panel cable
	15	LPLTM2685SCZZ	A D			Separater
ŀ	15	MSPRT2661SCZZ MSPRP2652SCZZ	AB			Separate spring Paper feed spring
	18	LPLTG2678SCZZ	AD			Separate rubber
	19	MSPRT2676SCZA	AB		С	Spring (Open and shut)
٨	20	LPLTP2676SCZZ	ΑE		С	Stopper plate
Δ.	21	PCOVP2097SCZB NGERH2258SCZZ	AH		Č	Document inserting cover
ł	23	NBRGP2141XH77	A B A H			Transfer gear 18Z Transfer bearing 2
Ì	24	PSHEZ2935SCZZ	AB			Rear sheet
	25	P S H E Z 2 9 3 5 S C Z Z M S P R T 2 6 5 7 S C Z Z	AB		C	Release lever return spring
ļ	26	MLEVP2166SCZZ	A M		С	Release lever
}	27	N R O L R 2 2 8 4 S C Z Z N R O L P 2 2 4 9 X H Z Z	AK			Transfer roller 1
	29	MSPRP2535XHZZ	A E A D	-		Pinch spring 2
$\boldsymbol{\Psi}$	30	QCNW-4175SCZZ	ĀĒ			Earth cable
	31	PBRS-2041SCZZ	AG		Č	Brush,electro – static discharger
Δ	32	PG i DM 2 3 9 6 SC Z B	ВА		С	Document guide upper
ŀ	33	MSPRC2681SCZZ	A C		C	Spring 2 (Open and shut)
ŀ	36	M S P R D 2 6 5 5 S C Z Z M S P R D 2 6 5 6 S C Z Z	A C		C	Spring (Open and shut) (L) Spring (Open and shut) (R)
ŀ	101	CCABA2232SC12	BM	N	E	Upper cabinet ass'y
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5 Packing material & Accessories

	NO.	PARTS CODE		NEW MARK		DESCRIPTION
	1	SPAKP4080SCZZA	M		D	Vinyl cover
Α	2	PSHEZ2930SCZB	A E		C	Face sheet
	3	SPAKA4345SCZZ			۵	Protector
	4	SPAKA4106SCZZ			ם	Side pat R
	5	SPAKA4105SCZZI	A M		D	Side pat L
L	7	QCNW-3376XHUG	AT		c	Hand set cord
L		SSAKA3001CCZZ A	A		D	Vinyl bag for AC cord (140×360mm)
- 1		SPAKC4202SCZZA	Р		D	Packing case
- 1		SPAKA4274SCZZ	AB		D	Protector
- 1		UBNDA1008CCZZA	Α		С	AC code band (120mm)
- 1		DUNTK4925SCD3A	X		E	Hand set
ŀ		SPAKA4109SCZZA	E		D	Pat
		SPAKA4108SCZZ	AN		_ <u>D</u>	Add
4		LPLTP2679SCZC	AS	N	С	Inserting tray
4		LPLTP2681SCZB	A			Exit tra ICI y
4		LPLTP2683SCZZ	AU	<u></u>	C	Original document out tray
4		PHOP-2076SCZZ	AQ		C	Original document in tray
ŀ		S S A K A 2 3 4 4 Q C Z Z	AB		D	Viny! bag (240×360mm)
- 1		T i N S E 3 3 9 7 S C Z Z P S H E Z 2 8 9 7 S C A 4	AZ	N	D	Operation manual
H		TCADZ 2 1 3 9 S C Z Z	A K		C D	Document carrier (A4)
ŀ		TCADZZISSCZZ	AE	-	_	Installation card
ŀ		TGANE 2 0 3 6 S C Z Z A	C	-	D	
, I		CADZ 2 2 7 4 S C Z B A	D		D	Warranty card Rapid key label
		LPLTP2682SCZBA	Н		C	Extend paper plate
А		SSAKA3341QCZZA	0		ă	Vinyl bag
Α		LPLTP2680SCZBA	N			Extend paper tray
Α	3				C	Extend plate spring
ŀ		3 SSAKA3340QCZZ			Ď	Vinyl bag (320×380mm)
ŀ		4 SSAKA1340QCZZ			D	Vinyl bag (180×320mm)
ŀ		5 TLABH3435SCZE			Ď	Paper setting label
ŀ		F382279828C2Z	AD		č	Protector
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	ontrol i wb unit					
NO.	PARTS CODE	RANK	NEW MARK	RANK	DESCRIPTION	
	UBATN2010SCZZ	I A N		В	Battery (CR2354-1HF)	[BAT]
	EAPS106AF1EA	С			apacitor (2 5 W V 10 μ	[C1]
	V C K Y T V 1 H F 2 2 3 Z A	Α			apacitor (5 O W V 0.022μF)	[C2]
	VCKYTV1EF104ZA	Α		C C	apacitor (25 W V 0.10 µF)	[C3]
	VCKYCY1EF104Z	AA		С	Capacitor (25WV 0.10µF)	[C4]
	VCKYCY1EF104Z	AA		С	Capacitor (25WV 0.10µF)	[C5]
	VCKYCY1EF104Z	AA		С	(Capacitor (25WV 0.10µF)	[C6]
	VCKYCY1HB102K	A A		С	Capacitor (50WV 1000PF)	[C7]
	VCEAPS336AF1C	A C		С	Capacitor (16WV 33µF)	[C8]
	VCEAPS105AF1H	ΑB		С	Capacitor (50WV 1µF)	[C9]
	VCKYCY1EF223Z	AA		С	Capacitor (25WV 0.022µF)	[C10]
	VCEAPS226AF1C	A C		С	Capacitor (16WV 22µF)	[C11]
13	VCEAPS226AF1C	A C		С	Capacitor (16WV 22μF)	[C12]
14	VCEAPS226AF1C	AC		С	Capacitor (16WV 22µF)	[C13]
15	VCKYTV1HB2-22K/	A A			apacitor (50WV 2200pF)	[C14]
1 6					Capacitor (5 <mark>0WV 2200pF)</mark>	[C15]
1	7				apacitor (5 0 W V 2200pF)	[C16]
1 8	8			C C	apacitor (50WV 2200pF)	[C17]
	VCKYTV1HB222K	AA	l	C	Capacitor (50WV 2200pF)	[C18]_
	VCKYTV1HF223Z	AA		С	Capacitor (50WV 0.022µF)	[C101]
	VCKYTV1EF104Z	AA		С	Capacitor (25WV 0.10µF)	[C102]
	VCKYTV1HB222K	AA		С	Capacitor (50WV 2200pF)	[C103]_
23	VCKYTV1HB222K	AA		С	Capacitor (50WV_2200pF)	[C104]
	VCCCTV1HH5R0J	AA		C	Capacitor (50WV 5pF)	[C105]
	VCKYTV1EF104Z	AA		С	Capacitor (25WV 0.10µF)	[C106]
	VCKYTV1EF104Z	A A		С	Capacitor (25WV 0.10µF)	[C107]
	VCKYTV1HB222K	AA	L	С	Capacitor (50WV 2200pF)	[C108]
	VCKYTV1HB222K	AA		С	Capacitor (50WV 2200pF)	[C109]
	VCCCTV1HH300J	AA		С	Capacitor (50WV 30PF)	[C110]
	VCCCTV1HH300J	AA	ļ	С	Capacitor (50WV 30PF)	[C111]
	VCKYTV1HB222K	AA		C	Capacitor (50WV 2200pF)	[C112]
	VCKYTV1HB222K	AA	L	С	Capacitor (50WV 2200pF)	[C113]
	VCKYTV1EF104Z	AA		С	Capacitor (25WV 0.10µF)	[C114]
	VCKYTV1EF104Z	AA	L	C_	Capacitor (25WV 0.10µF)	[C115]
	VCKYTV1HF223Z	AA		С	Capacitor (50WV 0.022µF)	[C116]
	VCKYTV1HF223Z	AA		С	Capacitor (50WV 0.022µF)	[C117]
	VCKYTV1EF104Z	A A		C	Capacitor (25WV 0.10µF)	[C118]
	VCKYTV1HF223Z	AA	L	С	Capacitor (50WV 0.022µF)	[C119]
	VCKYTV1EF104Z	AA		С	Capacitor (25WV 0.10µF)	[C120]
40	VCKYTV1HF223Z	AA		С	Capacitor (50WV 0.022µF)	[C121]

NO.	PARTS CODE	PRICE	NEW MARK	PART	DESCRIPTION	
41	VCKYTV1HB222K	AA	MAKA	RANK	Capacitor (50WV 2200pF)	[C122]
42	VCCCTV1HH330J	AA	<u> </u>	Ċ	Capacitor (50WV 33pF)	[C122]
43	VCKYTQ1CF105Z	ΑE		С	Capacitor (16WV 1.0µF)	[C124]
	VCKYTV1HB222K VCKYTQ1CF105Z	AA	<u> </u>	C	Capacitor (50WV 2200pF)	[C125]
46	VCKYTV1EF104Z	AA		C	Capacitor (16WV 1.0µF) Capacitor (25WV 0.10µF)	[C126]
47	VCKYTQ1EF104Z	AA		C	Capacitor (25WV 0.10µF)	[C127] [C128]
48	VCCCTV1HH330J	AA		С	Capacitor (50WV 33pF)	[C129]
49	VCSAPJ1AA475M	AB		C	Capacitor (10WV 4.7µF)	[C130]
51	V C K Y T V 1 E F 1 0 4 Z V C K Y T Q 1 C F 1 0 5 Z	A A AE		C	Capacitor (25WV 0.10µF)	[C131]
52	VCCSTV1HL102J	A A		<u>с</u>	Capacitor_(16WV_1.0μF)_ Capacitor (50WV 1000PF)	[C132]
53	VCCSTV1HL102J	AA		C	Capacitor (50WV 1000PF)	[C133] [C134]
54	VCKYTV1EF104Z	AA		С	Capacitor (25WV 0.10µF)	[C135]
55	V C K Y T V 1 E F 1 0 4 Z V C K Y T V 1 E F 1 0 4 Z	AA		C	Capacitor (25WV 0.10µF)	[C136]
57 V	V C C C T V 1 H H 2 2 1 J A	A A		C	Capacitor (25WV 0.10µF) Capacitor (50WV 220PF)	[Ĉ[37]
58	VCCCTV 1HH221JA	A		C	Capacitor (50WV 220FF)	[C138] [C139]
5 9	VCKYTV 1 H B 2 2 2 K A	Α		C	Capacitor (50HIV 2200pF)	[C140]
	VCKYTV 1 H B 2 2 2 K A VCKYTV 1 H B 2 2 2 K A	A		С	C a acitor 50WV220σβr	[C141]
	VCKYTV 1 H F 2 2 3 Z A	A A		<u>C</u>	Capacitor (50WV 2200pF) Capacitor (50WV 6.022µF	[C142]
6 3	VCKYTV1HF223Z	А А			Capacitor (50WV 0.022μF)	[C143]
6 4	VCKYTV 1 H B 2 2 2 K I A	A A		C	Capacitor_(50V2200pF)	[C144] [C145]
6 5					Capacitor (50WV 0.022µF)	[C146]
6 7	VCKYTV 1 H F 2 2 3 ZT VCCCTV 1 H H 1 8 0 1	A A	1 .	C	Capacitor (50WV_0.02F) _y spacitor (50WV 18PF)	[C147]
68	VCCCTV1HH180		<u> </u>	C (C	Capacitor (50 W V 18PF)	[C148]
69	vCKYTV1HF223		Ti	С	Capacitor_(50\0.022\(\mu\)F)	[C149] [C150]
70	TCKYTV1HR2224K	. , 🔼 T	<u>, ,1</u>		Caracitor 50WV 2200PF)	C150]_ C152]_ [C153]
7 2	VCCCTVIHHIOIJ		1 !		Capacitor (25WV 0.10µF) Capacitor (50WV 100PF)	
	VCKYTV 1 E F 1 0 4 Z I A		<u> </u>	C C	epacitor $(25 \text{WV} 0.10 \mu\text{F})$	[C155] [C156]
7 4	V C KIEF104ZI	A A	i		Ωριπαςίτης Δ3ΕΨΨ 0.10μF)	[C150]
	VCKYTV1EF104Z VCKYTV1HB272K	AA			Capacitor (25WV O.AOµF)	[C158]
	VCKYTV1HB272K	A A			Capacitor (50WV 2700PF) Capacitor (25WV 0.10µF)	[C159]
78 \	VCKYTV1HB681K	AA			Capacitor (50WV 680PF)	[C160] [C161]
79 \	VCKYTV1EF104Z	AA			Capacitor (25WV 0.10µF)	[C162]
81 1	VCKYTV1HB472K VCCSTV1HL102J	AA		С	Capacitor (50WV 4700pF)	[C163]
82 \	VCKYTV1EF104Z	AA		C	Capacitor (50WV 1000PF) Capacitor (25WV 0.10µF)	[C164] [C165]
83 \	VCKYTV1EF104Z	AA		C	Capacitor (25WV 0.10µF)	[C166]
84 \	VCKYTV1EF104Z	AA		С	Capacitor (25WV 0.10µF)	[C167]
86 \	VCKYTV1EF104Z VCCCTV1HH101J	AA		C	Capacitor (25WV 0.10µF) Capacitor (50WV 100PF)	[C168]
87 C	QCNCM7014SC0G	AB			Connector (7pin)	[C172] [CNCCD]
88 (QCNCM2465SC3J	AF		С	Connector (30pin)	[CNDR]
89 (Q C N C M 2 4 3 6 S C 5 J Q C N C M 2 3 8 9 S C 2 B	AB			Connector (50pin)	[CNLIU]
	QCNCM7014SC0E	A E A B			Connector (22pin) Connector (5pin)	[CNPNL]
	QCNCM7014SC0F	AB			Connector (Spin)	[CNSNS] [CNTXM]
	VHD MN 1 0 / / - 1	A C			Diode (IMN10)	[D101]
94 V	VHD i MN 1 0 / / - 1 VHD R B 4 0 0 D / / - 1	A C			Diode (IMN10)	[D102]
	VHV i CPN 2 0 / / - 1	A C			Diode (RB400D) IC protector (ICP – N20)	[D103]
97 V	VHIR96SHF//-1	ВD			IC (R96SHF)	[F1] [iCi]
98 V	/ H i M 6 6 3 3 3 F D - 1	<u>50</u>		8	IC (M66333FP)	[102]
100 l v	/HILH5268T410 / H C	AS			IC (LH5268T410) IC (64180ZRS08)	[IC3]
101 V	H i 4 3 2 5 7 A G 1 0 L	ÂŶ	 +		IC (43257AG10L)	[IC4]
102 \	VH i F 2 5 5 0 1 1/-1	AU		В	IC (F255011)	[IC5] [IC7]
103 V	/ H i P S T 6 0 0 C M T 1 / H i 7 4 H C U 0 4 F - 1	AE		_B	IC (PST600CMT1)	[ic8]
105 V	/H 74 H C U U 4 F - 1	A C A X			IC (74HCU04F) IC (GM4256BSJ7)	[1C9]
106 ∨	H i HM 5 1 4 8 0 0 J 8	BG			IC (HM514800JB)	[IC10]
107 V	_H i L Z 9 5 G 3 8 / - 1 T	ΑX		B 1	C (LZ95G38)	[IC11] [IC12]_
	HILB1730//-1	A H			C (LB1730)	(iC13)
	/H : T L 0 8 4 C N / - F				(NJM318M) (TL084CN)	[IC101]
1 1 1	VHIMC14066BMF				(MC14066BMF)	[IC102] [IC103]
112 V	H MC 1 4 0 6 6 BM F A	D		В	IC (MC14066BMF)	[IC104]
113 \ 114 V	VH SN74 0 6 N S - 1 A	F C			C (SN7406NS)	[IC105]
115 V	HIMC14053BMF	AE	+		<u>IC (SN74HC04NS)</u> IC (MC14053BN	[IC106]
116 V	H i N J M 4 5 5 8 F - 1	A D			C (NJM4558MF)	[IC107] [IC108]
1 1 7	TVH i M C 7 4 H C 1 4 M F			B 1	C (MC74HC14MF)	[IC108]
	H TSN 7 4 H C 0 4 N S A H I M C 7 4 H C 3 2 M F A	C	-		C (SN74HC04NS)	[IC110]_
1 2 0	VHINJU6355E-1				(MC74HC32MF) (NJU6355E)	[IC111]
		<u> </u>		_ [.5	<u> </u>	[ICI_12]_

NO. PARTS CODE		NEW PART MARK RANK	DESCRIPTION	
1 2 1 VH i LM 3 9 3 P S	/- \$ A C	В	IC (LM393PS)	[IC113]
1 2 2 Q S O C Z 2 O 5 1 S 1 2 3 V H i 2 7 O 2 O F		C	IC socket (32pin)	[IC114]
1 2 3 VH 1 2 7 0 2 0 F 124 VRS - TF 2 B D 0 0 0		N B	IC (27020FCB0B)	[IC114]
125 V R S - T P 2 B D 0 0 0	J AA	<u>C</u>	Resistor (1/8W 0Ω ±5%)	[L101]
126 VRS-TP2BD000	JAA	C	Resistor (1/8W 0Ω ±5%)	[L102]
127 V R S - T P 2 B D 0 0 0	J ÂÂ	C	Resistor (1/8W 0Ω ±5%) Resistor (1/8W 0Ω ±5%)	[L103]
128 VRS-TP2BD000	JAA	T C	Resistor (1/8W O Ω ±5%)	[L104]
129 V S 2 S C 2 4 1 2 K S -	-1 AB	В	Transistor (2SC2412KS)	[L105]
130 V S 2 S C 2 4 1 2 K S -	1 AB	В	Transistor (2SC2412KS)	[Q101] [Q102]
131 V S D T C 1 1 4 E K / -	-1 AB	В	Transistor (DTC114EK)	[0102]
132 VSDTC114EK/-	1 AB	В	Transistor (DTC114EK)	[Q104]
133 VSDTA114EK/-	1 AB	8	Transistor (DTA114EK)	[Q105]
134 VRS-TS2AD102	JAA	C	Resistor (1/10W 1.0KΩ ±5%)	[R1]
135 V R S - T S 2 A D 3 3 0	JAA	C	Resistor (1/10W 33\Omega \pm 5\%)	[R2]
136 VRS-TS2AD102 137 VRS-TS2AD102	JAA	C	Resistor (1/10W 1.0K Ω ±5%)	[R3]
138 V R S - C Y 1 J D 2 2 1	JAA	C	Resistor (1/10W 1.0KΩ ±5%)	[R4]
139 V R S - C Y 1 J D 2 2 2	J AA	- <u>c</u>	Resistor (1/16W 220Ω ±5%)	[R6]
140 V R S - C Y 1 J D 2 2 2	JAA	C	Resistor (1/16W 2.2KΩ ±5%) Resistor (1/16W 2.2KΩ ±5%)	[R7]
141 VRS-CY1JD163	JAA	č	Resistor (1/16W 16K Ω ±5%)	[R8]
142 VRS-CY1JD222	JAA	Č	Resistor (1/16W 2.2K $\Omega \pm 5\%$)	[R9]
143 V R S - C Y 1 J D 1 0 3	J AA	Č	Resistor (1/16W 10K Ω ±5%)	[R10] [R11]
144 VRS-CY1JD470	JAA	Č	Resistor (1/16W 47 Ω ±5%)	[R12]
145 V R S - C Y 1 J D 2 2 4	J AA	C	Resistor (1/16W 220KΩ ±5%)	[R13]
146 VRD-HT2HY331		С	Resistor (1/4W 330Ω ±5%)	[R14]
147 V R D - H T 2 H Y 3 3 1		С	Resistor (1/4W 330Ω ±5%)	[R15]
148 V R.S. T S 2 A D 2 7 1	JAA	C	Resistor (1/10W 270Ω ±5%)	[R16]
149 V R S - T S 2 A D 2 7 1 150 V R S - T S 2 A D 2 7 1	JAA	c	Resistor (1/10W 270Ω ±5%)	[R17]
150 V R S - T S 2 A D 2 7 1 151 V R S - T S 2 A D 2 7 1	JAA	C	Resistor (1/10W 270Ω ±5%)	[R18]
151 V R S - T S 2 A D 2 7 1	JAAI	<u> </u>	Periotes (1/10W 270Ω ±5%)	[R19]
	7 1	<u>C</u>	Resistor 0/10W 270Ω ±5%) Resistor 0/10W 270Ω ±5%)	[R20]
154 V R S - T S 2 A D 2 7 1	JAA	' l' C	Resistor (1/10W 270Ω ±5%)	[R21]
155 V R S - T S 2 A D 1 0 0	JAA	l č	Resistor (1/10W 270Ω ±5%)	[R22]
156 V R S - T S 2 A D 3 3 0	JAA	č	Resistor (1/10W 33 Ω ±5%)	[R101]
157 V R S - T S 2 A D 1 8 2	J AA	- c	Resistor (1/10W 1.8KΩ ±5%)	[R102] [R103]
158 VRS-TS2AD000	J AA	C	Resistor (1/10W 0 Ω ±5%)	[R104]
159 V R S - T S 2 A D 1 0 3	J AA	С	Resistor (1/10W 10KΩ ±5%)	[R106]
160 VRS-TS2AD000	J AA	С	Resistor (1/10W 0Ω ±5%)	[R107]
161 V R S - T S 2 A D 0 0 0	J AA	С	Resistor (1/10W 0 Ω ±5%)	[R108]
162 VRS-TS2AD472	J AA	C	Resistor (1/10W 4.7KΩ ±5%)	[R110]
163 VRS-TS2AD103		C	Resistor (1/10W 10KΩ ±5%)	[R111]
165 VRS-TS2AD102		<u> </u>	Resistor (1/10W 1.0KΩ ±5%)	[R112]
		<u>C</u> C	Resistor (1/10W 5.6KΩ ±5%)	[R113]
<u>166</u> V R S — T S 2 A D 3 3 0 1 6 7 V R S - T S 2 A D 4 7 2		l C	Resistor $1/10W$ 33 Ω $\pm 5\%$) Resistor (1/10W 4.7K Ω $\pm 5\%$)	[R114]
168 V R S - T S 2 A D 4 7 2		C	Resistor (1/10W 4.7KΩ ±5%) / Resistor (1/10W 4.7KΩ ±5%)	[R115]
169 V R S - T S 2 A D 4 7 2	J AA	i č	Resistor (1/10W 4.7KD ±5%)	[R116]
170 VRS-TS2AD103	JAA	Č	Resistor (1/10W 10K Ω ±5%)	[R117]
171 VRS-TS2AD101		C	Resistor (1/10W 100 Ω ±5%)	[R119] [R120]
172 V R S - T S 2 A D 4 7 2	J AA	Ç	Resistor (1/10W 4.7KΩ ±5%)	[R121]
173 V R S - T S 2 A D 0 0 0	J AA	C	Resistor (1/10W 0Ω ±5%)	[R122]
174 V R S - T S 2 A D 8 2 2	J AA	С	Resistor (1/10W 8.2KΩ ±5%)	[R123]
175 VRS-TS2AD392		С	Resistor (1/10W 3.9K Ω ±5%)	[R124]
176 V R S - T S 2 A D 2 0 2	J AA	<u>C</u>	Resistor (1/10W 2KΩ ±5%)	[R125]
177 VRS-TS2AD222	J- AA	c	Resistor (1/10W 2.2KΩ ±5%)	[R126]
178 V R S - T S 2 A D 2 2 2 179 V R S - T S 2 A D 1 0 2	JAA	<u>c</u>	Resistor (1/10W 2.2KΩ ±5%)	[R127]
180 VRS-TS2AD102	JAA	<u> </u>	Resistor (1/10W 1.0K Ω ±5%)	[R128]
181 V R S - T S 2 A D 4 7 2	J AA	- C	Resistor (1/10W 2.2KΩ ±5%)	[R129]
182 V R S - T S 2 A D 5 6 1		C	Resistor (1/10W 4.7K Ω ±5%) Resistor (1/10W 560 Ω ±5%)	[R130]
183 V R S - T S 2 A D 4 7 2		- C	Resistor (1/10W 560H ±5%) Resistor (1/10W 4.7KΩ ±5%)	[R131]
184 V R S - T S 2 A D 2 2 2		, č	Resistor (1/10W 4.7KII ±5%) Resistor (1/10W 2.2KΩ ±5%)	[R132] [R133]
	J AA	Č	Resistor (1/10W $2K\Omega \pm 5\%$)	[R134]
<u>185 V</u> RS-TS2AD202	JAA	C	Resistor (1/10W 100 Ω ±5%)	[R135]
186 V R S - T S 2 A D 1 0 1			Resistor (1/10W 220Ω ±5%)	[R137]
186 VRS-TS2AD101 187 VRS-TS2AD221	J AA	С		
186 VRS-TS2AD101 187 VRS-TS2AD221 188 VRS-TS2AD391	J AA	С	Resistor (1/10W 390Ω ±5%)	[R138]
186 VRS-TS2AD101 187 VRS-TS2AD221 188 VRS-TS2AD391 189 VRS-TS2AD391	J AA J AA	C C	Resistor (1/10W 1.0KΩ ±5%)	[R138] [R139]
186 VRS-TS2AD101 187 VRS-TS2AD221 188 VRS-TS2AD391 189 VRS-TS2AD102 190 VRS-TS2AD822	J A A J A A J A A	C C C	Resistor (1/10W 1.0KΩ ±5%) Resistor (1/10W 8.2KΩ ±5%)	
186 VRS-TS2AD101 187 VRS-TS2AD221 188 VRS-TS2AD391 189 VRS-TS2AD391 190 VRS-TS2AD822 191 VRS-TS2AD822	J A A J A A J A A J A A J A A J	C C C	Resistor (1/10W 1.0KΩ ±5%) Resistor (1/10W 8.2KΩ ±5%) Resistor (1/10W 2.2KΩ ±5%)	[R139]
186 VRS-TS2AD101 187 VRS-TS2AD221 188 VRS-TS2AD391 189 VRS-TS2AD391 190 VRS-TS2AD32 191 VRS-TS2AD222 192 VRS-TS2AD471	J A A J A A J A A J A A J A A J A A J A A	C C C C	Resistor (1/10W 1.0KΩ ±5%) Resistor (1/10W 8.2KΩ ±5%) Resistor (1/10W 2.2KΩ ±5%) Resistor (1/10W 470Ω ±5%)	[R139] [R140] [R141] [R142]
186	J A A J A A J A A J A A J A A J A A J A A J A A	C C C C	Resistor (1/10W 1.0KΩ ±5%) Resistor (1/10W 8.2KΩ ±5%) Resistor (1/10W 2.2KΩ ±5%) Resistor (1/10W 470Ω ±5%) Resistor (1/10W 10KΩ ±5%)	[R139] [R140] [R141] [R142] [R143]
186 V R S - T S 2 A D 1 0 1 187 V R S - T S 2 A D 2 2 1 188 V R S - T S 2 A D 3 9 1 189 V R S - T S 2 A D 1 0 2 190 V R S - T S 2 A D 8 2 2 191 V R S - T S 2 A D 2 2 2 191 V R S - T S 2 A D 4 7 1 193 V R S - T S 2 A D 1 0 3 194 V R S - T S 2 A D 2 0 1	J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A	C C C C C C C C C C C C C C C C C C C	Resistor (1/10W 1.0ΚΩ ±5%) Resistor (1/10W 8.2ΚΩ ±5%) Resistor (1/10W 2.2ΚΩ ±5%) Resistor (1/10W 470Ω ±5%) Resistor (1/10W 10ΚΩ ±5%) Resistor (1/10W 200Ω ±5%)	[R139] [R140] [R141] [R142] [R143] [R144]
186	J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A	C C C C C C C C C C C C C C C C C C C	Resistor (1/10W 1.0ΚΩ ±5%) Resistor (1/10W 8.2ΚΩ ±5%) Resistor (1/10W 2.2ΚΩ ±5%) Resistor (1/10W 470Ω ±5%) Resistor (1/10W 10ΚΩ ±5%) Resistor (1/10W 200Ω ±5%) Resistor (1/10W 0Ω ±5%)	[R139] [R140] [R141] [R142] [R143] [R144] [R146]
186 V R S - T S 2 A D 1 0 1 187 V R S - T S 2 A D 2 2 1 188 V R S - T S 2 A D 3 9 1 189 V R S - T S 2 A D 8 2 2 190 V R S - T S 2 A D 8 2 2 2 191 V R S - T S 2 A D 4 7 1 193 V R S - T S 2 A D 4 7 1 193 V R S - T S 2 A D 0 0 0 0 196 V L - T S 2 A D 0 0 0 196 V L - T S 2 A D 1 0 3	J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A	C C C C C C C C C C C C C C C C C C C	Resistor (1/10W 1.0ΚΩ ±5%) Resistor (1/10W 8.2ΚΩ ±5%) Resistor (1/10W 2.2ΚΩ ±5%) Resistor (1/10W 470Ω ±5%) Resistor (1/10W 10ΚΩ ±5%) Resistor (1/10W 200Ω ±5%) Resistor (1/10W 0Ω ±5%) Resistor (1/10W 10ΚΩ ±5%)	[R139] [R140] [R141] [R142] [R143] [R144] [R146] [R148]
186	J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A	C C C C C C C C C C C C C C C C C C C	Resistor (1/10W 1.0KΩ ±5%) Resistor (1/10W 8.2KΩ ±5%) Resistor (1/10W 2.2KΩ ±5%) Resistor (1/10W 470Ω ±5%) Resistor (1/10W 10KΩ ±5%) Resistor (1/10W 200Ω ±5%) Resistor (1/10W 10KΩ ±5%)	[R139] [R140] [R141] [R142] [R143] [R144] [R146] [R148] [R149]
186	J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A J A A	C C C C C C C C C C C C C C C C C C C	Resistor (1/10W 1.0ΚΩ ±5%) Resistor (1/10W 8.2ΚΩ ±5%) Resistor (1/10W 2.2ΚΩ ±5%) Resistor (1/10W 470Ω ±5%) Resistor (1/10W 10ΚΩ ±5%) Resistor (1/10W 200Ω ±5%) Resistor (1/10W 0Ω ±5%) Resistor (1/10W 10ΚΩ ±5%)	[R139] [R140] [R141] [R142] [R143] [R144] [R146] [R146]

NO.	PARTS CODE	PP/A.N.Y	MARK	RARK	DESCRIPTION	
201	VRS-TS2AD103J	AA		Ċ	Resistor (1/10W_10KΩ ±5%)	[R153]
202	VRS-TP2BD101J	АА		С	Resistor (1/8W 100Ω ±5%)	[R154]
203	VRS-TS2AD103J	A A	ļ	C	Resistor (1/10W 10KΩ ±5%)	[R155]
204	VRS-TP2BD000J VRSTS2AD4752F	AA		C	Resistor (1/8W 0Ω ±5%)	[R156]
206	VRS-TS2AD103JA	A A A		Ċ	Resistor (1/10W 47.5KΩ ±1%)	[R157]
207	VRS-TS2AD100JA	A		С	Resistor (1/10W 10K Ω ±5%) Resistor (1/10W 10 Ω ±5%)	[R158]
208	VRS-TS2AD100JA	A		C	Resistor (1/10W 10Ω ± 5%)	[R159] [R160]
	VRS-TS2AD100JA	Α		С	Resistor (1/10W 10Ω ± 5%)	[R161]
210	VRS-TS2AD100JA	Α		С	Resistor $(1/10W_10\Omega \pm 5\%)$	[R162]
211	V R S T S 2 A D 1 7 4 2 F A V R S T S 2 A D 8 6 6 2 F A	A		С	Resistor (1/10W 17.4KO ±1%)	[R163]
	VRS-TS2AD8662FA	A A		C	Resistor (1/10W 86.6KΩ ±1%)	[R164]
214	VRSTS2AD8662F	AA		č	Resistor (1/10W 10 Ω ±5%) Resistor (1/10W 86.6K Ω ±1%)	[R165]
215	VRS-TS2AD302J	AA		č	Resistor (1/10W 3.0KΩ ±5%)	[R166] [R167]
216	VRS-TS2AD103J	АА		C	Resistor (1/10W 10K Ω ±5%)	[R168]
	VRS-TS2AD103J	AA		С	Resistor (1/10W 10K Ω ±5%)	[R169]
218	VRSTS2AD1183F	AA		С	Resistor (1/10W 118K Ω ±1%)	[R170]
220	VRS-TS2AD103J VRS-TS2AD271J	AA		<u>c</u>	Resistor (1/10W 10KΩ ±5%)	[R171]
221	VRS-TS2AD271J	AA		C C	Resistor (1/10W 270Ω ±5%) Resistor (1/10W 270Ω ±5%)	[R172]
222	VRS-TS2AD103J	ÂÂ		c	Resistor (1/10W 2/0H ±5%) Resistor (1/10W 10ΚΩ ±5%)	[R173] [R174]
223	VRS-TS2AD271J	AA		č	Resistor (1/10W 270 Ω ±5%)	[R175]
224	VRS-TS2AD103J	AA		С	Resistor (1/10W 10K $\Omega \pm 5\%$)	[R176]
225	VRS-TS2AD000J	AA			Resistor (1/10W 0Ω ±5%)	[R177]
220	V R S - T S 2 A D 5 6 2 J V R S - T S 2 A D 1 0 5 J	AA			Resistor (1/10W 5.6KΩ ±5%)	[R178]
228	VRS-TS2AD105J VRS-TS2AD000J	AA		C	Resistor (1/10W 1MΩ ±5%)	[R179]
229	VRS-TS2AD562J	AA		C C	Resistor (1/10W $0\Omega \pm 5\%$) Resistor_(1/10W $5.6K\Omega \pm 5\%$)	[R180]
230	VRS-TS2AD332J	AA		Č	Resistor (1/10W 3.3KD ±5%)	[R181] [R182]
231	VRS-TS2AD000J	ΑА		C	Resistor (1/10W 0 Ω ±5%)	[R183]
232	VRS-TS2AD271J	AA		C R	esistor_(1/10W_270Ω ±5%)	[R184]
233	VRS-TS2AD27 L # VRS-TS2AD473 J	Δ.Δ.	- 1	<u> </u>	Resistor (1/10W 270Ω ±5%)	[R185]
235	VRS-TS2AD473J	AA		C	Resistor (1/10W 47K Ω ±5%) Resistor (1/10W 47K Ω ±5%)	[R186]
236	VRS-TS2AD103	AA		c	Resistor_(1/10W 4/KH ±5%)	[R187]
237	VRS-TS2AD271J	AA			Resistor (1/10W 270 Ω ±5%)	[R189] [R190]
238	VRS-TS2AD103J	AA		С	Resistor (1/10W 10K Ω ±5%)	[R191]
239	VRS-TS2AD103J	A A		С	Resistor (1/10W 10KΩ ±5%)	[R192]
240		AAA I			Resistor (1/10W 10KO ±5%)	[R193]
242	VRS-TS2AD103J	A^A		C C	Resistor (1/10W 10K Ω ±5%) Resistor (1/10W 820 Ω ±5%)	[R194]
243	VRS-TS2ADLOLL				Resistor (1/10W 100Ω ±5%)	[R195] [R196]
244	VRS-TS2AD103J	AA		Č	Resistor (1/10W 10K $\Omega \pm 5\%$)	[R197]
	VRS-TS2AD271J	AA		С	Resistor (1/10W 270 Ω ±5%)	[R198]
246	VRS-TS2AD103J	A A)			Resistor_(1/10W_10KO_±5%)	[R199]
247	V R S - T S 2 A D 1 0 3 J V R S - T S 2 A D 1 0 3 J	AA		C	Resistor (1/10W 10KΩ ±5%)	[R200]
	VRS-TS2AD103J	AA		C	Resistor (1/10W 10K Ω ±5%) Resistor (1/10W 10K Ω ±5%)	[R201]
250	VRS-TS2AD472J	AA		č	Resistor (1/10W 4.7K Ω ±5%)	[R202] [R203]
251	VRS-TS2AD103JAI	А			Resistor $(1/10W 10K\Omega \pm 5\%)$	[R205]
252 \	VRS-TS2AD103JA	Α		С	Resistor (1/10W 10KΩ ±5%)	[R206]
253 \	VRS-TS2AD103JA	A		С	Resistor (1/10W_10KΩ ±5%)	[R207]
255	V R S T S 2 A D 1 0 3 J A V R S T S 2 A D 1 0 <u>3</u> J A	A		C	Resistor (1/10W 10KΩ ±5%	[R208]
256	VRS-TS2AD103JA	A			Resistor (1/10W 10KΩ $\pm 5\%$) Resistor (1/10W 5.6KΩ $\pm 5\%$)	[R209]
257	/RS-TS2AD471-JA	Α			Resistor (1/10W 470 Ω ±5%)	[R210] [R211]
258	ZRS-TS2AD471JA	Α		С	Resistor (1/10W 470Ω ±5%)	JR2121
259	VRS-TS2AD471JA	Α,		.C-	Resistor (1/10W 470Ω ±5%)	[R213]
	VRS-TS2AD471J VRS-TS2AD271J	AA			Resistor (1/10W 4700 ±5%)	[R214]
	V R S - T S 2 A D 2 7 1 J	AA		C	Resistor (1/10W 270Ω ±5%) Resistor (1/10W 270Ω ±5%)	[R215]
263	TRS-TS2AD271J I	AA	 	<u> </u>	Resistor (1/10W 270\(\Omega \pm 5\%))	[R216] [R217]
264	RS-TS2AD4711	AA	l,		Resistor (1/10W 470 Ω ±5%)	[R218]
265	VRS-TS2AD271J	AΑ		С	Resistor (1/10W 270Ω ±5%)	[R219]
	VRS-TS2AD103J	AA			Resistor (1/10W 10K Ω ±5%)	[R220]
269	VRS-TS2AD103J VRS-TS2AD103J	AA		_ <u>C</u> _	Resistor (1/10W 10KΩ ±5%)	[R221]
	VRS-TS2AD103J	AA A A			Resistor_(1/10W_10KΩ ±5%) Resistor_(1/10W_10KΩ ±5%)	[R222]
270	VRS-TS2AD103J	ÂÂ		č	Resistor (1/10W 10KD \pm 5%)	[R223] [R224]
271	VRS-TS2AD330J	AA		C	Resistor (1/10W 33 Ω ±5%)	[R225]
272	V R S - T S 2 A D 3 3 0 J	AA		С	Resistor (1/10W 33 Ω ±5%)	[R226]
273	V R S - T S 2 A D 3 3 0 J	AA		C	Resistor (1/10W 33 Ω ±5%)	[R227]
275	V R S - T S 2 A D 3 3 0 J Y R S - T S 2 A D 3 3 0 J	AA		С	Resistor (1/10W 33Ω ±5%)	[R228]
276	RS-TS2AD330J	AA I	- 1.		Resistor (1/10W 330 ±5%))	[R229]
277	VRS-TS2AD330J	AA			Resistor (1/10W 33 Ω ±5%) Resistor (1/10W 33 Ω ±5%)	[R230] [R231]
278	VRS-TS2AD330J	AA			Resistor (1/10W 33 Ω ±5%)	[R232]
279	VRS-TS2AD330J	AA		С	Resistor (1/10W 33Ω ±5%)	[R233]
280	VRS-TS2AD330J	AA	1	C	Resistor (1/10W_33Q_±5%)	[R234]

ControlPWBunit

NO. PARTS (KAINN	NEW PA	ART NK DESCRIPTION	
281 V R S - T S 2 A	D100JA A	i li	C Resistor $(1/10W 10\Omega \pm 5\%)$	[R235]
282 V R S - T S 2 A	D100JA A	T I	C Resistor (1/10W 10Ω ±5%)	[R236]
283 V R S - T S 2 A	D1001 AA	ا ار	Resistanu(\(1) NW 1 NO. ±5%)	
284 VRS-TS2A	D100JA A	T C	Resistor (1/10W 10Ω ±5%)	[R237] [R238]
285 VRS-TS2A	D100JA A		C Resistor_(1/10W_10Ω_±5%	[R239]
286 VRS-15 ZA	D 2 2 3 J 1 A 1 A	` I	c Resistor (1/10W 22KΩ ±5%)	
287 VRS-TS2A	D103JA A	С	Resistor $(1/10W 10\Omega \pm 5\%)$	[R242]
288 VRS-TS2A	DUUUJ AA	C		[R243]
289 VRS-TP28		I C	Resistor $(1/8W 560\Omega \pm 5\%)$	CR2451
290 RCRSP 2 0 8	OSCZZ A F	B	Crystal (24.000.14KHz)	[X1]
291 RCRSZ700	8 S C Z Z A D		Crystal (16MHz)	[X2]_
292 RCRSP2 0 8	3 S C Z Z A E	1 8	Crystal (32.768KHz)	[X3]
293 R C R S Q 2 0 9	OSCZZIA D	С	Crystal (614.4MHz)	[X4]
294 TLABP 3 0 7 8		[Shading label (for EP-ROM)	• • • • • • • • • • • • • • • • • • • •
(Unit))	9.		
901 DCEKC78	OFSCZZ CA	N E	Control PWB unit	
	. 5.475			

7 Power supply PWB unit

		+	55105	NIE W	DADT		
	NO.	PARTS CODE		NEW		DESCRIPTION	
Δ.				MARK			
⚠		0CB829820363/B	С		В	Transformer (PT-P79-KTT)	
Δ	2	OCBUKZ 0582ZZ/	А Н		С	Filter (FU 105V0R4A203)	[L1.2]
	3	<u>0</u> CBPZZ0604ZZ/	A C	•	С	Jumping wire (IPS -3002-4)	[FB1]
	4		A C	i	C	Ferrite core (BL01RN1 – A62B1)	
	5	0 C B U C C 0 0 1 3 D Z /				C (UPC78N12H)	[FB2]
		0 C B U C C 0 0 1 0 F Z /					[IC1]_
				1 1		C (NJM79L12A)	[1C2]
		OCBUCB 0 1 1 2 A Z/	AK			IC (NJM7805FA)_	[IC3]]
Λ		OCBUAGO091AZ/	ΑQ		В	FET (2SK1601)	[01]
	9	0 C B U A C 0 0 5 6 B Z / A	D		В	Transistor (2SC2002-L)	[02]
	. 10	0 C B U A C 0 0 9 8 A Z / A	G		В	Transistor (2SC3518)	[03]
		OCBUACOLOADZZ,				Tanasistr (2SC945 ¬PA)	[04]
Λ	1					Vindae (D2SBA60)	-
		3 OCBUBCO220BZ/		''			[D1]
		VHD1N414,8//-1				Niode (1NU41)	[D2]
		0.001100				Diode (1SS55)	[D3]
	10	OCBUBC 0 2 2 1 A Z /	A C			Diode (ERA18-02)	[D4,5]
	16 (O C B U B CO 2 & O.B.Z.Z.,				Diode (11EQ04)	[D6]
	1	7 OCBUBB01874		A G I	В	Diode (D10LC20U)	[D7]
		B OCBUBDAC270D/	A C		I B Ze	ner diode (RD27ESAB3)	[ZD1]
	19	OCBUBDAA3ROC/	A C			effer diode (RD3.0EB2)	[ZD2]
1		OCBUBDAE 150B/	A D			ner dio de (RD15FB1)	[ZD3]
		OCBUBDAA6R2C/	AC			Zener diode (RD6.2EB2)	[ZD4]
		OCBUBDAE 3 O O D	ΑD			Zener diode (RD30FB3)	
Δ		0 C B U D C 0 1 3 9 A Z /	AN		В		[ZD5]
~						Photo coupler (PC113Y11)	[PC1]
		0 C B U E F C 5 6 4 B A /	AC		C	Metal film resistor (SFR25H560K(52))	[R1]
		OCBUEEC474BG/	A B		С	Carbon resistor (RDF1/2PS474J)	[R2]
- 1		OCBUEFER 3 3 CH/	AC		C	Metal film resistor (SPRX2R33J)	[R3]
- 1		OCBUEEB223BA/	A C			Resistor (R1/4PS223J)	[R4]
- 1		OCBUEEB564BA/	AA		С	Carbon resistor (R1/4PS564J)	[R5,6]
	29	OCBUEFE104CS/	AB		С	Metal film resistor (RSS2U104J)	[R7,17]
Į	30	OCBUEFE391CL/	A C		С	Metal film resistor (RSS2-L15-391J)	[R8]
	31	OCBUEEB471BA/=	A C		С	Carbon resistor (R1/4PS471J)	[R9]
ſ	32	OCBUEEB330BM/	AA		Č	Carbon resistor (F20R - 02J330)	[R10]
- 1		OCBUEFD561AU/	A C			Metal film resistor (RS1F561J)	[R11]
Ì		OCBUEEB222BA/	AC		č	Carbon resistor (1/4W 2.2K Ω ±5%)(R1/4PS222J)	
ł		OCBUEEB271BA/	AA		Č		[R12]
ŀ						Resistor (R1/4PS271J)	[R13]
ŀ		OCBUEEB822BA/	AA		C	Resistor (R1/4PS822J)	[R14]
- 1		OCBUEEB272BA/	AA			Carbon resistor (R1/4PS272J)	[R15]
!		OCBUFBA102DC/	A D			Variable resistor (KVSF637AB102)	[VR1]
4		OCBUGFZ224FY/	AG			Film capacitor (ECQ-U2A224MVA)	[C1,2]
Δ		OCBUGCZ222CK/			C Cer	amic_capacitor_(DE1410-15222M(CT4K-KD)	[C3,4]
Δ		OCBUGBQ820BR/	AΡ		C	Block capacitor (LGQ2G820MHSZ)	[C5]
[42	OCBUGCU103BC/	A D			Ceramic capacitor (DE1307-1E103Z1K)	[C6]
Γ	43	OCBUGCU221BR/	AC			Ceramic capacitor (DE0705R221K1K-MHR)	[C7]
ſ		OCBUGFF103ER/	A C			Capacitor (AMZF – 103K50)	[C8,10]
1		OCBUGFF683ER/	A D			Capacitor (AMZF - 683K50)	
ŀ		OCBUGFF102ER/	AC		č l	Capacitor (AMZF- 083K50) Capacitor (AMZF- 102K50)	[C9]
Δ		0 C B U G C Q 2 2 2 A Q /					[C11]
44				- -	<u> </u>	Ceramic capacitor (DE7 100 - 1F222MVA1 - KC)	[C12,13]
ŀ		OCBUGAD390PR/				apacitor (LXF25VB39(M)FM-5)	[C14,15]
- 1		OCBUGAD100HD/	A C			Capacitor (UVZ1E100MDH1AA)	[C16,17]
Ļ		0 C B U G A C 1 2 2 G K /	AG			Capacitor (UPL1C122MRH1AA)	[C18]
		OCBUGAC221HD/	A C		C	Capacitor (UVZ1C221MEH1AA)	[C19]
L		OCBUGCS222AP/	A C		С	Ceramic capacitor (DD08-63E222P500)	[C20]
	53	OCBUGAE 1 2 2 NS/	АН			Capacitor (LXF35VB1200(M)MC - 12.5)	[C21]
Γ		OCBUGAE221HD/	A D			Capacitor (UVZ1V221MPH1AA)	[C23]
- 1		OCBUGCF104DS/	A C			Ceramic capacitor (DD308 = 63F104Z50)	[C24]
Δ		OCBUERALE 471/	AF			Varistor (ENC471D - 07A)	[V1]
L						Tallotte (Chotte of h)	[V1]

PowersupplyPWBunit

	NO.	PARTS CODE	PRICE	NEW MARK	PART RANK	DESCRIPTION	
Δ		0 C B U Z Z O 1 0 0 Z Z /	АН		В	Varistor (AG-10PC702R-L3N)	[V2]
Δ		OCBPJCZZO037/	AG		Α	Current fuse (19181 1.25A)	[F1,2]
Δ	59	0 C B P J T 0 1 1 5 Z Z /	AF		Α	Thermal cutoff (U22 (115°C))	[F3]
4		0 C B U D Z 0 0 5 2 Z Z /	AG		В	Thermistor (M16007C)	[TH1]
4			A C		l c	Connector (B2P3-VH)	[CN1]
Ļ		OCBPCZ0161ZZ/	AF		С	Connector (09R - FJ)	[CN2]
L	63	0 C B P C Z O 1 6 0 Z Z /	ΑE		С	Connector (M1698(MEP1698))	[CN3 1]
		0 C B P Z Z O 7 3 9 Z Z /	ΑE			Bush (M1773(MOL1773))	[CN3 2]
Λ		0 C B P F Z 0 2 4 2 Z Z / A			В	Switch (SJ-W2R4A-30BB)	[SW1]
1		0 C B L R H 0 3 0 8 Z Q / A			С	Heat sink (D2157 - 5001B EZS)	[MTI]
L		OCBLRSO 103ZZ/A			С	Supporter (SUP- 103 SUS)	[MT2]
		OCBLRSO 101ZZ/A			С	Supporter (SUP- 101 SUS)	[MT3]
	6.9	0 C B M R Z C J R 7_7_/	A F			Radiation.cap (45T - T0 - 220 - 01220)	[RA1]
	7 0	0 C B M R S 0 0 2 9 Z ;			I C	Radiation sheet (30T-55-24-A)	[RA2]
	7	1 0 C B F B Z 0 0 9 8 Z Z /	/ A C		l c	Terminal (TM - 12)	[01]
		(Unit)					
Δ	901	RDENT2074SCZZ I	3 N	.	E Pov	wer supply PWB unit	<u> </u>
		f	1			T	
i		1	1	1			

8 TEL-Liu PWB unit

NO.	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION	
	VCQYNA1HM224K	A C		С	Capacitor (50WV 0.22µF)	[C1]
	VCEAEA0JW107M	AA		C	Capacitor (6.3WV 100µF)	[C2]
	VCEAEA0JW107MA	Α		C	Capacitor (6.3WV 100µF)	[C3]
	VCEAL_1HW475M	AA		t	Capacitor 50WV 4 7F)	[C4]
5	VCEAEA1AW226M	AA		С	Capacitor (10WV 22 _µ F)	[C5]
6	VCEAEA1HW474M	AA		С	Capacitor (50WV 0.47µF)	[C6]
	VCEAEU1HW105M	AA		С	Capacitor (50WV 1.0µF)	[C7]
	VCEAEA0JW107M	AA		C	Capacitor (6.3WV 100µF)	[C8]
<u> </u>	VCEAEAIVW226M_	AA		С	Capacitor (35WV 22µF)	[C9]
11	VCEAEQ1HB333K	AA AA		C	Capacitor_(50WV_0.033µF)	[C11]
L		<u> </u>		l c	Capacitor 50WV 3.3µF)	[C12]
	VCKYTV1HB222K	AA		С	Capacitor (50WV 2200PF)	[C13]
13	VCKYTV1HB222K	AA		С	Capacitor (50WV 2200PF)	[C14]
	VCKYTV1HB332K	AA		С	Capacitor (50WV 3300PF)	[C15]
	VCKYTQ1HB472KA	Α	1	C	Capacitor (50WV 4700PF)	[C16]
	VCKYTQ1HB104KA	В		С	Capacitor (50WV 0.10µF)	[C19]
	VCKYTV1HB471KA	Α		С	Capacitor (50WV 47OPF)	[C21]
	VCKYTV1HB153KA	Α		С	Capacitor (50WV 0.015µF)	[C22]
19	VCKYTQ1HB104KA	В		С	Capacitor (50WV 0.10µF)	[C24]
20	VCKYTV1HB473KA	Α		С	Capacitor (50WV 0.047 µF)	[C25]
21	VCKYTV1HB392KA	Α		С	Capacitor (50WV 3900PF)	[C26]
22	VCKYTQ1HB563K	AA		<u> </u>	Capacitor (50WV 0.056µF)	[C27]
23	VCKYTV1HB223K	AA		С	Capacitor (50WV 0.022µF)	[C30]
	VCKYTQ1HB563KA	Α		С	Capacitor (50WV 0.056µF)	[C31]
25	QCNCM2476SC2JA	K		С	Connector (20pin)	[CNHIC]
26	VHDDAN 2 1 2 K/- 1 A	С		В	Diode (DAN212K)	[DA1]
27	VHDDAN212K/-1A	С		В	Diode (DAN212K)	[DA2]
28	VH TA 3 1 0 6 5 A - 1 A	K		В	IC (TA31065A)	[IC1]
29	VRS-TS2AD000J	A A		С	Resistor (1/10W 0 Ω ±5%)	[JP6]
	VSBS108///-1	ΑE		В	Transistor (BS108)	[01]
	V S 2 S A 1 7 2 7 // - 1	ΑE		В	Transistor (2SA1727)	[Q2]
	V S 2 S C 4 0 6 1 K / -1	A C		B	Transistor (2SC4061K)	[Q3]
	V S 2 S C 2 4 1 2 K R - 1	A D		В	Transistor (2SC2412KR)	[Q4]
	VS2SA1727//-1	ΑE		В	Transistor (2SA1727)	[Q5]
	V S 2 S A 1 0 3 7 K R - 1	AB	,	В	Transistor_(2SA1037KR)	[Q6]
	V S 2 S C 2 4 1 2 K R - 1	\ D		В	Transistor (2SC2412KR)	<u>[37]</u>
3		A D		В	Transistor (2SC2412KR)	[Q8]
3	8 VS2SC4061K/-1			В	Transistor (2SC4061K)	[69]
3 9				С	Resistor (1W 1500 ±5%)	[R1]
	VRS-TS2AD103J	AA		С	Resistor (1/10W 10KΩ ±5%)	[R3]
	VRS-TS2AD204J	AA		С	Resistor (1/10W 200KΩ ±5%)	[R4]
42	VRS-TS2AD562J	AA		С	Resistor (1/10W 5.6K Ω ±5%)	[R5]
43	VRS-TS2AD222J	AA		С	Resistor (1/10W 2.2Kn ±5%)	[R6]
44	VRS-TS2AD433J	AA		С	Resistor (1/10W 43KΩ ±5%)	[R7]
45	VRS-TS2AD751J	AA		С	Resistor (1/10W 750Ω ±5%)	[R8]
46	VRS-TS2AD183J	AA		<u>C</u>	Resistor (1/10W 18KΩ ±5%)	[R9]
	VRD-HT2HY150J	AA		С	Resistor (1/2W 15Ω ±5%)	[R10]
	VRS-TS2AD514J	A G		С	Resistor (1/10W 510K0 ±5%)	[R11]
	V R S - T S 2 A D 2 2 3 J	AA		<u>C</u>	Resistor (1/10W 22KΩ ±5%)	[R12]
	V R S - T S 2 A D 2 2 3 J	AA		<u> </u>	Resistor (1/10W 22K Ω ±5%)	(R13)
	VRS-TS2AD202J	AA		C	Resistor (1/10W 2KΩ ±5%)	[R15]
52	V R S - T S 2 A D 9 1 2 J	AA		C	Resistor (1/10W 9.1KΩ ±5%)	[R17]
53	VRS-TS2AD105J	AA		С	Resistor (1/10W 1.0MΩ ±5%)	[R21]
	V R S - T S 2 A D 2 2 5 J	AA		С	Resistor (1/10W 2.2MΩ ±5%)	[HZZ]
	VRS-TS2AD105J	l A A	1	С	Resistor (1/10W 1.0MΩ ±5%)	[R23]

8 TEL-Liu PWB unit

	NO.	PARTS CODE	PRICE RANK		PART RANK	DESCRIPTION	
	55 V	/ R S - T S 2 A D 1 0 2 J / R S - T S 2 A D 2 2 3 J	AA		C	Resistor (1/10W 1.0KΩ ±5%)	[R24]
	58 I v	/RS-TS2AD2221	AA			Resistor $(1/10W 22K\Omega \pm 5\%)$ Resistor $(1/10W 2.2K\Omega \pm 5\%)$	[R25]
	59 V	/RS-TS2AD221J	AA		C	Resistor (1/10W 220 $\Omega \pm 5\%$)	[R26]
	60 V	/RS-TS2AD511J	АА		С	Resistor (1/10W 510 Ω ±5%)	[R27] [R28]
	61 V	/RS-TS2AD272J /RS-TS2AD821J	A A	-	С	Resistor (1/10W 2.7KΩ ±5%)	[R29]
	63 V	/RS-TS2AD244J	A A A A		C	Resistor (1/10W 820Ω ±5%)	[R30]
	64 V	/RS-TS2AD514J	AG		C	Resistor (1/10W 240KΩ ±5%) Resistor (1/10W 510KΩ ±5%)	[R31]
	65 V	RS-TS2AD514J	AG	_		Resistor $(1/10W_510K\Omega_\pm 5\%)$	[R32] [R33]
	66 V	RS-TS2AD204J	. A		С	Resistor $(1/10W_200K\Omega_{\pm}5\%)$	[R34]
	68 V	/RS-TS2AD103JA /RS-TS2AD102J			C	Resistor (1/10W_10KΩ_±5%)	[R35]
	69 V	RS-TS2AD752J	AA		,C	Resistor (1/10W 1.0KΩ ±5%) Resistor (1/10W 7.5KΩ ±5%)	[R36]
	70 V	RS-TS2AD820J	AA		C	Resistor (1/10W 7.5KH ±5%)	[R37] [R38]
	71 V	RS-TS2AD103J	.AA.		_ C	Resistor (1/10W 10K $\Omega \pm 5\%$)	[R39]
	72 V	RS-TS2AD103J	A A		C	Resistor (1/10W 10K Ω ±5%)	[R40]
		RS-TS2AD105J RS-TS2AD104J	AA AA		C	Resistor (1/10W_1.0M Ω ±5%)	[R41]
	75 %	វា S - T S 2 A D 7 5 1 J	_^^	AA		Resistor 1/10W 100KΩ ±5%) Resistor (1/10W 750Ω ±59	[R42]
	76 ∤∜	TRS-TS2AD151J	ÂA		С	Resistor (1/10W 150 Ω ±5%)	[R43] [R44]
	77 V	RS-TS2AD302J	AA		<u> </u>	Resistor (1/10W 3K Ω ±5%)	[R45]
ŀ	78 V	RS-TS2AD303J	_ A A		С	Resistor (1/10W 30K $\Omega \pm 5\%$)	[R46]
ł	80 LA	RS-TS2AD102J RS-TS2AD331J	A A		C	Resistor (1/10W 1.0KΩ ±5%)	[R47]
	81 V	RS-TS2AD2221	AA			Resistor (1/10W 330Ω \pm 5%) Resistor (1/10W 2.2KΩ \pm 5%)	[R48]
Ì	82 V	RS-TS2AD104J	AA			Resistor (1/10W $\frac{2.2KH}{1.5\%}$) Resistor (1/10W $\frac{100K\Omega}{1.25\%}$)	[R49]
	83 V	RS-TS2AD302J	AA		С	Resistor (1/10W 3K Ω ±5%)	[R52] [R53]
I		RS-TS2AD102J	AA			Resistor_(1/10W_1.0KΩ ±5%)	[R54]
ŀ		RS-TS2AD222J HDRB421D//-1	AA		С	Resistor_(1/10W_2.2 ±5%)	[R55]
ł		HDRB421D//-1	A C			Diode (RB421D)	[SD1]
İ		HEMTZ2ROA/-1	AA			Diode (RB421D) Zener diode (MTZ2ROA)	[SD2]
	89 V	HEMTZ2R0A/-1	AA			Zener diode (MTZ2ROA)	[ZD2] [ZD3]
ŀ	90 V	HEMTZ5R6B/-1	AB		<u> </u>	Zener_diode_(MTZ5R6B)	[ZD4]
7		HE1ZC15///-1 HV3P10P1//-1	AC	}	B	Zener atotte (12C15)	[ZD5]
4	202 0	TANZ2042SCZZ	A M A B		B C	Varistor (3P – 10) Terminal (M1902 – A)	[AR1]
Ì	203 R	ALMB2007SCZZ	AG			Buzzer (KBT – 33SB – 2T – 2)	[ARG]
	204 V	CQYNAIHM333K	AA			Capacitor (50WV OS)33µF)	[BZ1] [C3]
-	205 V	CEAEALAW227M	AR. I		C	Capacitor (10WV 220µF)	[C4]
ŀ	206 V	CEAEU1HW105M CEAEA1AW107M	AA			Capacitor (50WV 1.0µF)	[C7]
ŀ	208 V	CEAEA1HW334M	AB			Capacitor (10WV 100µF) Capacitor (50WV 0.33µF)	[C8]
Į	209 V	CQYNA1HM333K	AA			Capacitor (50WV 0.033µF)	[C9] [C10]
	210 V	CQYNU1HM334K	A D			Capacitor_(50WV_0.33µF)	[C11]
ŀ	211 V	CQYNU1HM334K	AD		C	Capacitor (50WV 0.33µF)	[C12]
ŀ	213 V	CEAEA1AW476M CEAEA1EW476M	A B		C	Capacitor (10WV 47µF)	[C16]
t	214 V	CEAEA1EW476M	AB		c	Capacitor (25WV 47µF) Capacitor (25WV 47µF)	[C17]
E	215 V	CEAEUIEW106M	AA			Capacitor (25WV 10µF)	[C18] [C19]
ŀ	216 V	CEAEU1HW105M	AA		C	Capacitor (50WV 1.0µF)	[C20]
ŀ	217 V	CFYJU2EA474K	A D			Capacitor (250WV 0.47µF)	[C21]
1	219 V	CFYJU2EA474K CEAEA1HW106A4	A D			Capacitor (250WV 0.47µF)	[C22]
r	220 R	C-FZ1131AFZZ	AC			Capacitor (50WV 10µF) Capacitor (250WV 0.82µF)	[C25] [C26]
F	221 V	CCCTV1HH300J	AA		C	Capacitor (50WV 30PF)	[C103]
L	222 V	CCCTV1HH300J	AA		C	Capacitor (50WV 30PF)	[C104]
1	223 V	CKYTV1HB102K CKYTV1HB102K	AA			Capacitor (50WV 1000PF)	[C106]
t	225 V	CKYT/1HF223Z	A A		C (C	Capacitor (50WV 1000PF) Capacitor (50WV 0.022µF)	[C107]
	226 7 (CKYTV1HB102K	AA	1		pacitor (50WV 0.022µF)	[C108] [C111]
L	227 1 (CKYTV1HF223Z	AA		C	Capacitor (50WV 0.022µF)	[C111]
H	228 V	CKYTV1HF223Z	A A		C (Capacitor (50WV 0.022µF)	[C113]
H	230 V	CKYTV1HF223Z CKYTV1HB681K	AA			Capacitor (50WV 0.022µF)	[C114]
r	231 V	CKYTV1HB881K	AA			Capacitor (50WV 680PF) Capacitor (50WV 8200PF)	[C115]
	232 V	CKYTV1HB122K	AB			Capacitor (50WV 8200FF)	[C116] [C117]
Ĺ	233 V	CKYTV1EF104Z	AA		C (Capacitor (25WV 0.1µF)	[C118]
H	234 V	CKYTV1HB331K	AA		C	Capacitor (50WV 330PF)	[C119]
H	235 V	CKYT/1HB331K CKYT/1HB331K	AA AA.			Capacitor (50WV 330PF)	[C120]
H	237 V	CKYTV1HB331K	AA.			Capacitor (50WV 330PF) Capacitor (50WV 330PF)	[C121]
t	238 V	CKYTV1HB331K	AA		c c	Capacitor (50WV 330PF)	[C122] [C123]
L	239 V	CKYTV1HB331K	AA		C	Capacitor (50WV 330PF)	[C124]
L		CKYTV1HB331K	AA		c c	apacitor (50WV 330PF)	(C125)
<u> </u>		CKYTV1HB331KTA	Α	1 1	<u>c</u> c	apacitor (50WV 330PF)	[C126]_
Η	243 V C	CKYTV1HB102K CKYTV1HF223Z A	A A A	[Capacitor (50WV 1000PF) pacitor (50WV 0.022µF)	[C127]
Ī	244 v (CKYTV1HB222KA	A			acitor (50WV 221)0PF)	[C128] [C129]
_							

8 TEL-Liu PWB unit

, NO	, PARTS CODE	PRICE RANK	NEW MARK		DESCRIPTION	
245	VCKYTV1HB273K	AA			Capacitor (50WV 0.027µF)	[C130]
	VCKYTV1HB392K	AA			Capacitor (50WV 3900PF)	cc1311
	V C K Y T V 1 H F 2 2 3 Z 7			C C	Capacitor (50WV 0.022μF) Capacitor (50WV 0.022μF)	CC1321
	VCKYTV1HF223ZA	A		C	Capacitor (50WV_0.022µF)	cc1331 [C134]
	VCKYTV1HF223ZA	Α		С	Capacitor (50WV 0.022µF)	[C135]
	VCKYTV1HB821KA	Α		С	Capacitor (50WV 820PF)	[C136]
252	V C K Y T V 1 H B 4 7 2 K Q J A K Z 2 0 2 9 S C O	0 AD		C	Capacitor (50WV 4700PF) Connector (4pin)	[C137]
254	QCNCM7014SC0BA	D A D		В	Connector (4pin)	[CNHJ] [CNLED]
255	QCNCW2436SC5J	ΑB		C	Connector (50pin)	[CNLIU]
	QCNCM7014SCOI	H A B		С	Connector (8pin)	[CNPI]
257	QCNCM705FAF02A QCNCM886JAFZZ	В		C	Connector (B6B-PH-K-R)	[CNPRT]
	QCNCM 2 4 0 1 SC 0 B A	A D A		C B	Connector (9pin) Connector (2pin)	[CNPW]
	QJAKZ2043SCFD	AC		Č	Jack	[CNSP] [CNLJ/TLJ]
261	VHDDSS133//-1	A A		В	Diode (DSS133)	[D2]
262	VHDDSS133//-1	AA		В	Diode (DSS133)	[D3]
	VHDDSS133//-1 VHD1SS82///-1	AA		B B	Diode (DSS133) Diode (1SS82)	[D4]
	VHDDSS131//-1	AA			Diode (15582)	[D5]
266	VHDDSS133//-1	AA	İ	В	Diode (DSS133)	[D6] [D7]
	V H D D S S 1 3 3 // - 1	A A		8	Diode (DSS133)	[D8]
	VH i U L N 2 0 0 3 A N / VH i U L N 2 0 0 3 A N /	A E		В	IC (ULN2003AN)	[IC1]
270	VH i N J M 4 5 5 8 D - 1	AN		8 B	IC (ULN2003AN) IC (NJM4558D)	[IC4]
271	VHINJM4558D-1	ÂN			1C (NJM4558D)	[IC5] [IC6]
272	VH i N J U 4 0 5 3 D - 1	ΑF		B	IC (NJU40530)	[IC7]
273	VH i MC 3 4 1 1 9 / - 1 A VH i MC 3 4 0 1 2 - 1 P	F A F		B B	IC (MC34; 19)	[IC9]
△ 275	VHITHS 5 6///-1 A	A F N		В	IC (MC34012) IC (THS56)	[IC10]
↑ 276	VH THS 56///-1	AN	,		IC (THS56)	[IC11] [IC12]
277	VH i S 7 2 3 5 F 2 F - 1	A M		В	IC (\$7235F2F)	[IC101]
278	V R S - T S 2 A D 0 0 0 J	AA		С	Resistor (1/10W $0\Omega \pm 5\%$)	[JP103]
279	VRS-TS2AD000J VRS-TS2AOOOOJ	A A		C	Resistor (1/10W 00 ±5%)	[JP107]
	V R S - T S 2 A D 0 0 0 J A	A		C	Resistor $(1/10W \Omega\Omega \pm 5\%)$ Resistor $(1/10W \Omega\Omega \pm 5\%)$	[JP109]
282	V R S - T S 2 A O O O O J	АА		С	Resistor (I/IOW On ±5%)	[JP110] [JP111]
283		A		С	Resistor (1/10W $\Omega\Omega \pm 5\%$)	[JP112]
284	V R S - T S 2 A D 0 0 0 J V R S - T S 2 A D 0 0 0 J	AA			Resistor (1/10W 0 Ω ±5%)	[JP113]
286	V R S - T S 2 A D 0 0 0 J	AA		C	Resistor (1/10W $\Omega\Omega$ ±5%) Resistor (1/10W $\Omega\Omega$ ±5%)	[JP114]
287	VIRS-TS2AD000J	AA		c	Resistor (1/10W 0 Ω ±5%)	[JP115] [JP118]
288 V		АА		С	Resistor (1/10W 0Ω ±5%)	[JP121]
	9 V R S - T S2 A D 0 0 0 J 0 R F i L N 2 0 1 1 S C Z Z	A A			Resistor (1/10W 0Ω ±5%)	[JP122]]
	V R O - R C Z E Y O O O J	AC	T	C	Coil (SBT – 0260) Resistor (1/4W <u>0Ω</u> ±5%)	[L1] 1 [L2]
	2 RFILN2011SCZZ			l c	Coil (SBT-0260)	[L3]
	3 VRO-RC2EYOOOJ			С	Resistor (1/4W On ±5%)	[L4]
294	\(\mathbb{R}\)D - R C 2 E Y 0 0 0 J A \(\mathbb{R}\)D - R C 2 E Y 0 0 0 J A			l c	Resistor (1/4W 0Ω ±5%)	נוטן
	6 VRO-RCZEYOOOJ			ו בר	Resistor (1,400 ± 5%) esistor (1/4W 0Ω ± 5%)	[L10]
297	VRD-RC2EY000JA	A	·	C	Resistor (1/4W OD $\pm 5\%$)	[L11]
2 9	9 8 R D. = R C. 2 E Y O O O J A	Α		С	Resistor (1/4W 0Ω ±5%)	[L13]
	VRD-R2EYOUTJA	A		С	Resistor (1/4W 0Ω ±5%)	[L14]
300	V R O - R C 2 E Y O O Q J A 1 V R D - R 2 E Y O O O J J .	ΑΔ		<u> </u>	Resistor (1/4W <u>00</u> +5%) Brsiotar ハイルの2 ±5%	[L15] [L16]
<u></u>	RFILN2011SCZZA	C		C	Coil (SBT-0260)	[L17]
1 303	RFILN2011SCZZA	С		С	Coil (SET-0260)	[L18]
	V RO-RC2EYOOOJ			C	Resistor (1/4W 0Ω ±5%)	[101]
	VHP.P.C.® J J / / - 1 _ 0 6 VHPPC817CD/-1	AM A C		B	Photo coupler (PC847) Photo coupler (PC817CD)	[PC2]
	7 VHPPC817CD/-1	AC			oto_coupler_(PC817CD)	[PC2]
∆ 308	V H P P C 8 1 7 D / / - 1 A	D		В	Photo coupler (PC817D)	[PC5]
	V S B S 1 0 8 / / - 1 A	E		В	Transistor (BS108)	[Q1]
	V S 2 S C 2 4 1 2 K R - 1 A V R S - R E 3 A A 2 2 2 J A	D A		B C	Transistor (2SC2412KR) Resistor (1W 2.2KΩ ±5%)	[Q101] [R2]
	VRS-RE3AA103JA	A		C	Resistor (1W 10KΩ ±5%)	[R4]
313	VRS-RE3DA750JA	L		C	Resistor (2W 750 ±5%)	[06]
314	VRD-HT2HY150JA	Α		С	Resistor (1/2W 15 Ω ±5%)	[R6]
315	VRD-HT2HY150J VRD-HT2HY474J	AA		C	Resistor (1/2W 15 Ω ±5%)	[50]
317	VRS-TS2AD221J	A A		Ċ	Resistor (1/2W 470K Ω ±5%) Resistor (1/10W 220 Ω ±5%)	[R8] [R101]
318	V R S - T S 2 A D 2 2 1 J	AA		C	Resistor (1/10W 220 $\Omega \pm 5\%$)	[R102]
319	VRS-TS2AD221J	ΑA		С	Resistor (1/10W 220 $\Omega \pm 5\%$)	[R103]
320	VRS-TS2AD221JA	A			Resistor (1/10W 220Ω ±5%)	[R104]
	VRS-TS2AD221JA VRS-TS2AOOOJ	A A A		C	Resistor (1/10W 22 Ω ±5%) Resistor (1/10W Ω \rightarrow \sim	[R105] [R107]
323	VRS-TS2AD104JA	A		C	Resistor (1/10W 100KΩ ±5%)	[K107] [
324	VRS-TS2AD000JA	Α		С	Resistor (1/10W On ±5%)	[R110]

8 TEL-Liu PWB unit

NO.	PARTS CODE	PRICE RANK	NEW MARK		DESCRIPTION	,
325	VRS-TS2AD122J VRS-TS2AD303J	AA			Resistor (1/10W 1.2KΩ ±5%)	[R11
327	VRS-TS2AD303J	AA	 	C	Resistor (1/10W 30KΩ ±5%)	[R11
328	VRS-TS2AD753J	AA		C	Resistor (1/10W 20KΩ ±5%)	[R11
329	VRS-TS2AD203J	AA			Resistor (1/10W 75KΩ ±5%) Resistor_(1/10W 20KΩ ±5%)	[R11
330	VRS-TS2AD000J	AA	1		Resistor (1/10W 0Ω ±5%)	[R12
331	VRS-TS2AD225J	AA			Resistor (1/10W 2.2M Ω ±5%)	[R12
332	VRS-TS2AD000J	AA			Resistor (1/10W 0Ω $\pm 5\%$)	[R12
333	VRS-TS2AD2031	AA			Resistor (1/10W 20KΩ ±5%)	[R12
334	VRS-TS2AD333J	AA			Resistor_(1/10W 33K Ω ±5%)	[R12 [R12
335	VRS-TS2AD622J	,AA,			Resistor (1/10W 6.2KΩ ±5%)	[R12
336	VRS-TS2AD103J	AA		С	Resistor (1/10W 10K Ω ±5%)	[R13
337	VRS-TS2AD203J	AA		С	Resistor (1/10W 20K $\Omega \pm 5\%$)	[R13
338	VRS-TS2AD103J	AA		С	Resistor (1/10W 10K $\Omega \pm 5\%$)	[R13
339	VRS-TS2AD203J	A A		Ĉ	Resistor_(1/10W_20KO ±5%)	[R13
341	VRS-TS2AD333J VRS-TS2AD203J	AA		C	Resistor (1/10W 33KΩ ±5%)	[R13
342	VRS-TS2AD203J	AA		Ç	Resistor (1/10W 20K Ω ±5%)	[R13
343	VRS-TS2AD753J	AA		C	Resistor (1/10W 20K Ω ±5%)	[R14
344	VRS-TS2AD302J	AA		C	Resistor (1/10W 75KΩ ±5%)	[R14
345	VRS-TS2AD103J	AA	— —	 	Resistor (1/10W 3.0KΩ ±5%) Resistor_(1/10W 10KΩ ±5%)	[R14
346	VRS-TS2AD685J	AA		č	Resistor (1/10W $\pm 5\%$) Resistor (1/10W $\pm 5\%$)	[R14
347	VRS-TS2AD164J	AA		c	Resistor (1/10W 160K Ω ±5%)	[R14
348	VRS-TS2AD272J	AA			Resistor (1/10W 2.7K Ω ±5%)	[R14
349	VRS-TS2AD101J	AA		č	Resistor (1/10W 100 Ω ±5%)	[R14 [R14
350	VRS-TS2AD822J	AA		Č	Resistor_(1/10W 8.2K Ω ±5%)	[R14
351	VRS-TS2AD333J	AA		С	Resistor (1/10W 33K $\Omega \pm 5\%$)	[R14
352	VRS-TS2AD821J	AA		C	Resistor (1/10W 820 Ω ±5%)	[R15
353	VRS-TS2AD821J	AA		С	Resistor (1/10W 820 Ω ±5%)	[R15
354	VRS-TS2AD203J	AA		<u>.</u>	Resistor (1/10W 20K $\Omega \pm 5\%$)	[R15
355	VRS-TS2AD623J	AA		C	Resistor (1/10W 62K Ω ±5%)	[R15
350	VRS-TS2AD103J	AA		C	Resistor (1/10W 10K $\Omega \pm 5\%$)	[R15
350	VRS-TS2AD183J VRS-TS2AD103J	AA		C	Resistor (1/10W 18K Ω ±5%)	[R15
350	VHD0R5G4B42-1	AA			Resistor (1/10W 10KΩ ±5%)	[R15
360	VHDS1ZB60//-1	A F A C			Diode (0R5G4B42)	[REC
361	RRLYZ3420SCZZ	AR			Diode (\$1ZB60) Relay (G6GN-2D)	[REC
362	RRLYZ3420SCZZ	AR			Relay (GGGN - 2D)	(R)
363	RRLYZ3420SCZZ	AR			Relay (G6GN – 2D)	[RY
364	VRD-RC2EY000J	AA			Resistor (1/4W 0 Ω ±5%)	[RY
365	VRD-HT2EY000J	AA			Resistor (1/4W 0 Ω ±5%)	[SP
366	VRD-HT2EY000J	AA			Resistor (1/4W $0\Omega \pm 5\%$)	[SF
367	PSPAZ2190SCZZ	AB			PWB spacer	[SPACE
368	QSW-S2166SC02	A C			Slide switch (HSW-1070-01-200)	[SW
369	QSW-S2166SC03	A C		В	Slide switch (HSW-1071-01-200)	[SW
	Q S W - Z 2 1 8 6 S C Z Z	AH			Switch (SPPY43)	[SW
3/1	RTRNZ2140SCZZ	AN		В	Transformer	[1
	RTRN 1 2 1 4 2 S C Z A	AP			Transformer	[]
374	R V R - Q 1 4 0 2 Q C Z Z	A D			Variable resistor (RS10M11AJ)	[VR
	R C R M - 0 0 9 1 A F Z Z V H E H Z S 3 B 1 / / - 1	A E			Crystal (CSA3.58MG)	[x
376	VHEHZS3B1//-1	A C			Zener diode (HZS3B1)	[ZD
	VHEMTZ5R6C/-1	AA		B	Zener diode (HZS3B1) Zener diode (MTZ5R6C)	[ZD
	VHEMTZ5R1C/-1	AA			Zener diode (MTZ5R1C)	[ZD
379	VHEHZS3B1//- 1=	AC			Zener diode (MTZSR1C) Zener diode (HZS3B1)	[20
380	VHEHZ53B1//-1	AC		В	Zener diode (HZS3B1)	[20
381	VHEHZS3B1//-1	A C			Zener diode (HZS3B1)	[ZD [ZD1
382	VHEHZS3B1//-1	A C			Zener diode (HZS3B1)	[ZD1
383	VHERD18EL2/-1	A A			Zener diode (RD18EL2)	[ZD1
384	VHEMTZ6R8B/-1	ΑB		В	Zener diode (MTZ6R8B)	[ZD1
	VHEMTZJ300B-1	A A			Zener diode (MTZJ300B)	[ZD1
386	QCNW-4260SCZZ	AA	N		Jumper wire	(201
	(Unit)					
901	DCEK1 2464888			!	Speech PWB unit (Na1~91)	
902	DCEKL346ASC32	ВТ	N	E	TEL—Liu PWB unit (Include No. 901)	

9 Panel PWB unit

NO.	PARTS CODE			PART RANK	DESCRIPTION	
	VCKYTQ1EF104ZA			С	Capacitor (25WV_0.1µF) [C1	11
	VCKYTQIHB222K, A	Α		Ĥ	Capacitor (50WV 2200PF) [C2	
3	V'k's'- TP2'B'D56211	A A ^R			IC (KS0066F00) [IC]	
				l c	Resistor (1/8W 5.6KΩ ±5%)_ [R1	= 11
5	<u> </u>	A A		C	Resistor $(1/8W_{5.6K\Omega} \pm 5\%)$ [R2	
	RS-TP2BD562J A	A A	Į	С		3]
7	<u> </u>	A A	Ì	С	Resistor (1/8W 5.6K Ω ±5%)	

9 Panel PWB unit

Ī	NO.	PARTS CODE	RANK	NEW MARK		DESCRIPTION	
Ī	8		A A		С	Resistor (1/8W On ±5%)	[R5]
	9	VRS-TP2BD104J	\ A		C R	esistor_(1/8W_100KΩ ±5%)	[R8]
	10	VRS-TP2BD1011	АА		С	Resister $(1/8W 100\Omega \pm 5\%)$	(R9)
	11	VRS-TP2BD101J LANGH2 70XHZZ	AD		Č	Bezel	
		PGUMM2 107SCZZ	AB		С	Rubber	
		VVLLF7174G6-1	AP		E	LCD (LLF7174G6)	
L		VHPGL3EG43/-1	AB		В	LED (Green) (GL3 _{EG43})	[AM]
		V H P G L 3 E G 4 3 /- 1	AB		8	LED (Green) (GL3EG43)	[AUTO]
1	53	RC-K1H104HCZZ	A C		С	Capacitor (50WV 0.1µF)	[C2]
	54	RC-EZ2017SCZZ	A C		С	Capacitor (16WV 22µF)	[C3]
Δ		QCNW-4174SCZZ	A C		С	LCD cable	[CNLCD]
L		QCNCM2419SC2B	ΑE		С	Connector (22pin)	[CNPN]
L		VHISN74LS145N	ΑH		В	IC (SN74LS145N)	[IC1]
		VRD-RC2EY000J	AA		С	Resistor (1/4W $0\Omega \pm 5\%$)	[J1]
		VRD-RC2EY000J	AA		С	Resistor (1/4W 0Ω ±5%)	[J2]
L		VRD-RC2EY000J	AA		С	Resistor (1/4W $0\Omega \pm 5\%$)	[J3]
L		VRD-RC2EY000J	AA		С	Resistor (1/4W $0\Omega \pm 5\%$)	[J4]
L	62	VRD-RC2EY000J	AA		С	Resistor (1/4W $0\Omega \pm 5\%$)	[J5]
L	63	VRD-RC2EY000J	AA		С	Resistor (1/4W 0Ω ±5%)	[J6]
L	64	VRD-RC2EY000J	AA		С	Resistor (1/4W $0\Omega \pm 5\%$)	[J7]
L	65	VRD-RC2EY000J	AA		С	Resistor (1/4W 0Ω ±5%)	[J8]
- 1	66	VRD-RC2EY000J	AA		С	Resistor (1/4W $\Omega\Omega \pm 5\%$)	[J9]
L	67	VHPGL3EG43/-1	AB		В	LED (Green) (GL3EG43)	[MAN]
L		VSDTC114ES/-1	AB		В	Transistor (DTC114ES)	[Q1]
L	69	VSDTC114ES/-1	AB		В	Transistor (DTC114ES)	[Q2]
L	70	VS/-1	ΑB		В	Transistor (DTC114ES)	[03]
L		V RD-RC2EY221JA	Α		С	Resistor (1/4W 220Ω ±5%)	[Ř1]
L		VRD-RC2EY221JA	Α		С	Resistor (1/4W 220Ω ±5%)	[R2]
L		VR D-RC2EY221JA	Α		С	Resistor (1/4W 220Ω ±5%)	[R3]
L		<u> </u>	Α		С	Resistor (1/4W_6.2KΩ ±5%3	[R4]
L		VRD-RC2EY102JA	Α		С	Resister $(1/4W 1.0K\Omega \pm 5\%)$	[R5]
L	76	Q S W - K 2 1 9 4 S C Z Z A	В		В	Tact switch (SOR - 123HS)	[SW]_
L						LCD PWB unit (No.1~13)	
L						Panel PWB unit (Na.51~76)	
		(Unit)	1				
-	901	DCEKP335ASC01	3 D		E P	anel PWB unit	
L	l	1	ſ	1 -	1	<u> </u>	
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Į,							

SensorPWBunit

NO.	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION	
	QCNCM7014SC0B	A D		В	Connector (2pin)	[CNLED(SNS)]
	QCNCM7014SC0G	AB		С	Connector (7pin)	[CNSNS]
3	Q S W - M 2 1 8 4 S C Z Z	A D		В	Door-switch (MSS-10A-6)	[DRSNS]
4	VHGP1S58V//-1	AE		В	Photo interrupter (GP1S58V)	[PC1]
5	VHGP1S58V//-1	ΑE		В	Photo interrupter (GP1S58V)	[PC2]
	VRD-HT2EY271J	AA		С	Resistor (1/4W 220Ω ±5%)	[R9]
7	VRD-HT2EY271J	AA		С	Resistor (1/4W 220Ω ±5%)	[R10]
	(Unit)					
901	DCEKS348ASC31	A W		E	Sensor PWB unit	
		1		T		

11 CCD PWB unit

NO.	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION
1	VCEAJA1EW226M	ΑB		С	Capacitor (25WV 22µF) [C1]
	VCKYTQ1EF104Z	AA		С	Capacitor (25WV 0.1μ F) [C2]
	QCNCM7014SC0G	A B		С	Connector (7pin) [CN1]
	VH TCD 1 2 0 0 D - 1	ΑZ		В	IC (TCD1200D) [IC1]
	V S 2 S C 2 4 1 2 K S - 1	ΑB		В	Transistor (2SC2412KS) [Q1]
	VRS-TP2BD222J	AA		С	Resistor (1/8W 2.2K Ω ±5%) [R1]
	VRS-TP2BD390J	AA		С	Resistor (1/8W 39 Ω ±5%) [R2]
8	P S H E Z 2 9 9 7 S C Z Z	AB		С	CCD sheet
	(Unit)				
901	DCEKD333ASC01B	Е		Ε	CCD PWB unit
				,	

50 Hardware parts

NO.	PARTS CODE	PRICE RANK	PART RANK	DESCRIPTION
B1	LX-BZ2178SCZZA	В	С	Screw

FO-3700A

50 Hardware parts

BBC LX = B 2 1 8 7 S C Z Z AB C C Serve WS S SERVEY S SER	NO.		PRICE RANK	NEW MARK	PART RANK	DESCRIPTION
B3	B2	LX-BZ2182SCZZ	AB		С	Screw
B4 XBPSD30P06K00 AA C Screw (3×6K) B5 XBPSE30P08K00 AA C Screw (3×8K) B6 XBPSN40P06K00 AA C Screw (4×6K) B7 XEBSD20P06000 AA C Screw (2×6) 88 XEBSD30P06000 AA C Screw (3×6) B9 XEBSF30P06000 AA C Screw (3×6) B10 XEBSD30P10000 AA C Screw (3×10) B11 XEBSF30P08000 AA C Screw (3×8) B12 XEBSD30P10000 AA C Screw (3×10) B13 XEPSD30P06000 AA C Screw (3×10) B14 XHBSD30P06000 AA C Screw (3×6) B15 XPSD30P06000 AA C Screw (3×6) B16 XUBSD30P06000 AA C Screw (3×6)	B3 l	XHBSD30P05000			C	Screw (3×5)
B6 X B P S N 4 0 P 0 6 K 0 0	B4	XBPSD30P06K00	AA		Č	Screw (3×6K)
B6 X B P S N 4 0 P 0 6 K 0 0	B5	XBPSE30P08K00				Screw (3×8K)
B7 X E B S D 2 0 P 0 6 0 0 0	B6	XBPSN40P06K00			C	Screw (4×6K)
88 X E B S D 3 0 P 0 6 0 0 0 A A C Screw (3×6) 89 X E B S F 3 0 P 0 6 0 0 0 A A C Screw (3×6) B10 X E B S D 3 0 P 1 0 0 0 0 A A C Screw (3×10) B11 X E B S F 3 0 P 0 8 0 0 0 A A C Screw (3×8) B12 X E B S E 3 0 P 1 0 0 0 0 A A C Screw (3×6X) B13 X E P S D 3 0 P 0 6 0 0 0 A A C Screw (3×6X) B14 X H B S D 3 0 P 0 6 0 0 0 A A C Screw (3×6) B15 X J P S D 3 0 P 0 4 0 0 0 A A C Screw (3×6) B16 X U B S D 2 0 P 0 6 0 0 0 A A C Screw (2×6) B17 X E B S E 3 0 P 0 8 0 0 0 A A C Screw (3×8)	B7	XEBSD20P06000			Č	Screw (2×6)
B9 X E B S F 3 0 P 0 6 0 0 0 A A C Screw (3×6) B10 X E B S D 3 0 P 1 0 0 0 0 A A C Screw (3×10) B11 X E B S F 3 0 P 0 8 0 0 0 A A C Screw (3×8) B12 X E B S E 3 0 P 1 0 0 0 0 A A C Screw (3×10) B13 X E P S D 3 0 P 0 6 0 0 0 A A C Screw (3×6X) B14 X H B S D 3 0 P 0 6 0 0 0 A A C Screw (3×6) B15 X J P S D 3 0 P 0 4 0 0 0 A A C Screw (3×4) B16 X U B S D 2 0 P 0 6 0 0 0 A A C Screw (2×6) B17 X E B S E 3 0 P 0 8 0 0 0 A A C Screw (3×8)	B8	XEBSD30P06000	AA			Screw (3×6)
B10	89	X F B S F 3 0 P 0 6 0 0 0			- c	Screw (3×6)
B12 X E B S E 3 0 P 1 0 0 0 0	B10	XEBSD30P10000	AA		<u> </u>	Screw (3×10)
B12 X E B S E 3 0 P 1 0 0 0 0	B11	X F B S F 3 0 P 0 8 0 0 0			 	Screw (3 × 8)
B13 X E P S D 3 0 P 0 6 X 0 0 A A C Screw (3×6X) B14 X H B S D 3 0 P 0 6 0 0 0 A A C Screw (3×6) B15 X J P S D 3 0 P 0 4 0 0 0 A A C Screw (3×4) B16 X U B S D 2 0 P 0 6 0 0 0 A A C Screw (2×6) B17 X E B S E 3 0 P 0 8 0 0 0 A A C Screw (3×8)	B12	XERSEROPLOCO			5	Screw (3×10)
B14 X H B S D 3 0 P 0 6 0 0 0	B13	X F P S D 3 O P O 6 Y O O				Screw (3 × 6 V)
B17 X E B S E 3 0 P 0 8 0 0 0 A A C Screw (3×8)	B14	YHRSD30P06000			 	Screw (3×6)
B17 X E B S E 3 0 P 0 8 0 0 0 A A C Screw (3×8)	B15	X 1 P S D 3 0 P 0 4 0 0 0	Λ Λ		 	Screw (3 × 4)
B17 X E B S E 3 0 P 0 8 0 0 0 A A C Screw (3×8)	B16	X 11 B 5 D 2 D D D 6 D D D			<u>, , , , , , , , , , , , , , , , , , , </u>	3Crew (3×4)
GIS 15 0 3 0 0 0 0 0 0 0 AA C Server(3X8) 10	B17	Y E B S E 3 O D O 8 O O O			· ·	Screw (2×0)
	D19	X E B S D 3 O B O 8 O O O			<u>~</u>	3Crew (3×0)
	B10	XE03030F08000	_ A A		<u> </u>	Screw (3×8)
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Index

PARTS CODE	NO.	PRICE RANK		PART RANK	
[C] CCABA2232SC12	0 101	2.4	<u> </u>		
CCNW-4171SC01	2- 101 1- 24	BM AR	N	E C	· · · · · · · · · · · · · · · · · · ·
[D]	1- 24	-2.5		<u> </u>	
DCEKC780FSCZZ	1- 18	CA	N	E	
//	6- 901	CA	N	E	
DCEKD333ASC01	4- 9	BE		Ε	
DOSK 1 24 5 4 5 0 2 0	11- 901	BE		Ε	
DCEKL346ASC32	1- 16 8-902	BT	N N	E	
DCEKP335ASC01	2- 13	BD	14	E	
"	9- 901	BD		Ē	
DCEKS348ASC31	1- 33	AW		E	
"	10- 901	AW		E	
DUNTK4925SCD3	5- 12	AX		E	<u> </u>
[G] GCABA2232SCZD	2- 4	AX	N	D	
GCABB2231SCZB	1- 22	88	N	ď	
GCABC2233SCZB	1- 8	ВА		D	
GLEGG2026SCZZ	1- 20	АА		С	
[j]					
JBTN-2085XHZC	2- 9	AR		C ·	
JBTN-2086XHZC JBTN-2115SCZB	2- 11	A Q		č	
JBTN-2116SCZB	2- 12 2- 8	A G		C	
JBTN-2117SCZB	2- 10	AC		C	
JKNBP2063SCZB	2- 7	AD		Č	-
[L]					
L ~ N N G + 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	<u>s- 11</u>	ΑD		C	
LBNDJ2006SCZZ	1- 14	AA		C	
LFRM-2147XHZZ LHLDW2133SCZZ	4- 4 i-23	A Y AC		C	
LPLTG2678SCZZ	2- 18	AD		0	
LPL 1 TH 2 3 6 4 3 C 2 Z 7	ı ² 19	AR		C	
LPLTM2685SCZZ	2- 15	A D		Ü	
LPLTM2687SCZZ	l - 1 0			C	
LPLTP2676SCZZ	2- 20	AE		C	
LPLTP2677SCZZ	1- 11	AH	A1	ç	
LPLTP2679SCZC LPLTP2680SCZB	5- 17 5- 31	A S AN	<u> _</u> %	C	
LPLTP2681SCZB	5- 18	ΑŲ		Č	
LPLTP2682SCZB	5- 29	AH		Č	
LPLTP2683SCZZ	5- 19	ΑU		С	
LX-BZ2178SCZZ	50-B1	AB		C	
LX-BZ2182SCZZ (M)	50-B2	, AB	-	С	<u> </u>
MLEVP2165SCZB	1- 32	'A'Ł	l I	U	i
MLEVP2166SCZZI2		AM		C	
MLEVP2169SCZZ 1		С		Č	
MLEVP2170SCZZ	1- 29	A C		С	
MSPRC2660SCZZ	2- 5	AR		_ <u>Ĉ</u>	
MSPRC2681SCZZ	2- 33	A C	-		<u> </u>
MSPRD2655SCZZ MSPRD2656SCZZ	2- 35 2- 36	A C	 	С	
MSPRD2659SCZZ	1- 31-	AB		C	
MSPRD2671SCZZ	1- 30	AB		Č	
MSPRP2512SCZZ	4- 8	AB		C	
MSPRP2535XHZZ	2- 29	AD		C	
MSPRP2618SCZZ	4- 5	AD		C	<u> </u>
MSPRP2619SCZZ MSPRP2652SCZZ	4- 3 2- 17	AD A D	<u> </u>	C	
MSPRP2653SCZZ	1- 7	AC		Č	
MSPRP2658SCZZ	5- 32	AC		C	
MSPRP2667SCZZ	1- 46	AC		Ċ	
MSPRT2657SCZZ	2- 25	ΑВ		Ċ	
MSPRT2661SCZZ	2- 16	A B		C	
MSPRT2676SCZA	2- 19	ΑB	<u> </u> 	C	
NBRGP2138XHZZ	1- 6	AD	 	C	
NBRGP2141XHZZ	1 - 2 5	, T. A	 	C	† <u>-</u>
"	2- 23	AH		C	
NGERH2210XHZZ	1- 12	AC		С	
NGERH2240XHZZ	1- 13	AC		С	
NGERH2257SCZZ	1- 3	AE	ļ	C	
NGERH2258SCZZ	1- 26 2- 22	AB	 	C	
NGERP2206XHZZ	2- 22	AE	 	C	
NROLP2249XHZZ	2- 28	AE	t	C	
NROLP2289SCZZ	1- 48	ΑĒ		С	
NROLP2292SCZZ	1- 5	IAD	1	i C	I
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ı	ı			1	,
PARTS CODE	NO.	PRICE RANK	NEW MARK	P A R T RANK	
NRŌLR2284SCZZ	2- 27	AK	MAKK	C	
NROL K22333522	1-7 2 .	· A M		Ü	
NROLR2286SCZZ	1- 27	AK		Č	
NRÕLR2288SCZZ	1- 47	AH		C	
NSFTZ2249SCZZ	1-4	AS		С	
[P]					
PBRS-2041SCZZ PCOVH2098SCZZ	2- 31 1- 21	AG		<u> </u>	
PCOVP2097SCZB	2- 21	AD		C	
PG i DM2 3 9 6 SC Z B	2- 32	BA		Ċ	
PG i DM2397SCLB	2- 2	AE		č	
PG i DM 2 3 9 7 SCRB	2- 3	ΑE		Č	
PGiDM2398SCZD	1- 1	ΑU	N	С	
PGLSP2043SCZZ	4- 6	AF		С	
PGUMM2107SCZZ	9- 12	A B		C	
PHOP-2076SCZZ	5- 20	AQ		C C	
PLNS-2043SCZZ PM:R-2061SCZZ	4- 10 4- 2	AV		B	
PM i R-2062SCZZ	4- 1	AH		C	
PSHEZ2879XHZZ	4- 7	AK		Č	
PSHEZ2880XHZZ	1- 44	AK		c	
PSHEZ2897SCA4	5- 23	ΑK		Č	
PSHEZ2930SCZB	5- 2	ΑE		С	
PSHEZ2935SCZZ	2- 24	A B		С	\
PSHEZ2940SCZZ	4- 12	, A B		C	
PSHEZ2974SCZZ PSHEZ2980SCZZ	4- 14	AA		C	
PSHEZ2980SCZZ	1- 49 5- 36	A M A D		C	
PSHEZ2997SCZZ	11- 8	AB		C	
PSHEZ3002SCZZ	1- 50	AD		C	<u> </u>
PSPAZ2181SCZZ	1- 42	ΑB		Ċ	
PSPAZ2190SCZZ	8- 367	ΑB		C	
[0]					
QACCL762ASCZZ	1- 15	AW		В	
QCNCM2389SC2B	6- 90	ΑE		C	
QCNCM2401SC0B	8- 259	AA		B	
QCNCM2419SC2B QCNCM2436SC5J	9- 56 6- 89	A E A B		C_	
QCNCM24565C3J	6- 88	AF	_	l c	
QCNCM2476SC2J	8- 25	Į AU.		C	
QCNCM7014SC0B	8- 254	AD		В	I
	10- 1	AD		В	
QCNCM7014SC0E	6~ 91	ΑB		C	
QCNCM7014SC0F_	6- 92	A B		C	
QCNCM7014SC0G	6- 87 10- 2	A B AB		C	
",		AB		С	
QCNCM7014SC0H	8- 256	AB	·	Č	
QCNCM705FAF02	8- 257	AB	1	C	
QCNCM886JAFZZ	8- 258	ΑD		С	
QCNCW2436SC5J	8- 255	ΑВ		C	
QCNW-3976XHOG	5- 7	ΑT		C	
QCNW-4167SCZZ	1- 38	A H AG		C	
QCNW-4168SCZZ QCNW-4169SCZZ	1- 39 4- 13	ŢĄĊ	\vdash	C	1
QCNW-4170SCZZ	2- 14	AT		Č	
QCNW-4172SCZZ	1- 34	AH		C	
QCNW-4173SCZZ	1- 37	A C		C	
QCNW-4174SCZZ	9- 55	A C		C	
QCNW-4175SCZZ	2- 30	AE	1.	C	
QCNW-4260SCZZ	8- 386	AA	N	C	
QJAKZ2029SC0D QJAKZ2043SCFD	8- 253 8- 260	A D	1	C	
QSOCZ2051SC32	6- 122	AG	 	C	
QSW-K2194SCZZ	9- 76	AB	 	B	
QSW-M2184SCZZ	10- 3	AD		В	
QSW-S2166SC02	8~ 368	A C		В	
QSW-S2166SC03	8- 369	A C		8	
QSW-Z2186SCZZ	8- 370	ΑH		В	
QTANZ2042SCZZ	8- 202	AB	L	<u> </u>	
[R]	0	A (1	D	
RALMB2007SCZZ RC-EZ2017SCZZ	8- 203 9- 54	AG	-	B	-
RC-EZ2030SCZZ	υ- ³ 235	A L		C	
RC-EZ20303CZZ	3- 238	AE		C	
RC-FZ1131AFZZ	R - 220	AC		C	
	5 3 /	1 C		Č	
RCORF2063SCZZ	1- 41	ΑĒ		С	
RUJ#7000005022	, [®] 1. ³ 35	ΑG		C	
RCORF2084SCZZ	1- 40	AG			

	Y				
PARTS CODE	NO.	PRICE	NEW	PART	
1 .	1	RANK	MARK	RANK	
RCORF2085SCZZ	3- 228	ΑE		С	
//	3- 239	ΑE		С	
RCRM-0091AFZZ	8- 374	ΑE		В	
RCRSP2080SCZZ	6-290	AF		8	
RCRSP2083SCZZ	6- 292	AE		В	
RCRSQ2090SCZZ	6- 293	A D		С	
RCRSZ7008SCZZ	6- 291	AD		В	
RDENT2074SCZZ	1- 17	BN		Ē	
"	7- 901	BN			
RFILN2011SCZZ	8- 296	AC		E	
	8- 292			C	
		A C		C	
<u>"</u>	&- 302	A C		c	
	a - 303	AC	1	<u> </u>	
RMOTZ2109SCZZ	1- 9	AX		В	
RR-DZ4103ACZZ	3- 227	AB		<u> </u>	
RR-SZ2002SCZZ	3- 234	AE		<u> </u>	
RRLYZ3420SCZZ	8- 361	AR	1	l B	_
	8- 362	AR		В	
	8- 363	AR		В	
RTRN i 2142SCZA	8- 372	AP		В	
RTRNZ2140SCZZ	8- 371	AN		В	
RVR-Q1402QCZZ	8- 373				
[S]	0 3/3	AD		B	
SPAKA4105SCZZ	-	A,,,			
SPAKA41050022	5- 5	1		D	
SPAKA4106SCZZ	5- 4	L A M		D	
SPAKA4108SCZZ	5- 16	AN		D	
SPAKA4109SCZZ	5- 15	AE	T	D	
SPAKA4274SCZZ	5- 10	ΑĐ		D	
SPAKA4345SCZZ	5- 3	AC		D	
SPAKC4202SCZZ	5- 9	AP		D	
SPAKP4080SCZZ	5- 1	AM		D	
SSAKA1340QCZZ	5- 34	AB		<u>D</u>	
SSAKA2344QCZZ	5- 21	AB	——- <u>i</u>	0	
SSAKA3001CCZZ	5- 8				
SSAKA3340QCZZ		AA		D	
SSAKA3340QCZZ	5- 33	A.S		D	
	5- 30	AB		D	
ኒግነ	ļ				
TCADZ2139SCZZ	5- 24	ΑĒ		D	
TCADZ2264SCZZ	5- 25	ΑE		D	
TCADZ2274SCZB	5- 27	AD		٥	
TGANE 2036SCZZ	5- 26	AC		D	
TINSE3397SCZZ	5- 22	AZ	N	D	
TLABH3435SCZB	5- 35	AD		Ď	
TLABP3078SCZZ	6- 294	AA			
TLABS3420SCZZ				D	
TLABS3421SCZZ		AB	N	D	
TLAB334213C22	1- 52	AB	N	D	
TLABZ3418SCZZ	1- 53	AB	N	D	
[U]		L			
UBATN2010SCZZ	6- 1	AN		<u> </u>	
UBNDA1008CCZZ	5- 11	AA		С	
[V]					
VCCCTV1HH101J	3- 222	AA		С	
"	6- 72	AA		C	
"	6- 86	AA	+	c	
VCCCTV1HH180J	6- 67	AA		č	
"	6- 68	AA	+	c	
VCCCTV1HH221J	6- 57	AA		c	
//	6- 58				
VCCCTV1HH300J		AA		C	
//	6- 29	AA		Č	
	6- 30	AA		C	
	8- 221	AA		C	
// VOCOTV::!!!	8- 222	AA		С	
VCCCTV1HH330J	6- 42	AA		С	
ii ii	6- 48	АА		C	
VCCCTV1HH470J	3- 221	АА		_ C	
VCCCTV1HH5R0 L	6 24	АА		С	
VCCSTVIHLI02J	6- 52	АА		С	
"	6- 53	АА		С	
<i>"</i>	6- 81	АА		c	
<u> እ</u> ናር ሣ E ሣ ይ ገ ለተ፤	ر م _ع کی	A A	Ī	С	
"	8- 3	A A	-+	С	
",		A A			
VCEAEA1AW107M				С	
* OEMENIAWIU / M	8- 207	A B		С	
VACACA LAMONA CO. T	8- 5 _['	H A	L'.	' ن	
VCEAEA1AW226M			1	С	
VCEAEA1AW227M	8- 205	AB			
VCEAEAIAW227M VCEAEAIAW476M	8- 212	AB		č	
VCEAEA1AW227M					
VCEAEAIAW227M VCEAEAIAW476M	8- 212	AB		С	
V C E A E A 1 A W 2 2 7 M V C E A E A 1 A W 4 7 6 M V C E A E A 1 E W 4 7 6 M	8- 212 8- 213 8- 214	A B A B		C C C	
V C E A E A 1 A W 2 2 7 M V C E A E A 1 A W 4 7 6 M V C E A E A 1 E W 4 7 6 M	8- 212 8- 213	AB AB		C C	

DADTO CODE	T	PRICE	NEW	PART	· T
PARTS CODE	NO.	RANK	MARK		
VCEAEA1HW335M	8- 11	AA		С	1
VCEAEA1HW474M	8- 6	AA		С	
VCEAEU1EW106M	8- 9	A A	ļ	С	
VCEAEU1HW105M	8- 215 8- 7	AA	-	C	
//	8- 7 8- 206	AA		C	-
"	8- 216	AA		C	
VCEAEU1HW475M	8- 4	AA		c	
VCEAJA1EW226M	11- 1	AB		C	
VCEAPS105AF1H	6- 10	AB		C	
VCEAPS106AF1E	6- 2	A C		С	
VCEAPS226AF1C	6- 12	A C		С	
	6- 13	A C		C	
VCEAPS336AF1C	6- 14	A C		C	
VCFYJU2EA474K	6- 9 8- 217	A C		C	
//	8- 218	AD		C	ļ
VCKRTQ1HR104K	3- 223	AB		C	
VCKRTV1HR102K	3- 220	AB		C	
VCKRTV1HR103K	3- 219	AB		C	
VCKYCY1EF104Z	6~ 5	AA		C	
	6- 6	AA		С	
// // // // // // // // // // // // //	6- 7	AA		C	
VCKYCY1EF223Z	6- 11	A A		C	1
VCKYCY1HB102K VCKYTQ1CF105Z	6- 8	AA		C_	<u> </u>
WCKTTQTCFTU3Z	6- 45	AE		С	
"	6- 51	AE		C	
VCKYTQ1EF104Z	6- 47	AA		C	
//	9- 1	AA		c	<u> </u>
//	11- 2	AA		Č	
VCKYTQ1HB104K	8- 16	AB		С	
//	8- 19	AB		С	
VCKYTQ1HB222K	9- 2	AA		С	
VCKYTQ1HB333K	8- 10	AA		C	
VCKYTQ1HB472K VCKYTQ1HB563K	8- 15	AA		C	
// // // // // // // // // // // // //	8- 22 8- 24	AA		C	
VCKYTV1EF104Z	6- 4	AA		C	·
//	6- 21	AA		C	
//	6- 25	AA		Č	
//	6- 26	AA		С	
	6- 33	AA		С	
	6- 34	AA		<u> </u>	
	6- 37	AA		С	
	6- 39 6- 46	AA		<u> </u>	
<i>"</i>	6- 50	AA		<u>C</u>	
"	6- 54	AA		C	
"	6- 55	AA		c	·
"	6- 56	AA		C	
//	6- 71	AA		С	
	6- 73	AA		С	
	6- 74	AA		С	
	6- 75	AA		<u>C</u>	
	6- 77 6- 79	AA		<u>C</u>	
" "	6- 82	AA		C	
11	6- 83	AA		C	
"	6- 84	AA		C	
//	6- 85	AA		C	
//	8- 233	AA		С	
VCKYTV1HB102K	8- 223	AA		С	
	8- 224	AA		С	
	8- 226 8- 242	AA		C	
VCKYTV1HB122K	8- 232	A A A B		C	
VCKYTV1HB122K	8- 18	AA		C	
VCKYTV1HB222K	6- 15	ÂÂ	+	C	
//	6- 16	AA		Č	
//	6- 17	AA		C	
//	6- 18	AA		С	
//	6- 19	AA		С	
	6- 22	AA		C	
//	6- 23	AA		C	
"	6- 27 6- 28	AA		C	
	6- 31	A A		C	
//	6- 32	ÂÂ	+	C	
//	6- 41	AA		C	

	1				
PARTS CODE	NO.		NEW		
VCKYTV1HB222K	- 44	<u> </u>		С	
//	6- 59	AΑ		č	
<i>!!</i>	6- 60	АА		С	
	6- 61	ΑA		C	
	6- 64	AA		С	
<u>"</u>	6- 70	AA	\vdash	C	
	8- 12 8- 13	AA	-	<u>C I</u>	
<u>"</u> ,	8- 244	AA		C	
VCKYTV1HB223K	8- 23	AA		- c	
VCKYTV1HB272K	6- 76	AA	 	C	
VCKYTV1HB273K	8- 245	AA		Č	
VCKYTV1HB331K	8- 234	AA		C	
"	8- 235	A A		С	
//	a- 2 36	A A		С	
//	a - 2 37	AA		С	
//	a- 2 38	AA		<u> </u>	
<u> </u>	a 239 a 240	ΑA	 	C	
		ÂÂ		C	
VCKYTV1HB332K	a- 241	AA		C	
VCKYTV1HB332K	8- 14	AA	-	C C	
//	8- 246	AA		C	
# # # # # # # # # # # # # # # # # # #		AA		C	
VCKYTV1HB472K	6- 80	2000	 	č	
//	R= 252	^AAA		Č	
VCKYTV1HB473K	8- 20	AA		Ç	5
VCKYTV1HB681K	6- 78	AA		С	
"	8- 230	AA		С	
VCKYTV1HB821KI8		. A		C I	
<u>VCKYTI'V' I HTG 6° 2° 2' K</u>	, of 2, 231	AA	Į.	С	
VCKYTV1HF223Z	6-3	AA ,		C	1
	6- 20	AA		<u> </u>	
//	6- 35	AA		<u>C</u>	
	6- 36	AA		C.	
"	6- 38 6- 40	A A A A		<u> </u>	
<i>''</i>	. a - 62	AA		Č	
<u>"</u>	6- 63	AA		č	
"	6-,65	A.A		Č	
"	6- 66	ΑA		-c	
"	6- 69	АА		C	
"	a - 22!	ΑA		С	
"	a- 227	AA		С	
//	a- 228	ΑA		С	
//	8- 229	ΑA		С	
//	8- 243	АА		C	
	8- 247	AA		С	
<i>"</i>	8- 250	A A	\vdash	· · ·	-
VCQYNA1HM224K	8-1	AC		C	
VCQYNA1HM333K	8- 204	A A			
//	a- 209	AA		C	
VCQYNU1HM334K	8- 210	A D		C	
"	8-211	AD		Č	
VCSAPJ1AA475M		^ AB		С	
VHDDAN212K/-1	8- 26	AC		В	
//	8- 27	A C	oxdot	В	
VHDDSS131//-1	8- 265	AA		В	
VHDDSS133//-1	8- 261	AA	1	<u>B</u>	
" "	8- 262	AA	 	B	
"	8- 263	AA	 	<u> 8</u>	
	8- 266 8- 267	AA	 	B 	
VHD MN 1 0 / / / - 1	6- 93	AC	 	- B	
"	6- 94	AC	 		
VHDPRAJAJAJA,	,6- U 95			B	
VHDRB421D//-1	8- 86	A C	1	В	
11	8- 87	AC		В	
VHDS12B60//-1	8- 360	AC		8	
VHD0R5G4B42-1	8- 359	AF		В	
VHD1N4148//-1	7- 14	AA		8	
VHD1SS82///-1	8- 264	AB		В	
VHEHZS3B1//-1	8- 375	A C		В	
		1 4 0	ı –	В	
	8- 376	A C	 		
"	8- 379	A C		В	
<i>II II</i>	8- 379 8- 380	A C		B B	
"	8- 379	A C		В	

PARTS CODE	NO.	PRICE	NEW	PART	
	_	RANK	MARK	RANK	
VHEMT 7 2 B 0 A Z-1	a- 385	AA		В	
VHEMTZ2R0A/-1	a- 88 a- 89	A A A A		B B	
VHEMTZ5R1C/-1	8- 378	AA		B	
VHEMTZ5R6B/-1	8- 90	ΑВ		В	
VHEMTZ5R6C/-1	8- 377	АА		В	
VHEMTZ6R8B/-1	a - 384	AB	, ,	В	
VHERD18EL2/-1	a - 3 3 3	J.AA,	L	В	
VHE12C15///-1 VHGP1S58V//-1	8- 91 10- 4	AC G^ E		B	
// // // // // // // // // // // // //		A E		В	
VHiF255011/-1	6~ 102	AU :		В	
VHIGM4256BSJ7	6- 105	ΑX		В	
VH i HM 5 1 4 8 0 0 J 8	6- 106	S.B		R	
VHIKS0066F00/	9- 3	AR		В	
VHiLB1730//-1	6- 108	AH		В	
VH i LH 5 2 6 8 T 4 1 0	6- 99	AS		В	
VHILM393PS/-S	6- 121	A C		В	
VH i L Z 9 5 G 3 8 / - 1 VH i MC 1 4 0 5 3 BMF	6- 107 , l 6- 115	,4,4	-	B	
VHIMC14053BMF	6- 111	A D		B	
// // // // // // // // // // // // //	6-112	A D		В	
	I - 77.4	ΑF		В	
VHIMC341-195-1	, 8-273	AF		В	
VHIMC74HC14MF	6- 117	ΑE		В	
VHiMC74HC32MF	6- 119	A C		В	
VH i M 6 6 3 3 3 F P-1	6- 9 _a	BC		В	
VH i N J M 3 1 8 M / - F VH i N J M 4 5 5 8 D - 1	6- 109 8- 270	AF	<u> </u>	B	
// // // // // // // // // // // // //		AN		В	
VHINJM4558F-1	8- 271 6- 116	AN		В	
VHINJU4053D-1	8- 272	AF		В	
VHINJU6355E-1	6- 120	AM		В	
VHIPST600CMT1	6- 103	ΑE		В	
VHIR96SHF//-1	6- 97	ВD		В	
VHISN74HC04NS	6- 114	A C		В	
	6- 118	A C		В	
VHISN74LS145N	9- 57	AH		В	
VHISN7406NS-1	6- 113	AF	 	В	
VH i S 7 2 3 5 F 2 F - 1 VH i T A 3 1 0 6 5 A - 1	8- 277 8- 2a	A M A K	<u> </u>	В	
VHITA31003A-1	11- 4	AZ		В	
VH:THS56///-1	8- 275	AN		В	
"	8- 276	AN		В	
VHITLOR4CN/-F	6- 110	AN		В	
VHIULN2003AN/	268 - ۹	<u> </u>		B	
//	8- 269	ΑE		В	
VH i 27020FCB0B	6- 123	ВМ	N	В	
VH i 4 3 2 5 7 A G 1 0 L	6- 101	AY		B	
VH	6- 100	i x		B	
VHPGL3EG43/-1	9-51	AB		В	
// // // // // // // // // // // // //	9- 52_	AB		В	
//	9- 67	A B		В	
VHPLT4657E7-1	4- 11	ΑY		В	
VHPPC817CD/-1	8- 306	A C		8	
// VHPP^&J.7\//~1	8- 307	AC		8	L
VHPPC847///-1	! ያ- 308 .a - 3 0 5	A D A M		B	1
VHV CPN20//-1	6- 96	A D		В	
VHV3P10P1//-1	8- 201	AM		В	
VRD-HT2EY000J	8- 365	AA		С	
"	8- 366	АА		С	
VRD-HT2EY271J	10- 6	AA		C	
// VPD -HT2HV1601	10- 7	A A_	<u> </u>	C	
VRD-HT2HY150J	8- 47	AA	 	C	
"	8- 314 8- 315	AA	 	C	
VRD-HT2HY331J	6- 146	AA		C	
// // // // // // // // // // // // //	a- 147	ΑA		C	
VRD-HT2HY474J	E - 3 1 6	A A		C	
VRD-ኊ፞፝፞ጜጜጜጜኯፘፘቜ	8- 291	АА	T	Č	
"	8-29 1 3	A A		С	
"	8- 294	AA	L	C	
	8- 295	AA	ļ	C	ļ
//	8- 296	AA	_	C	
					1
//	8- 297	AΑ	-		
//	8- 298	AA		С	

PARTS CODE	NO.	PRICE		PART	<u> </u>
VRD-RC2EY000J		RANK	MARK	RANK	
// // // // // // // // // // // // //	8- 301 8- 304	AA	<u> </u>	C	
//	8- 364	AA		C	
//	9- 58	AA		С	
"	9- 59	AA		C	
<u>"</u>	9- 60 9- 61	AA	<u> </u>	C	
"	9- 62	AA		c	
//	9- 63	AA		C	
	9- 64	AA		С	
	9- 65 9- 66	AA	<u> </u>	C	
VRD-RC2EY102J	9- 75	AA		C	
VRD-RC2EY221J	9- 71	AA		C	
	9- 72	AA		С	
VRD-RC2EY622J	9- 73	AA		C	
VRS-CYIJD103J	9- 74 6- 143	AA		C	
VRS-CYIJD163J	6- 141	AA		C	
VRS-CY1JD221J	6- 138	AA		С	
VRS-CY1JD222J	6- 139	AA		C	
	6- 140	AA		C	
VRS-CY1JD224J	6- 145	AA		C	
VRS-CY1JD470J	6- 144	AA		Č	
VRS-HT3AA151J	8- 39	AA		C	
VRS-RE3AA103J VRS-RE3AA222J	8- 312 8- 311	AA		C	L.
VRS-RE3DA750J	8- 313	AA		<u>C</u>	
VRS-TP2BD000J	6- 124	AA		С	
"	6- 125	AA		С	
<u>"</u>	6- 126	AA		_ <u>c</u>	
—— <u>"</u>	6- 127 6- 128	AA		C	
	6- 204	AA		č	
	9- 8	AA		С	
VRS-TP2BD101J	6- 202	AA		C	
VRS-TP2BD104J	9- 10 9- 9	AA		ပ	
VRS-TP2BD104J	11- 6	AA		C	
VRS-TP2BD390J	11- 7	AA		C	
VRS-TP2BD561J	6~ 289	AA		C	
VRS-TP2BD562J	9- 4 9- 5	AA		ပပ	
	9- 6	AA		C	
"	9- 7	AA		C	
VRS-TS2AD000J	6- 158	AA		C	
<i>"</i>	6- 160 6- 161	AΑ		C	
"	6- 173	AA		C	
//	6- 195	AA		c	
	6- 197	AA		С	
"	6- 225	AA		C	
"	6- 228 6- 231	AA		C C	
"	6- 288	AA		č	
//	8- 29	AA		С	
	8- 278	AA		Ç	
"	8- 279 8- 280	A A		C	
"	8- 281	AA		c	
"	8- 282	A A		С	
<u>"</u>	8- 283	AA		С	
<i>"</i>	8- 284 8- 285	AA		C	
"	8- 286	AA		c	
11	8- 287	ÂÂ		С	
"	8~ 288	AA		С	
"	8- 289	AA		C	
"	8- 322 8- 324	AA		C	
"	8- 330	AA	+	C	
"	8- 332	AA		С	
VRS-TS2AD100J	6- 155	AA		С	
<u>"</u>	6- 207	AA		C	
"	6- 208 6- 209	AA		C	
	6- 210	AA	+	c	
IJ	6- 213	AA		С	
	6- 281	AA		С	

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PARTS CODE	NO.		NEW		
VRS-TS2AD100J	6- 282	RANK	MARK	RANK	
//	6- 283	AA	İ	C	
"	6- 284	AA		C	
	6- 285	AA		С	
VRS-TS2AD101J	6- 171	AA		C	
	6- 186	A A	ļ	C	
 "	6- 243 8- 349	AA		C	
VRS-TS2AD102J	3- 224	AA		C	
"	6- 134	AA		C	
"	6- 136	AA		Č	
	6- 137	AA		С	
"	6- 164	AA		С	
	6- 179	AA		С	
<i>''</i>	6- 189	AA		C	
" "	8- 56 8- 68	AA		C	
"	8- 79	AA		C	
"	8- 84	AA		C	
VRS-TS2AD103J	6- 159	AA		C	
"	6- 163	AA		C	
"	6- 170	AA		С	
	6- 193	AA		С	
	6- 196	AA		C	
<i>"</i>	6- 198	AA	ļ	C	
<u>"</u>	6- 200	AA		C	
","	6- 201	AA	 -	C	
"	6- 203	AA		C	
//	6- 206	AA		C	
	6- 216	AA		С	
"	6- 217	AA		С	
	6- 219	AA		С	
	6- 222	AA		C	
"	6- 224	AA		- č	
"	6- 238	AA		č	
"	6- 239	AA		C	
и	6- 240	AA		c	
	6- 241	AA		Č	
1/	6- 244	AA		С	
	6- 246	AA		С	
<u>"</u>	6- 247	AA		С	
<i>"</i>	6- 248 6- 249	AA		C	
<u>"</u> ,	6- 251	AA		C	
"	6- 252	AA		c	
//	6- 253	AA		č	-
3 4	6-254	AA		Č	-
//	6- 255	AA		С	
<i>"</i>	6- 266	AA		С	
	6- 267	AA		C	
<u> </u> 	6- 268	AA		C	
"	6- 269 6- 270	AA		C C	
"	6- 287	AA		C	
"	8- 40	AA		č	
"	8- 67	AA		C	
	8- 71	AA		С	
	8- 72	AA		С	
	8- 336	AA		- Ç	
//	8- 338 8- 345	AA		C	
"	8- 356	AA		C	
"	8- 358	AA		C	
VRS-TS2AD104J	8- 74	AA		C	
	8- 82	AA		С	
// VBC TC0451051	8- 323	AA		С	
VRS-TS2AD105J	6- 227	AA		С	
<u> </u>	a- 53 8- 55	AA		C	
"	8- 55 8- 73	A A		C	
VRS-TS2AD122J	8- 325	AA		c	
VRS-TS2AD124J	3- 226	AA	+	c	
VRS-TS2AD151J	8- 76	AA		c	
VRS-TS2AD164J	8- 347	AA		С	
VRS-TS2AD182J	6- 157	AA		C	
VRS-TS2AD183J	8- 46	AA		C	
VRS-TS2AD201J	, o ^o 357	AA	A.I	C	
1 N 3 - 1 3 7 M D 7 D 1 J	6- 194 ,	ΑG	, N	С,	

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PARTS CODE	NO.	RANK	NEW PART MARK RANK	
VRS-TS2AD202J	6- 176	*A A	C	
// //	8- 185	A A AA	C	<u> </u>
VRS-TS2AD203J	o- 327	AA A A	C	
"	a- 329 I	AA	Ü	
//	I a- 333 I	AA	l c	
<i>"</i>	a-337 8- 339	AA	C	1
	8- 339 %- 341	A A A A	C	
//	8- 342	AA	C	
"	8- 354	AA	C	
VRS-TS2AD204J	8- 41 8- 66	AA	C	
VRS-TS2AD221J	3- 237	AA	C	
"	6- 187	AA	C	
	8- 59	AA	C	
<i>"</i>	8- 317 8- 318	AA	C	-
",	8- 319	AA	Č	
//	8- 320	AA	С	
//	8- 321	AA	C	
VRS-TS2AD222J	3- 225 6- 177	AA	C	
<i>"</i>	6- 178	AA		+
"	6- 180	ΑA	Č	
//	6- la4	ΑA	C	
<i>''</i>	6- 191 8- 43	A A A A	C	
"	a- 43	AA	0	+
"	a- 81	AA	C	
//	a- a5	АА	C	
VRS-TS2AD223J	6- 286	A A A A	C	
<i> </i>	8- 4 9 '	AA	C	
JJ	8- 57	ΑA	Č	
VRS-TS2AD225J	8- 54	AA	C	
// VDC TC0AD0AA	8- 331	A A	C	
VRS-TS2AD244J VRS-TS2AD271J	8- 63 6- 148	AA	C	
" " " " " " " " " " " " " " " " " " "	6- 149	AA	C	
"	6- 150	AA	C	
	6- 151	AA	C	
	6- 152 6- 153	AA	C	+
"	6- 154	AA	C	
"	6- 220	AA	С	
	6- 221	AA	<u>C</u>	
	6- 223 6- 232	AA	C	
<u>"</u>	6- 233	AA	Č	
	6- 237	AA	C	
//	6- 245	AA	C	
	6- 261	AA	C	
	6- 262 6- 263	AA	C	-
"	6- 265	AA	C	
VRS-TS2AD272J	8- 61	AA	C	
// VRS-TS2AD273J	8- 348	AA	C	
VRS-TS2AD273J	3- 232 6- 215	AA		
# # # # # # # # # # # # # # # # # # #	8- 77	AA	С	
	8- 83	AA	C	
// // // // // // // // // // // // //	8- 344 8- 78	AA	C	
VRS-TS2AD303J	8- 78	AA	C	
VRS-TS2AD330J	6- 135	AA	С	
//	6- 156	AA	C	
	6- 166	AA	C	
<i>"</i>	6- 271 6- 272	AA	C	+
" "	6- 273	AA	1 1 č	<u> </u>
"	6- 274	АА	С	
"	6- 275	AA	C	1
// //	6- 276 6- 277	A A	I C	1
"	6- 278	AA	C	
//	6- 279		· · · · · · · · · · · · · · · · · · ·	
// VDC_TC2AD2211	6- 280	A A	<u>C</u>	1
VRS-TS2AD331J		30 AA	<u>C</u>	1
LVRS-TS2AD332J_	6- 230	A A		

PARTS CODE NO. RANK MARK MARK MARK MARK MARK MARK MARK MARK MARK MARK MARK MARK MARK	· · · · · · · · · · · · · · · · · · ·		,			
\(VRS-TS2AD333 J \	PARTS CODE	NO.	PRICE	NEW	PART	
## 8-340	VRS-TS2AD3331	8- 334		MONIN		
VRS-TS2AD392J 6-188 AA C VRS-TS2AD433J 8-44 AA C VRS-TS2AD433J 8-44 AA C VRS-TS2AD471J 6-192 AA C "" 6-258 AA C "" 6-258 AA C "" 6-259 AA C "" 6-259 AA C "" 6-260 AA C "" 6-260 AA C "" 6-260 AA C "" 6-161 AA C "" 6-162 AA C "" 6-163 AA C "" 6-163 AA C "" 6-163 AA C "" 6-169 AA C "" 6-169 AA C "" 6-169 AA C "" 6-181 AA C "" 6-181 AA C "" 6-181 AA C "" 6-183 AA C "" 7 6-183 AA C "" 7 6-234 AA C "" 7 6-234 AA C "" 7 6-234 AA C "" 8-65 AG						
VRS-TS2AD392_J 6-175		8- 351	AA			
VRS-TS 2 AD 4 3 J				ļ		
\(\text{VRS-TS 2 A O 4 7 1 J} \) 6- 192 \(\text{A A} \) C \\ \(\text{V} \) 6- 258 \(\text{A A} \) C \\ \(\text{V} \) 6- 259 \(\text{A A} \) C \\ \(\text{V} \) 6- 259 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 4 7 2 J} \) 6- 162 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 4 7 2 J} \) 6- 162 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 4 7 2 J} \) 6- 163 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 4 7 2 J} \) 6- 163 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 4 7 2 J} \) 6- 163 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 4 7 3 J} \) 6- 163 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 4 7 3 J} \) 3- 231 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 4 7 3 J} \) 3- 231 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 1 J} \) 4- 6- 235 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 1 J} \) 4- 8- 48 \(\text{A G} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 4- 8- 48 \(\text{A G} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 4- 182 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 4- 182 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 4- 182 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 4- 182 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 4- 182 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 4- 182 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 6- 193 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 6- 193 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 6- 193 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 6- 193 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 6- 193 \(\text{A A} \) C \\ \(\text{VRS-TS 2 A O 5 6 J J} \) 6- 193 \(\text{A A} \) C \\(\text{VRS-TS 2 A O 6 8 J J} \) 8- 335 \(\text{A A} \) C \\(\text{VRS-TS 2 A O 6 8 J J} \) 8- 335 \(\text{A A} \) C \\(\text{VRS-TS 2 A O 6 8 J J} \) 8- 335 \(\text{A A} \) C \\(\text{VRS-TS 2 A O 6 8 S J} \) 8- 346 \(\text{A A} \) C \\(\text{VRS-TS 2 A O 6 8 J J} \) 8- 346 \(\text{A A} \) C \\(\text{VRS-TS 2 A O 6 8 J J} \) 8- 346 \(\text{A A} \) C \\(\text{VRS-TS 2 A O 6 8 J J} \) 8						
## 6-257			+	-		
## 6-258			+			
## 6-260			+			
	//	6- 259	AA		С	
VRS-TS2AD472J						
## 6-167						
## 6-168 ## AA			+	-		
## 6-169 ## A				-		
## 6-181						
## 6-183	"	6- 172	AA		С	
				<u> </u>		
VRS-TS2AD473J				<u> </u>		
## 6-234 ## AA				ļ		
				-		
VRS-TS2AD511J						
## a- 64		a- 60	, A A		С	
## A						
VRS-TS2AD561J				ļ		
VRS-TS2AD562J						
## 6- 226 ## AA				 		
## 6-229 ## AA					-	
## S	//		AA		С	
VRS-TS2AD62J 8-335 AA C VRS-TS2AD68JJ 3-355 AA C VRS-TS2AD68JJ 3-229 AA C VRS-TS2AD68JJ 8-346 AA C VRS-TS2AD751J 8-45 AA C VRS-TS2AD752J 8-69 AA C VRS-TS2AD753J 8-328 AA C VRS-TS2AD820J 8-70 AA C VRS-TS2AD820J 8-70 AA C VRS-TS2AD821J 6-244 AA C WRS-TS2AD822J 6-174 AA C WRS-TS2AD822J 6-174 AA C WRS-TS2AD812J 8-52 AA C VRS-TV2AB112J 3-236 AA C VRS-TV2AB112J 3-236 AA C VRSTS2AD1742F 6-218 AA C VRSTS2AD1742F 6-218 AA C VRSTS2ADA752F 5-200 AA C VRSTS2ADA752F 6-214 AA C VRSTS2ADA662F 6-212						
VRS-TS2AD623J 3-355 AA C VRS-TS2AD682J 3-229 AA C VRS-TS2AD685J 8-346 AA C VRS-TS2AD751J 8-45 AA C VRS-TS2AD752J 8-69 AA C VRS-TS2AD753J 8-328 AA C VRS-TS2AD820J 8-343 AA C VRS-TS2AD821J 8-343 AA C VRS-TS2AD821J 8-343 AA C W 8-352 AA C C WRS-TS2AD821J 8-350 AA C C WRS-TS2AD822J 6-174 AA C C WRS-TS2AD822J 6-174 AA C C WRS-TS2AD912J 8-350 AA C C VRS-TS2AD912J 3-236 AA C C VRS-TV2AB112J 3-236 AA C C VRSTS2AD1183F 6-218 VRSTS2AD183F C VRSTS2AD18472F 6-211 AA C VRSTS2AD4752F 6-212 AA C VRSTS2AD8662F 6-214 AA C <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>						
VRS-TS2AD682J 3-229 AA C VRS-TS2AD685J 8-346 AA C VRS-TS2AD751J 8-45 AA C " 8-75 AA C VRS-TS2AD752J 8-69 AA C VRS-TS2AD753J 8-382 AA C " 8-343 AA C " 8-343 AA C " 8-362 AA C " 8-352 AA C " 8-352 AA C " 8-352 AA C " 8-352 AA C " 8-353 AA C " 8-350 AA C VRS-TS2AD822J 6-174 AA C VRS-TS2AD912J 8-52 AA C VRS-TV2AB112J 3-236 AA C VRSTS2AD183F 6-218 C VRSTS2AD183F 6-218 C VRSTS2AD4752F 5-200 AA C				 		
VRS-TS2AD685J				+	 č -	
VRS-TS2AD751J			-+	 	+	
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0 C B U B D A A 6 R 2 C / 7 - 21 AC B 0 C B U B D A C 2 7 0 D / 7 - 18 AC B 0 C B U B D A E 1 5 0 B / 7 - 20 AD B 0 C B U B D A E 3 0 0 D / 7 - 22 A D B 0 C B U C B 0 1 1 2 A Z / 7 - 7 - A K B B 0 C B U C C 0 0 1 0 F Z / 7 - 6 A C B 0 C B U C C 0 1 3 D Z / 7 - 5 AM B 0 C B U D C 0 1 3 9 A Z / 7 - 23 A N B 0 C B U D C 0 1 3 9 A Z / 7 - 23 A N B 0 C B U E E B 2 2 2 B A / 7 - 34 A C C 0 C B U E E B 2 2 2 B A / 7 - 34 A C C 0 C B U E E B 2 2 2 B A / 7 - 35 A A C 0 C B U E E B 3 3 0 B M / 7 - 32 A A C 0 C B U E E B 3 3 0 B M / 7 - 32 A A C 0 C B U E E B 3 3 0 B M / 7 - 31 A C C 0 C B U E E B 3 3 0 B M / 7 - 32 A A C 0 C B U E E B 3 3 0 B M / 7 - 28 A A C 0 C B U E E B 3 3 0 B M / 7 - 24 A C C 0 C B U E E B 3 3 0 B M / 7 - 24 A C	0CBUBC0280BZ/	7~ 16	AC		В	
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0 C B U G A D 3 9 0 P R / 7- 481				 		
OCBUGAE122NS/ 7-53 AH C OCBUGAE221HD/ 7-54 AD C OCBUGBQ820BR/ 7-41 AP C OCBUGCF1√4-629 7-55 AC C OCBUGCQ222AQ/ 7-47 AE C OCBUGCS222AP/ 7-52 AC C OCBUGCU103BC/ 7-42 AD C			AC		С	
0 CBUGAE221HD/ 7-54 AD C 0 CBUGBQ820BR/ 7-41 AP C 0 CBUGCF1 √4 € ∂5 / 7-55 AC C 0 CBUGCQ222AQ/ 7-47 AE C 0 CBUGCS222AP/ 7-52 AC C 0 CBUGCU103BC/ 7-42 AD C						
0 CBUGBQ8 2 0 BR/ 7-41 AP C 0 CBUGCF1 \(\sigma \text{+ UDS} \) \(\frac{1}{7} - 55\) AC C 0 CBUGCQ 2 2 2 AQ/ 7-47 AE C 0 CBUGCS 2 2 2 AP/ 7-52 AC C 0 CBUGCU103BC/ 7-42 AD C						
0 CBUGBQ820BR/ 7-41 AP C 0 CBUGCF1 v-4 b 5 / 17-55 AC C 0 CBUGCQ222AQ/ 7-47 AE C 0 CBUGCS222AP/ 7-52 AC C 0 CBUGCU103BC/ 7-42 AD C	OCBUGAE221HD/	7- 54	A D		C	
OCBUGCF1		7- 41			C	
0 C B U G C Q 2 2 2 A Q / 7- 47 A E C 0 C B U G C S 2 2 2 A P / 7- 52 A C C 0 C B U G C U 1 0 3 B C / 7- 42 A D C	OCBUGCF1 V4 658 /	_				
0 CBUGCS222AP/ 7-52 AC C 0 CBUGCU103BC/ 7-42 AD C	OCBUGCQ222AO/					
0 C B U G C U 1 0 3 B C / 7- 42 A D C						
				l		1
	OCBUGCU103BC/	/ - 4/	A 1)			

DADTS CODE	NO.	PRICE	NEW	PART	
PARTS CODE		RANK	MARK		
OCBUGCZ222CK/	7- 40	AF		С	
OCBUGFF102ER/	7- 46	A C	ļ <u>.</u>		
OCBUGFF103ER/	7- 44	A C		C	
0 C B U G F F 6 8 3 E R / 0 C B U G F Z 2 2 4 F Y /	7- 45 7- 39	AD		C	ļ
0 CBUK Z 0 5 8 2 Z Z/		AG		C_	
0 C B U Z Z O 1 O O Z Z /	7- 2 7- 57	AH		C B	
0 CB 8 2 9 8 2 0 3 6 3/	7- 1	BC		В	
0 J Z C 2 1 4 4 6 7 8 1 0	3- 217	ВН		C	
0 J Z C 2 1 4 4 6 7 9 0 3	3- 22	AN		C	
0 J Z C 2 1 4 4 6 7 9 0 4	3- 3	BL		C	
0 J Z C 2 1 4 4 6 7 9 1 0	3- 45	BX		Č	
0 J Z C 2 1 4 4 6 7 9 1 1	3- 32	AF		Č	
0 J Z C 2 1 4 4 6 7 9 1 2	3- 30	AG		Č	
0 J Z C 2 1 4 4 6 7 9 1 4	3- 38	BK		С	
0 J Z C 2 1 4 4 6 7 9 1 5	3- 31	AG		С	
0 J Z C 2 1 4 4 6 7 9 1 6	3- 11	AY		С	
0 J Z C 2 1 4 4 6 7 9 1 7	3- 15	ВМ		В	
0 J Z C 2 1 4 4 6 7 9 1 8	3- 29	AL		С	
0 J Z C 2 1 4 4 6 7 9 2 0	3~ 5	BK		С	
0 J Z C 2 1 4 4 6 7 9 2 2	3- 37	BF		С	
0 J Z C 2 1 4 4 6 7 9 2 3	3- 17	AB		C	
0 J Z C 2 1 4 4 6 7 9 2 6	3- 47	CG		Ε	
0 J Z C 2 1 4 4 6 7 9 2 8	3- 35	AQ		С	
0 J Z C 2 1 4 4 6 7 9 2 9	3- 19	AV		С	\.
0 J Z C 2 1 4 4 6 7 9 3 2	3- 33	AN		С	
0 J Z C 2 1 4 4 6 7 9 3 3	3- 28	AT		С	
0 J Z C 2 1 4 4 6 7 9 3 4	3- 21	AY		С	
0 J Z C 2 1 4 4 6 7 9 3 5	3- 20	AY		С	
0 J Z C 2 I 4 4 6 7 9 3 6	3- 39	BE		С	
0 J Z C 2 1 4 4 6 7 9 3 8	3- 46	BF		С	
0 J Z C 2 1 4 4 6 7 9 4 0	3- 23	BC		С	
0 J Z C 2 1 4 4 6 7 9 4 2	3- 2	BC		С	
0 J Z C 2 1 4 4 6 7 9 4 3	3- 6	AY		С	
0 J Z C 2 1 4 4 6 7 9 4 4 0 J Z C 2 1 4 4 6 7 9 4 5	3- 24	AP		C	
0 J Z C 2 1 4 4 6 7 9 4 6	3- 18	AP		C	
0 J Z C 2 1 4 4 6 7 9 4 7	3- 101 3- 12	CH		C	
0 J Z C 2 1 4 4 6 7 9 4 8	3- 12	AW		C	
0 J Z C 2 1 4 4 6 7 9 4 9	3- 43	AW		C	
0 J Z C 2 1 4 4 6 7 9 5 0	3- 42	AG		C	
0 J Z C 2 1 4 4 6 7 9 5 2	3- 41	AW		c	
0 J Z C 2 1 4 4 6 7 9 5 3	3- 44	AH		C	
0 J Z C 2 1 4 4 6 7 9 5 4	3- 13	ΑÜ		C	
0 J Z C 2 1 4 4 6 7 9 5 5	3- 201	2.1		 ~ 	
0 J Z C 2 1 4 4 6 7 9 5 6	3- 202	AG		B	
0 J Z C 2 1 4 4 6 7 9 5 7	3~ 203	, A ,H (C (
0 J Z C 2 1 4 4 6 7 9 5 8	3- 204	AL		č	
0 J Z C 2 1 4 4 6 7 9 5 9	3- 205	AQ		č	
0 J Z C 2 1 4 4 6 7 9 6 0	3- 206	AK		č	
JZC214467961	3- 207	AK		Č	
JZC214467962	3- 4	AR		č	
0 J Z C 2 1 4 4 6 7 9 6 3	3- 36	ΑE		č	
0 J Z C 2 1 4 4 6 7 9 6 4	3- 10	AM		č	
JZC214467965	3- 7	AH		С	
) JZC214467966	3- 8	AS		C	
)JZC214467967	3- 9	ΑQ		С	
) J Z C 2 1 4 4 6 7 9 6 8	3- 16	ΑQ		С	
)JZC214467969	3- 40	A D		С	
//	3- 218	A D		С	
JZC214467970	3- 1	ΑE		С	
JZC214467971	3- 27	AD		С	
) J Z C 2 1 4 4 6 7 9 7 2	3- 34	A D		С	
JZC214467973	3- 26	AL	1	С	
JZC214467975	3- 25	AM		С	
JZC214467980	3- 208	BF		8	
JZC214467982	3- 209	, 28_L ,	<u>, </u>	1 28	
JZC214467983	3- 210		<u> </u>		<u> </u>
JZC214467984	3- 211	AX		В	
JZC214467985	3- 212	BE		8	
DJZC214467986	3- 213	BE		В	
JZC214467987	3- 214	AF		В	
) J Z C 2 1 4 4 6 7 9 8 8	3- 215	ا الم	1	<u> </u>	
JJZC214467989	3- 216	AT	!	<u> </u>	
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- CAUTION FOR BATTERY REPLACEMENT -

(Danish)

ADVARSEL!

Lithiumbatteri – Eksplosionsfare ved fejlagtig handtering.
Udskiftning må kun ske med batten
af samme fabrikat og type.

Lever det brugte batten tilbage til ieverandoren.

(English)

Caution!

Danger of explosion if battery is incorrectly replaced.

Replace only with the same or equivalent type recommended by the equipment manufacturer.

Discard used batteries according to manufacturer's instructions.

(Finnish)

VAROITUS

Paristo voi räjähtää, jos se on virheellisesti asennettu. Vaihda paristo ainoastaan laitevalmistajan suosittelemaan tyyppiin. Hävitä käytetty paristo valmistajan ohjeiden mukaisesti.

(French)

ATTENTION

Il y a danger d'explosion s' il y a remplacement incorrect de la batterie. Remplacer uniquement avec une batterie du même type ou d'un type recommande par le constructeur. Mettre au rebut les batteries usagées conformement aux instructions du fabricant.

(Swedish)

VARNING

Explosionsfare vid felaktigt battenbyte.

Använd samma batterityp eller en ekvivalent typ som rekommenderas av apparattfllverkaren.

Kassera använt batten enligt fabrikantens instruktion.



00ZUX114A3M/E 00ZF0235A/BME

SHARP

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