## SHARP SERVICE MANUAL



## FACSIMILE model UX-B800A

| MODEL | SELECTION CODE | DESTINATION |
| :---: | :---: | :---: |
| UX-B800 | A | Australia |

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## CHAPTER 1. GENERAL DESCRIPTION

## [1] Caution

## 1. Caution for Battery replacement

## (Danish) ADVARSEL!

Lithiumbatteri-Eksplosionsfare ved fejlagtig håndtering. Udskiftning må kun ske med batteri af samme fabrikat og type. Levér det brugte batteri tilbage til leverandoren.
(English) Caution!
Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the equipment manufacturer. Discard used batteries according to manufacturer's instructions. (Finnish)

VAROITUS
Paristo voi räjähtää, jos se on virheellisesti asennettu.
Vaihda paristo ainoastaan laitevalmistajan suosittelemaan tyyppiin. Hävitä käytetty paristo valmistajan ohjeiden mukaisesti.

## (French) ATTENTION

Il y a danger d'explosion s' il y a remplacement incorrect de la batterie. Remplacer uniquement avec une batterie du même type ou d'un type recommandé par le constructeur. Mettre au rébut les batteries usagées conformément aux instructions du fabricant. (Swedish) VARNING

Explosionsfare vid felaktigt batteribyte.
Använd samma batterityp eller en ekvivalent typ som rekommenderas av apparattillverkaren.

Kassera använt batteri enligt fabrikantens instruktion.

## (German) Achtung

Explosionsgefahr bei Verwendung inkorrekter Batterien.
Als Ersatzbatterien dürfen nur Batterien vom gleichen Typ oder vom Hersteller empfohlene Batterien verwendet werden. Entsorgung der gebrauchten Batterien nur nach den vom Hersteller angegebenen Anweisungen.

## 2. Precautions for using Lead-Free Solder

1. Employing lead-free solder

The Control PWB, LIU PWB, Power Supply PWB and Operation Panel PWB of this model employs lead-free solder. This is indicated by the "LF" symbol printed on the PWB and in the service manual.
The suffix letter indicates the alloy type of the solder.
Example:


Indicates lead-free solder of tin, silver and copper.

## 2. Using lead-free solder

When repairing a PWB with the "LF" symbol, only lead-free solder should be used. (Using normal tin/lead alloy solder may result in cold soldered joints and damage to printed patterns.)
As the melting point of lead-free solder is approximately $40^{\circ} \mathrm{C}$ higher than tin/lead alloy solder, it is recommended that a dedicated bit is used, and that the iron temperature is adjusted accordingly.

## 3. Soldering

As the melting point of lead-free solder (Sn-Ag-Cu) is higher and has poorer melting point (flow), to prevent damage to the land of the PWB, extreme care should be taken not to leave the bit in contact with the PWB for an extended period of time. Remove the bit as soon as a good flow is achieved.
The high content of tin in lead free solder will cause premature corrosion of the bit.
To reduce wear on the bit, reduce the temperature or turn off the iron when it is not required.
Leaving different types of solder on the bit will cause contamination of the different alloys, which will alter their characteristics, making good soldering more difficult.
It will be necessary to clean and replace bits more often when using lead-free solder. To reduce bit wear, care should be taken to clean the bit thoroughly after each use.

## [2] Specifications

| Print cartridge yield ${ }^{1,2}$ : | Replacement cartridge: SHARP UX-C80B <br> Ink Save mode ON: Approx. 800 A4 pages <br> Ink Save mode OFF: Approx. 480 A4 pages <br> (The initial cartridge has the same yield as the above replacement cartridge.) |
| :---: | :---: |
| Paper tray capacity: | Approx. 200 A 4 sheets ( 60 to $80 \mathrm{~g} / \mathrm{cm}^{2}$ ) (At room temperature; maximum stack height should not be higher than the line on the tray) |
| Recording system: | Thermal inkjet |
| Print resolution: | $600 \times 600 \mathrm{dpi}$ |
| Effective printing width: | 203 mm max. |
| Scan to E-mail: | Yes; requires an Internet connection and a router, switch, or hub to connect the machine. LAN cable (10Base-T straight-through cable) must be purchased separately. <br> LAN interface: 10Base-T IEEE802.3 |
| File formats: | Tiff (G4), PDF (G4) |
| Memory capacity ${ }^{2}$ : | Approx. 100 average pages |
| Modem speed: | 14,400 bps with auto fallback to lower speeds. |
| Transmission time ${ }^{2}$ : | Approx. 6 seconds |
| Compatibility: | ITU-T (CCITT) G3 mode |
| Compression scheme: | MR, MH, MMR |
| Storable transmission destinations: | Rapid Keys: 36 A, B, C keys: 3(e-mail only) Speed Dial numbers: 63 |
| Telephone function: | Yes (cannot be used if power fails) |
| Effective scanning width: | 208 mm max. |
| Applicable telephone line: | Public switched telephone network |
| Reception modes: | AUTO, MANUAL, TEL/FAX |
| Automatic document feeder: | A4: 20 sheets max. (60-80 g/cm². paper; temperature: $18-28^{\circ} \mathrm{C}$; humidity: $45-65 \% \mathrm{RH}$ ) |
| Input document size: | Automatic feeding: <br> Width: 148 to 210 mm <br> Length: 140 to 297 mm <br> Manual feeding: <br> Width: 70 to 210 mm <br> Length: 140 to 356 mm |
| Scanning Resolution: | Standard: $203 \times 98$ dpi <br> Fine: $203 \times 196$ dpi <br> Super fine: $203 \times 391$ dpi <br> Halftone: $203 \times 196$ dpi |
| Halftone (grayscale): | 64 levels |
| Contrast control: | Automatic/Dark selectable |
| Copy function: | Single/Multi/Sort (99 copies/page) |
| Display: | LCD display, 20 digits $\times 2$ lines |
| Power requirements: | $230-240$ V AC, 50 Hz |
| Power consumption: | Standby: 6.0 W, Maximum: 40 W |
| Operating temperature: | 15-32 ${ }^{\circ} \mathrm{C}$ |
| Humidity: | 25-80\% RH |
| Dimensions (without attachments): | Width: 380 mm Depth: 280 mm Height: 156 mm |
| Weight (without attachments): | Approx. 4.5 kg |
| ${ }^{1}$ Ink Save mode is initially turned off. To turn on Ink Save mode. <br> 2Based on Sharp Standard Chart at standard resolution, excluding time for protocol signals (i.e., ITU-T phase C time only). |  |

[^1]
## [3] Operation panel



Note: Affix the Rapid Key labels as shown above.

1. Number key: Use to dial numbers.
2. SCAN TO E-MAIL key: Press to send a document to an e-mail recipient.
3. Display: This displays messages to help you operate the machine.
4. A, B, C keys: An e-mail address can be stored in each key for one-touch selection.
5. RESOLUTION/RECEPTION key: When a document is in the feeder, press to adjust the scanning resolution. At any other time, press to select the reception mode.
6. FUNCTION key: Press this key followed by the arrow keys to select special functions and settings.
7. COPY/HELP key: When a document is in the feeder, press to make a copy. At any other time, press to print the Help List.
8. STOP key: Press to cancel an operation before it is completed.
9. Group keys: Multiple fax or e-mail destinations can be stored in each key for one-touch selection.
10. Rapid keys/Letter keys: A fax number or e-mail address can be stored in each of these keys for one-touch selection (attach the Rapid Key labels). When entering text, the keys are used as letter keys.
11. BROADCAST key: Press to send to multiple fax or e-mail destinations.
12. SECURE key: Press to use the fax secure receive function.
13. DUPLEX key: Use to scan two-sided documents for transmission and copying.
14. SPEED DIAL key: Press to select a fax or e-mail destination stored in a 2 -digit Speed Dial number.
15. HOLD key: Press to put a phone call on hold.
16. SPEAKER key: Press to listen to the line and fax tones through the speaker when faxing.
Note: This is not a speakerphone. It cannot be used for speaking.
17. START/MEMORY key: Press after dialing to send a fax. Press before dialing to send a fax through memory. Press in the date and time display to show the percentage of memory currently used.
18. Arrow key: Use to scroll through and select settings, and to search for stored destinations.

## Letter keys

Use these keys when the display prompts you to enter a name, e-mail address, or other text.


1. SYMBOL key: Press to enter a symbol (the character appearing on the right side of each letter key). Press again to return to normal letter entry mode.
2. Caps Lock key: Press to enter upper case letters. Press again to return to lower case letter entry.
3. SHIFT key: Hold down to temporarily change case.
4. Letter keys: Use to enter letters.
5. @ key, .com key: Press to enter "@" and ".com" when entering an e-mail address.
6. DEL key: Press to delete the character marked by the cursor. When the cursor is to the right of a line of text, press to backspace and clear characters. (The cursor can be moved by pressing or down until all characters are cleared.

## Monitoring phone conversations

When speaking through the handset, you can press $\square_{\square}^{\text {SPEAKR }}$ to allow a third person to listen to the conversation through the speaker. (To turn off the speaker, press the key again.)
To adjust the volume of the speaker when monitoring a conversation, press - or (the volume reverts to the lowest setting each time the handset is replaced).
Note that the speaker cannot be used for speaking; it is only for listening.
To avoid feedback (a loud howling sound), be sure to turn off the speaker (press $\square_{\square}^{\text {SPEAKR }}$ once again) before you replace the handset.

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## [4] Transmittable documents

## 1. Document Sizes

| Normal size | Width | $148-210 \mathrm{~mm}$ |
| :--- | :---: | :---: |
|  | Length | $140-297 \mathrm{~mm}$ |



XX Use document carrier sheet for smaller documents.

- With special sizes, only one sheet can be fed into the machine at a time. Insert next page into feeder as current page is being scanned.


## 2. Paper Thickness \& Weight

|  | 10 sheets | 1 sheet (Manual) |
| :--- | :--- | :--- |
| Paper weight | 20 lbs. | $14 \mathrm{lbs} \sim 42 \mathrm{lbs}$. |
|  | $\left(74.3 \mathrm{~g} / \mathrm{m}^{2}\right)$ | $\left(52 \mathrm{~g} / \mathrm{m}^{2} \sim 157 \mathrm{~g} / \mathrm{m}^{2}\right)$ |
|  | at the environment |  |
|  | $23 \pm 5^{\circ} \mathrm{C}$ |  |
|  | $55 \pm 10 \%$ | $0.06 \mathrm{~mm} \sim 0.18 \mathrm{~mm}$ |
| Paper thickness (ref.) | 0.09 mm |  |
| Paper size | A4 $(210 \mathrm{~mm} \times 297 \mathrm{~mm})$ |  |
| Feeder capacity | A4: 10 sheets max. |  |

## 3. Document Types

- Normal paper

Documents handwritten in pencil (No. 2 lead or softer), fountain pen, ball-point pen, or felt-tipped pen can be transmitted.
Documents of normal contrast duplicated by a copying machine can also be transmitted.

- Diazo copy (blue print)
- Diazo copy documents of a normal contrast may be transmitted.
- Carbon copy

A carbon copy may be transmitted if its contrast is normal.

## 4. Cautions on Transmitting Documents

- Documents written in yellow, greenish yellow, or light blue ink cannot be transmitted.
- Ink, glue, and correcting fluid on documents must be dry before the documents can be transmitted.
- All clips, staples and pins must be removed from documents before transmission.
- Patched (taped) documents should be copied first on a copier and then the copies used for transmission.
- All documents should be fanned before insertion into the feeder to prevent possible double feeds.


## 5. Automatic Document Feeder Capacity

Number of pages that can be placed into the feeder at as follows:
Normal size: max. ADF 20 pages

- Temperature: $18 \sim 28^{\circ} \mathrm{C}$
- Humidity: $45 \sim 65 \%$

Special size: single sheet only (manual feed)

## NOTE

- When you need to send or copy more pages than the feeder limit, place additional pages in feeder when last page in feeder is being scanned.
- Place additional pages carefully and gently in feeder. If force is used, double-feeding or a document jam may result.


## 6. Readable Width \& Length

The readable width and length of a document are slightly smaller than the actual document size.
Note that characters or graphics outside the effective document scanning range will not be read.

- Readable width 208mm, max



## - Readable length

This is the length of the document sent minus 4 mm from the top and bottom edges.


## [5] Installation

## 1. Site selection

Take the following points into consideration when selecting a site for this model.

## ENVIRONMENT

- The machine must be installed on a level surface.
- Keep the machine away from air conditioners, heaters, direct sunlight, and dust.
- Provide easy access to the front, back, and sides of the machine. In particular, keep the area in front of the machine clear, or the original document may jam as it comes out after scanning.
- The temperature should be between $15-32^{\circ} \mathrm{C}$.
- The humidity should be between $25 \%$ and $80 \%$ (without condensation).


## ELECTRICITY

AC 230-240 V, 50 Hz , earthed AC outlet is required.

## Caution!

- Connection to a power source other than that specified will cause damage to the equipment and is not covered under the warranty.
- If your area experiences a high incidence of lightning or power surges, we recommend that you install a surge protector for the power and telephone lines. Surge protectors can be purchased at most telephone specialty stores.
If the machine is moved from a cold to a warm place...
Condensation may form on the reading glass if machine is moved from a cold to a warm place, this will prevent proper scanning of documents for transmission. Turn on the power and wait approximately 2 hours before using machine.


## TELEPHONE JACK

A standard telephone jack must be located near the machine. This is the telephone jack commonly used in most homes and offices.

- Plugging the fax machine into a jack which is not a standard analog telephone jack, may result in damage to the machine or your telephone system.


## 2. Removing the packing tape

1) Open the cover ( (1) ) and remove the tape ( 2 ). (Do not install the print cartridge yet.)

2) Close the cover.


## 3. Connect the handset



## 4. Attaching the tray

1) Attach the received document tray

2) Attach the paper tray


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## 5. Loading Paper

Approximately 200 sheets of A4-size paper ( $60-80 \mathrm{~g} / \mathrm{m}^{2}$ ) can be loaded in the paper tray (at room temperature; maximum stack height should not be higher than the line on the tray).

1) Fan the paper, and then tap the edges against a flat surface to even the stack.

2) Pull the paper plate forward and insert the stack of paper into the tray, print side up.


- If paper remains in the tray, take it out and combine it into a single stack with the new paper.
- Printing on the reverse side of the paper may result in poor print quality.
- Do not use paper that has already been printed on or is curled.
- Remove received faxes and other printed output before about 50 sheets accumulate in the received document tray (otherwise the sheets may scatter).

If at any time the display shows [ADD PAPER \&] / [PRESS START KEY], check the paper tray. If the tray is empty, add paper. If there is paper in the tray, take it out and then reinsert it.


## 6. Connecting the power cord

Caution! Remove all packing tape before plugging in the power cord, or machine to the machine may result.

- The power outlet must be installed near the equipment and must be easily accessible.
Plug the plug adapter into a $230-240 \mathrm{~V}, 50 \mathrm{~Hz}$, earthed (3-prong) AC outlet.

- The display will show [CHECK CARTRIDGE] until you install the print cartridge.

When disconnecting the machine, always disconnect the phone line before you disconnect the power cord. When re-connecting the machine, always connect the power cord before connecting the phone line.

It is recommended that you install surge protectors for the power and telephone lines. Surge protectors can be purchased at most telephone specialty sotres.

## 7. Installing the print cartridge

## Print cartridge yield

## SHARP UX-C80B replacement cartridge

Ink Save mode OFF: Approx. 480 A4 pages Ink Save mode ON: Approx. 800 A4 pages (The initial cartridge has the same yield as the replacement cartridge.)

- Ink Save mode is initially turned off. To turn on Ink Save mode.

Caution! Do not open the cover while the machine is printing.
Note: Keep ink cartridges sealed in their packages until you are ready to install them. It is recommended that you do not use a cartridge that has been left unused for a long time after opening, as the print quality may be considerably degraded.

- Make sure the machine's power cord is plugged in and paper is loaded in the paper tray.

1) Open the cover (grasp the finger grip).


[^2]2) Remove only the tape from the new cartridge.

- Make sure all tape is removed.

- CAUTION! DO NOT touch the gold contact area on the cartridge.


3) Make sure the cartridge holder is in the position shown. Place your fingers on top of the holder and open the holder release.


- CAUTION! DO NOT touch the gold contact area or pull on the cable.
- If you are replacing the cartridge, remove the old cartridge. If you are going to use the old cartridge again, place it in an airtight container (containers for this purpose can be purchased at most office supply stores).

4) Insert the new print cartridge ( 1 ). Close the holder ( 2 ), making sure it clicks into place.

5) Close the cover, pressing firmly on both front corners.


Display: [CHANGE CARTRIDGE]/[1=NEW, 2=OLD].
6) Press $\square$ (NEW) if the cartridge you installed is new.

Display: [NEW CARTRIDGE?] / [OK:PRESS START]
Press 2 (OLD) if the cartridge you installed is old.
7) Press


If you selected "OLD" in the previous step, this completes the installation procedure. (Note: If you find that print quality is not satisfactory, align the print cartridge.)
If you selected "NEW", the machine will print an alignment page. Continue with the steps below.
8) In the alignment page, locate the line that is closest to a straight line.

ALIGNMENT TEST SHEET


In this example, "15" is closest to a straight line.
9) Enter the number of the straightest line with the number keys.

- To clear a mistake, press

10)Press


The machine is initially set to print high quality images, which require more ink. If you prefer to use less ink and speed up drying time, change the "INK SAVE MODE" setting to "ON".

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## 8. Connecting the Phone Line

Connect the phone line cord to the TEL. LINE socket and a wall telephone socket (use the provided adapter if needed).


The machine is initially set for tone dialing. If you are on a pulse dial line, change the "DIAL MODE" setting.

It is recommended that you install surge protectors for the power and telephone lines. Surge protectors can be purchased at most telephone specialty stores.

## [6] Scan to E-mail

The Scan to E-mail feature lets you scan a document and directly send the image to an e-mail recipient.

- This saves you the trouble of scanning the document into a computer and sending the scanned file using an e-mail program.
- The scanned document is sent as an attachment to an e-mail message. The attached file can be in TIFF or PDF format.


## Requirements

To use Scan to E-mail, you must.

- Have an Internet connection.
- Have a broadband router or LAN switch/hub through which you can connect the machine to the Internet (the router is usually connected to a cable modem or DSL modem).
A typical setup is shown below.


Note: The Scan to E-mail function is not compatible with mail servers that require security protection (SSL).

## 1. Connecting the Machine

The machine is connected using an Ethernet cable (not included). Please purchase a 10Base-T straight-through (regular) cable that supports 10Base-T.
Connect one end of the cable to the LAN port on the machine.


Connect the other end to a LAN port on your router, switch, or hub.

- If you are uncertain which port to connect the cable to, see the manual for the router, switch or hub.
- The connections can be made with all devices powered on.
- For other questions about how the machine should be connected, consult your Internet service provider.


## 1. IP address

The machine requires an IP address to communicate on the Internet.
If your network is set up to automatically assign IP addresses, you do not need to do anything; the machine will automatically receive an IP address when it is connected.

Automatic assignment of IP addresses is normally performed by a DHCP server, which may be a computer, router, or other device on the network. (Most routers used on home networks contain a DHCP server.)

If your network requires that a static (permanent) IP address be assigned to the machine, see Network Settings to enter the IP address and related information.
2. Checking your IP address and the connection

To check the machine's IP address and verify that the machine is able to communicate on the network, follow these steps.

2) 2 Press TION]
3) 3 Press


- If the machine has automatically received an IP address and is able to communicate, [IP ADDRESS/DHCP: ON] will appear on the top line of the display, followed by the IP address on the bot-
tom line. To show the machine's host name, press
- If a static IP address has been entered in the machine and the machine is able to communicate, [IP ADDRESS/DHCP: OFF] will appear on the top line of the display, followed by the IP address on the bottom line. To show the machine's host name,
press
- If the machine was unable to receive an IP address automatically, or if the router locked up after the machine received an IP address, [IP ADDRESS/DHCP: FAIL] / [192.168. 1 .201] will appear. "192.168. 1 .201" is an IP address that is assigned to the machine for diagnostic purposes when connection fails (it cannot be used for regular communication).
- If [NOW CONNECTING/CHECK LATER] appears, press
to exit. Make sure the cable is connected correctly, wait briefly, and then repeat the procedure again.

3. If the machine is unable to communicate...

- Check the Ethernet cable. Are the connections secure? Are you using straight-through (not crossover) cable for 10Base-T?
- There may be a problem in the router or other device to which the machine is connected. Try removing and then restoring the power to the router (see the manual for the router).
- If [IP ADDRESS/DHCP: FAIL] appears, remove and then reinsert the Ethernet cable plug, and repeat the procedure to check the machine's IP address. If the same message appears, check the device that acts as the DHCP server on your network (try removing and restoring the power). If your router is your DHCP server, make sure that DHCP is enabled in the router settings. If your network does not have a DHCP server, set a static IP address as explained.
- If a static IP address has been set, make sure that the network settings are correct.


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## 2. E-mail Settings

Before the Scan to E-mail function can be used, you must configure the e-mail settings.
The e-mail settings specify your outgoing mail server, as well as your sender name, sender address, and other items that appear in the email message to which scanned image files are attached.

- It is easiest to configure the e-mail settings in the machine's Web page. The machine's Web page can be accessed from any computer on the same network as the machine.
- The e-mail settings can also be configured using the machine's operation panel.

Note: If your Internet service provider requires that you configure "SMTP Authentication" or "POP before SMTP" settings, you must use the Web page.

If you do not know your SMTP server name or the settings that are required, ask your Internet service provider.

## 1. Accessing the machine's Web page

To access the Web page, you will need to know the machine's host name or IP address (usually it is most convenient to use the host name).
To check the machine's host name or IP address, follow the procedure in Checking your IP address and the connection, or print the Network Settings List.

Type in the host name or IP address of the machine in the Address bar of the Web browser on your computer.

The Web page menu will appear. Select E-mail Setup and configure the settings. For explanations of the settings, click Help in the Web page menu.
Note: If the Web page is used while the machine is in operation (except for voice calls using the handset), machine operation may become unstable.

## 2. Using the machine's operation panel to configure the settings

Follow the steps below to display the e-mail settings, and then configure each setting as explained in the table on the next page.

1) Press $\square^{\text {FUnction }}$, then $-\square$ until [E-MAIL SETTING] appears.
2) Pres
 repeatedly until the setting that you wish to configure appears.
3) Press (Note: This step is not necessary for the "FILE FORMAT" setting.)
4) Enter the required information for the setting as explained in the table on the next page.
5) After configuring the setting, the next e-mail setting will appear in the display. You can configure the next setting, or press $\theta$ to exit.

## 3. E-mail settings

Enter the required information for each setting as explained below.
This is done in step 4 of the procedure on the previous page.

- The sender address and SMTP server settings are required. The other settings are optional.
- Use the letter keys to enter text. For information on using the letter keys.
- The e-mail subject, e-mail message, and file format that you enter below are default settings. They will be used if you do not specify these items at the time of transmission.

| Setting | Description |
| :---: | :---: |
| SENDER NAME | Enter the sender name that will appear in the e-mail message (max. of 50 characters). When finished, press $\begin{gathered}\text { siveraif } \\ \text { (i) }\end{gathered}$. |
| SENDER ADDRESS | Enter your sender e-mail address (max. of 128 characters). When finished, press $\square$ (Note: Be sure to enter your actual e-mail address. Return e-mail and undelivered mail reports will be sent to this address. In addition, some STMP servers reject outgoing email if a valid sender e-mail address is not entered.) |
| E-MAIL SUBJECT | Enter a default subject (max. of 128 characters). When finished, press <br>  $\square$ |
| E-MAIL MESSAGE | Enter a default message (max. of 256 characters). When finished, press $\square$ START' MEMOAY . |
| FILE FORMAT | This sets the default format of the file that is created when you scan a document. Press 1 for TIFF or 2 for PDF. Initial setting: TIFF |
| SMTP SERVER | Enter the host name or IP address of your outgoing (SMTP) mail <br>  |

## [7] Clearing paper jams

## 1. Clearing a jammed document

If the original document doesn't feed properly during transmission or copying, or [DOCUMENT JAMMED] appears in the display, first try pressing $\square$. If the document doesn't feed out, remove it as explained below.
Important: Do not try to remove a jammed document without releasing it as explained below. This may damage the feeder mechanism.

1) Open the cover ( (1) ). Rotate the lever so that points straight up ( (2) ), and pull it up ( (3). Pull the roller out to the right, and remove the document ( (4) ).

2) Replace the white roller, inserting the left end first. Push the lever down and rotate it back down ( (1) ). Close the cover, pressing firmly on both front corners (2).


## [8] Quick reference guide

## 1. Fax / Scan to E-mail Transmission

Place your document (up to 20 pages) face down in the document feeder.


## 2. Sending a Fax by Normal Dialing

1. Lift the handset or press

2. Dial the fax number.
3. Wait for the reception tone (if a person answers, ask them to press their Start key).


## 3. Using a Rapid Key (fax/e-mail)

Press the Rapid Key for the destination.
4. Using a Speed Dial number (fax/e-mail)

1. Press

2. Enter the appropriate 2-digit Speed Dial number and press | STAPT/ |
| :---: | :---: |
| MEMORY |
| (1) | .

## 5. Searching for a destination (fax/e-mail)

1. Press $\square$ or $\square$ until the desired destination appears.
2. Press $\stackrel{\substack{\text { STARAT/ } \\ \text { MEMOAY } \\ \text { (1) }}}{ }$

## 6. Receiving Faxes

Press $\square^{\substack{\text { RESOLUTION } \\ \text { RECEPTIONODE }}}$ until the desired reception mode appears in the display.
AUTO mode: The machine automatically answers and receives faxes. MANUAL mode: Answer all calls (even faxes) by picking up the handset. To begin fax reception, press $\qquad$
TEL/FAX mode: The machine automatically answers and receives faxes. Voice calls are signalled by a special ringing sound.

## 7. Storing Destinations


2. Press an unused Rapid Key, or press $\square^{\text {SPEED DIAL }}$ and enter an unused 2-digit Speed Dial number (01 to 63). (For an e-mail destination, you can also press the A, B or C key and go to Step 4.)
3. Press 1 for fax or 2 for e-mail.
4. Enter the fax number or the e-mail address.
5. Press $\begin{gathered}\substack{\text { START/ } \\ \text { MEMORY } \\(1)}\end{gathered}$
6. Enter a name with the letter keys.
7. Press $\begin{gathered}\substack{\text { sTARFA } \\ \text { MEMORY } \\(1)}\end{gathered}$ and then $\theta$ stop

## CHAPTER 2. ADJUSTMENTS

## [1] Adjustments

## 1. General description

Since the following adjustments and settings are provided for this model, make adjustments and/or setup as necessary.

## 2. Adjustments of output voltage (FACTORY ONLY)

1. Install the power supply unit in the machine.
2. Set the recording paper and document.
3. When the document is loaded, power is supplied to the output lines. Confirm that outputs are within the limits below.

### 2.1. Output voltage settings



| Output | Voltage limits |
| :---: | :---: |
| +5 V | $4.75-5.25 \mathrm{~V}$ |
| +30 V | $27.0-33.0 \mathrm{~V}$ |


| Pin No. | CNPW |
| :---: | :---: |
| 1 | +30 V |
| 2 | +30 V |
| 3 | MG |
| 4 | MG |
| 5 | DG |
| 6 | DG |
| 7 | +5 V |
| 8 | +5 V |

## 3. IC protectors replacement

ICPs (IC Protectors) are installed to protect the CIS and Ope amp. circuit. ICPs protect various ICs and electronic circuits from an overcurrent condition.
The location of ICPs are shown below:


1) FU900 (KAB5002) is installed in order to protect overcurrent +24 V supply. If FU900 is open, replace it with a new one.

## 4. Settings

### 4.1. Dial mode selector

DIAL mode (Soft Switch No. SW-9 Data No. 8)
Use this to set the fax machine to the type of telephone line you are on.

- The factory setting is "TONE".
(step 1) Select "OPTION SETTING".
$\begin{array}{ll}\text { KEY : } & \text { FUNCTION } \rightarrow \boldsymbol{\nabla}, \boldsymbol{\nabla}, \boldsymbol{\nabla}, \boldsymbol{\nabla} \\ \text { DISPLAY: } & \text { OPTION SETTING } \quad \text { PRESS } \rightarrow \text { KEY } \quad \text { - }\end{array}$
(step 2) Select "DIAL MODE"
KEY:

| $\boldsymbol{\nabla}, \boldsymbol{\nabla}, \boldsymbol{\nabla}, \boldsymbol{\nabla}$ |
| :--- |
| DIAL MODE |
| 1: TONE, 2: PULSE |

(step 3) Select, using "1" or "2".
KEY: (1)
DISPLAY: TONE
KEY:
(2)

DISPLAY: PULSE
(step 4) End, using the "STOP" key.
(Press the STOP key three times.)
KEY: ${ }^{\text {STOP }}$

## 5. Volume adjustments

### 5.1. Speaker volume

1 Press $\square^{\text {SPEAKER }}$.
2 Press $-\underset{\square}{-\dot{C}}$ or to select HIGH, MIDDLE, or LOW.

- Press Speaker speaker.


### 5.2. Ringer volume

1 Press $-\underset{\square}{-1}$ or $-\underset{\square}{\square}$ to select HIGH, MIDDLE, LOW, or OFF. (Make sure ${ }^{\text {SPEAKER }}$ has not been pressed, the handset is not lifted, and a document is not loaded in the feeder.)

- The ringer will ring once at the selected level.
2 If you selected "OFF OK ?" to turn



## [2] Diagnostics and service soft switch

## 1. Entering the diagnostic mode



## 2. Diagnostic items description

### 2.1. SOFT SWITCH MODE

In this mode, the soft switches are set and the soft switch list is printed.

### 2.1.1 Operation

Soft switch mode screen


1234567 8: Data No.

### 2.1.2 Switch number selection and data setting

1. Enter three digits of a soft switch number to set the switch number. Of a switch number of unexacting soft switch is entered, key error buzzer sounds to reject the input.

2. Press [START/MEMORY] key moves the cursor to the data number 1 of the former soft switch. If the switch number is the final, pressing [START/MEMORY] key will exit the soft switch mode.

3. Data number selection
a) Pressing [ $>$ ] or [\#] key moves the cursor to the right. If the cursor is on data number 8 , pressing [ $>$ ] or [\#] key shifts the cursor to data number 1 of the next switch number. If the switch number is the final, pressing [>] or [\#] key will exit the soft switch mode.

b) Pressing $[<]$ or $[\star]$ key moves the cursor to the left. If the cursor is on data number 1, pressing [ $<$ ] or $[*$ ] key shifts the cursor to data number 8 of the former switch number. If the switch number is 001 , pressing $[<]$ or $[\star]$ key do not move the cursor.

c) Pressing [UP] or [4] key moves the cursor to the data number 1 of the next soft switch. If the switch number is 001, pressing [UP] or [4] key shifts the cursor to data number 1.

d) Pressing [DOWN] or [6] key moves the cursor to the data number 1 of the former soft switch. If the switch number is the final, pressing [down] or [6] key will exit the soft switch mode.

e) Pressing [8] key moves the cursor to the data number 1 of the 10th former soft switch.

f) Pressing [2] key moves the cursor to the data number 1 of the 10th next soft switch.


### 2.1.3 Data setting

Press the [1] key, and the data to the position of the cursor will be changed to 1.

Press the [0] key, and the data to the position of the cursor will be changed to 0 .

### 2.1.4 Outputting method of soft switch list

In the soft switch mode, press [COPY/HELP] key, and the soft switch list will be printed.

### 2.1.5 Storage of data

In the following cases, the data if the soft switches set will be stored.

- It is shifted to set the next soft switch by pressing [START/MEMORY] key.
- If is shifted to set the former soft switch with the [<] or [UP] or [4] key.
- It is shifted to set the next soft switch with the [>] or [DOWN] or [6] key.
- If is shifted to set the 10 th former soft switch with the [8] key.
- It is shifted to set the 10th next soft switch with the [2] key.


### 2.2. ROM \& RAM check

To check the sum value of Firmware or RAM.

| No. | Device | Alarm Buzzer | Remarks |
| :---: | :---: | :--- | :--- | :---: |
| Main | ROM <br> (PROGRAM FLASH) | 1 time <Short <br> sound> |  |
|  | SDRAM | 3 times <Short <br> sounds> |  |

### 2.2.1 Display



### 2.2.2 Result printing

After checking, the results print starts.

### 2.3. AGING MODE

A total of 10 sheets of check patterns are printed at 1-hour intervals.
This mode is exited when executed to the end (after 9 hours) or by pressing the [STOP] key.


### 2.4. PANEL CHECK MODE

This is used to check whether each key is normally operated or not. After the test, the test result will be printed.

### 2.4.1 Flow

Press any key except [STOP] key. At this time, the name of each key will be displayed every push of the key.

a) When all keys can be inputted, the following message will be displayed.

## ALL KEY OK!!

Then the screen will be all displayed in black (refer to (2)) and the test result will be printed.
b) If any key skipped, the following message will be displayed.

## KEY ERROR !!

A key name that is not pressed yet is displayed so that this test can be continued.

At that time, pressing the [STOP] key will exit this mode. And the result will be printed.

### 2.4.2 Black screen



### 2.5. CHECK PATTERN MODE

Printing the pattern.

| DIAG MODE |
| :--- | :--- |
| 05:CHECK PATTERN |$\square$| Start by pressing the |
| :--- |
| [START/MEMORY] key |$\quad \square$ PRINTING

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### 2.6. SIGNAL SEND MODE

The specified signals are transmitted in the following sequence to check the modem.

1. Press the [START/MEMORY] key, and no signals with the loop state starts.
[1] No signals (making the loop)
[2] 14400BPS (V.17)
[3] 12000BPS (V.17)
[4] 9600BPS (V.17)
[5] 7200BPS (V.17)
[6] 9600BPS (V.29)
[7] 7200BPS (V.29)
[8] 4800BPS (V.27ter)
[9] 2400BPS (V.27ter)
[10] 300BPS (FLAG)
[11] 2100 Hz (CED)
[12] 1100 Hz (CNG)

2. Pressing the [START/MEMORY] key during transmitting CNG signal, or pressing the [STOP] key will stop the output of signal and exit the mode.

### 2.7. MEMORY CLEAR

Clear the back-up memory including the soft switches, registration data. After executing this mode, the memory clear report is printed.
Note: The following data is not cleared. Values for Printer life including the adjusted value of printing void.


### 2.8. SHADING MODE

Shading waveforms are stored.
Set a shading sheet and press the [START/MEMORY] key to start shading.

| DIAG MODE 08:SHADING MODE | $\square$ | Start by pressing the [START/MEMORY] key | $\square$ TOTAL SHADING |
| :---: | :---: | :---: | :---: |

### 2.9. AUTO FEEDER MODE

Inserting and discharging the document can check the auto feeder.

1. The information of document sensor (A4 sensor) and ORG sensor is displayed when the documents are inserted to the Auto Feeder. Press the [START/MEMORY] key, and feeding documents is started.

2. Press [STOP] key to exit the mode.

### 2.10. FLASH MEMORY CHECK

Read/write of the flash memory is checked.
The result is printed after completion of check.
Be sure to clear the memory after execution.


### 2.11. MEMORY SET MODE

It is possible to read or write the memory contents.
When 8 digits of physical address are entered, the data of that address is displayed.
When 2 digits of data are entered after entering 8 digits of address and a key is pressed, the contents can be rewritten.
Rewriting is enabled only in the range of addresses 00000000 to 007FFFFF.

If the memory is rewritten, the machine may malfunction.


### 2.12. CIS ADJUSTING MODE

In this mode, the position of Scanner motor is moved from the home position up to the specified value. And document reading lamp is turned on.


Press the [STOP] key to exit the mode.

### 2.13. SHARP CHART AGING

SHARP CHART is printed continuously.


Press the [STOP] key for interruption.

### 2.14. ASF AGING

The recording paper is fed continuously.


Press the [STOP] key for interruption.

### 2.15. IJP LIST MODE

Maintenance data of the printer is output.

| MACHINE DOT COUNT | Machine total dot count |
| :--- | :--- |
| MACHINE PAGE COUNT | Machine total page count |
| CARTRIDGE DOT COUNT | Cartridge-by-cartridge dot count |
| IDLE MAINTENANCE COUNT | Maintenance information |
| INSTALL MAINTENANCE <br> COUNT | Maintenance information |
| BEGIN OF JOB MAINTENANCE <br> COUNT | Maintenance information |
| THRESHOLD MAINTENANCE <br> COUNT | Maintenance information |
| POJ MAINTENANCE COUNT | Maintenance information |
| SWATH START TEMPERA- <br> TURE | Temperature control information |
| FUSE MEMORY VALUE | Cartridge information |
| VPH ON TIME | Cartridge information |
| CARTRIDGE CHANGE COUNT | Cartridge replacement count |
| JAM COUNT | Jam count |
| NOZZLE CLEAN COUNT | Nozzle cleaning count |
| CALIBRATION ENVIRONMENT <br> TEMPERATURE | Environmental temperature infor- <br> mation |
| CALIBRATION PRINT HEAD <br> TEMPERATURE | Cartridge temperature informa- <br> tion |
| TEMPERATURE MODE | Temperature control information |



### 2.16. LIFE MODE

MACHINE PAGE COUNT is displayed.


Press the [STOP] key to exit the mode.

- LIFE MODE can also be checked by the following operation.

$$
\text { STAND-BY state } \quad \text { Press } \square^{\text {FUNктion }} \square \mathrm{L}^{+}
$$

### 2.17. LIFE CLEAR MODE

MACHINE DOT COUNT and MACHINE PAGE COUNT are cleared.


### 2.18. SECURE PASS LIST

Print the SECURE PASS LIST.

| DIAG MODE <br> 18:SECURE PASS LIST | Start by pressing the <br> [START/MEMORY] key |  |  |  |
| :--- | :--- | :---: | :---: | :---: |

## 3. How to make soft switch setting

To enter the soft switch mode, press the following key entries in sequence.

$$
\text { Press FUNCTION } 9 * 8 \# \# \pi \text { START/MEMORY START/MEMORY } 0,010
$$

$\Omega$
DATA No. $1 \begin{array}{llllllll}2 & 3 & 4 & 5 & 6 & 7 & 8\end{array}$

SW001 $=0$| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

SW001 $=1$|  | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |


$\square$ Press \# or < key.
$\square$ Press *or $\triangle$ key.


- Bit1-8 are set.

Press START/MEMORY key during setting.
Press 1 or 0 key.

Soft SW002 - SW300 are set.

- To finish the settings halfway between SW001 and SW300, press the STOP key. In this case, the setting being done to the SW No. on display will be nullified while settings done to the preceding SW No. remain in effect.
- When the COPY/HELP key is pressed, the contents of soft switches are printed.

The soft switch mode is terminated.

## 4. Soft switch description

### 4.1. Soft switch






| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{gathered} \text { SW } \\ 24 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 25 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{gathered} \text { SW } \\ 26 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 27 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 28 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{gathered} \text { SW } \\ 29 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 30 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |

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| $\begin{aligned} & \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  |  |  |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | 1 |  |  | 0 |  |  |
| $\begin{gathered} \text { SW } \\ 31 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 32 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Error criterion | 10-20\% |  |  | -10\% |  | 0 |  |
|  |  | EOL (End of Line) detection timer |  | 13sec | 25 sec | 5 sec |  |  |  |
|  | 5 |  | No. 5 | 0 | 0 | 1 |  | 0 |  |
|  | 6 |  | No. 6 | 0 | 1 | 0 |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 33 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 34 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 35 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 36 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 37 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |



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| $\begin{aligned} & \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{gathered} \text { SW } \\ 44 \end{gathered}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 45 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 46 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 47 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 48 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 49 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 50 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |


| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | DATANo. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{gathered} \text { SW } \\ 51 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Busy tone detect when TEL/FAX mode | No | Yes | 0 |  |
|  | 7 | Dial tone detect when TEL/FAX mode | No | Yes | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 52 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 53 \end{gathered}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{gathered} \text { SW } \\ 54 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 55 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{gathered} \text { SW } \\ 56 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 57 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |

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| $\begin{aligned} & \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  |  |  |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | 1 |  |  | 0 |  |  |
| $\begin{gathered} S W \\ 58 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 59 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 60 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 1 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 1 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 61 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 1 |  |
|  | 2 | Reserved |  |  |  |  |  | 1 |  |
|  | 34 | Ringer Volume |  | OFF | LOW | MIDDLE | HIGH | $\begin{aligned} & 0 \\ & 1 \end{aligned}$ |  |
|  |  |  | No. 3 | 0 | 0 | 1 | 1 |  |  |
|  |  |  | No. 4 | 0 | 1 | 0 | 1 |  |  |
|  |  | Speaker Volume |  | LOW | LOW | MIDDLE | HIGH | 01 |  |
|  | 5 |  | No. 5 | 0 | 0 | 1 | 1 |  |  |
|  | 6 |  | No. 6 | 0 | 1 | 0 | 1 |  |  |
|  | 7 | Reserved |  |  |  |  |  | 1 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 62 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 1 |  |
|  | 3 | Reserved |  |  |  |  |  | 1 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 1 |  |
|  | 6 | Reserved |  |  |  |  |  | 1 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 63 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 1 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 1 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 64 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 1 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 1 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 1 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |


| $\begin{aligned} & \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | NO. |  | 1 | 0 |  |  |
| $\begin{gathered} \text { SW } \\ 65 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 66 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 67 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 68 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 69 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 70 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 71 \end{gathered}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |

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| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{gathered} \text { SW } \\ 72 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 73 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 74 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 75 \end{gathered}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 76 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 77 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 78 \end{gathered}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |


| $\begin{aligned} & \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{gathered} \text { SW } \\ 79 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 80 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 81 \end{gathered}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 82 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{gathered} \text { SW } \\ 83 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 84 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} S W \\ 85 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |

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| $\begin{aligned} & \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{gathered} \text { SW } \\ 86 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{gathered} \text { SW } \\ 87 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 88 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 89 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 90 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 91 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 92 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |


| $\begin{aligned} & \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{gathered} \text { SW } \\ 93 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 94 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 95 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 96 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 97 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 98 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{gathered} \text { SW } \\ 99 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |

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| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{aligned} & \text { SW } \\ & 100 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 101 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 102 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & S W \\ & 103 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 104 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 105 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 106 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |





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| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{aligned} & \text { SW } \\ & 122 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 123 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 124 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 125 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 126 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 127 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & S W \\ & 128 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |


| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{aligned} & \text { SW } \\ & 129 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 130 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & S W \\ & 131 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 132 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 133 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 134 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 135 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |


| SW | DATA NO. | ITEM | Switch setting and function |  |  |  |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| NO. |  |  |  | 1 |  |  | 0 |  |  |
| $\begin{aligned} & \text { SW } \\ & 136 \end{aligned}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 137 \end{aligned}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 138 \end{aligned}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 139 \end{aligned}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 140 \end{aligned}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 141 \end{aligned}$ |  | Delay timer before line connect in auto dial |  | Osec | 1.5 sec | 3.0sec |  | 1 |  |
|  | 1 |  | No. 1 | 0 | 0 | 1 |  |  |  |
|  | 2 |  | No. 2 | 0 | 1 | 0 |  |  |  |
|  | $\begin{aligned} & 3 \\ & 4 \\ & \hline \end{aligned}$ | Delay timer after line connect in auto dial |  | 1.7 sec | 3.0sec | 3.6 sec | 4.0sec | 01 |  |
|  |  |  | No. 3 | 0 | 0 | 1 | 1 |  |  |
|  |  |  | No. 4 | 0 | 1 | 0 | 1 |  |  |
|  | 56 | Calling time |  | 45sec | 90sec | 55 sec |  | 0 |  |
|  |  |  | No. 5 | 0 | 0 | 1 |  |  |  |
|  |  |  | No. 6 | 0 | 1 | 0 |  |  |  |
|  | 7 | CNG timing |  | 3.5 sec | 1.5 sec | 3.0 sec |  |  |  |
|  |  |  | No. 7 | 0 | 0 | 1 |  | 0 |  |
|  |  |  | No. 8 | 0 | 1 | 0 |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 142 \end{aligned}$ | 1 | Dial tone detection (before auto dial) | Yes |  |  | No |  | 0 | OPTION |
|  | 2 | Busy tone detection (after auto dial) | Yes |  |  | No |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 1 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |


| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{aligned} & \text { SW } \\ & 143 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 144 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 145 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 146 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 147 \\ \text { I } \\ \text { SW } \\ 157 \end{gathered}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 158 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \mathrm{SU} \\ & 15 \mathrm{~S} \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |

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| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM |  |  | Initial |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 | setting | Remarks |
| $\begin{aligned} & \text { SW } \\ & 160 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 161 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 162 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 163 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & \text { SW } \\ & 164 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & \text { SW } \\ & 165 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & \text { SW } \\ & 166 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |


| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | $\begin{aligned} & \hline \text { DATA } \\ & \text { NO. } \end{aligned}$ | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{aligned} & \text { SW } \\ & 167 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & S W \\ & 168 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 169 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 170 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 171 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & S W \\ & 172 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 173 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |

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| $\begin{aligned} & \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{aligned} & \text { SW } \\ & 174 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & \text { SW } \\ & 175 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & \text { SW } \\ & 176 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 177 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & \text { SW } \\ & 178 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 179 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & \text { SW } \\ & 180 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |


| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | $\begin{aligned} & \hline \text { DATA } \\ & \text { NO. } \end{aligned}$ | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{aligned} & \text { SW } \\ & 181 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & \text { SW } \\ & 182 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 1 |  |
| $\begin{aligned} & \text { SW } \\ & 183 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 184 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 1 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 185 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 1 |  |
|  | 3 | Reserved |  |  | 1 |  |
|  | 4 | Reserved |  |  | 1 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 1 |  |
|  | 7 | Reserved |  |  | 1 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 186 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \mathrm{SU} \\ & 187 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |

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| $\begin{aligned} & \text { SW } \\ & \text { NO. } \end{aligned}$ | DATA NO. | ITEM | Switch setting and function |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 | 0 |  |  |
| $\begin{aligned} & \text { SW } \\ & 188 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 189 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 190 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 191 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 192 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 193 \end{aligned}$ | 1 | Reserved |  |  | 1 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 194 \end{aligned}$ | 1 | Reserved |  |  | 0 |  |
|  | 2 | Reserved |  |  | 0 |  |
|  | 3 | Reserved |  |  | 0 |  |
|  | 4 | Reserved |  |  | 0 |  |
|  | 5 | Reserved |  |  | 0 |  |
|  | 6 | Reserved |  |  | 0 |  |
|  | 7 | Reserved |  |  | 0 |  |
|  | 8 | Reserved |  |  | 0 |  |


| $\begin{aligned} & \hline \text { SW } \\ & \text { NO. } \end{aligned}$ | $\begin{aligned} & \hline \text { DATA } \\ & \text { NO. } \end{aligned}$ | ITEM | Switch setting and function |  |  |  |  | Initial setting | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 1 |  |  | 0 |  |  |  |
| $\begin{aligned} & \text { SW } \\ & 195 \end{aligned}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{aligned} & \text { SW } \\ & 196 \end{aligned}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 1 |  |
|  |  | Busy tone detect pulse in TEL/ |  | 2pulse | 4pulse | 6 pulse | 10pulse | 0 |  |
|  | 4 | FAX mode | No. 4 | 0 | 0 | 1 | 1 |  |  |
|  | 5 |  | No. 5 | 0 | 1 | 0 | 1 |  |  |
|  | 6 | Dial tone detect time in TEL/FAX mode | 10sec |  |  | 5 sec |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |
| $\begin{gathered} \text { SW } \\ 197 \\ \text { I } \\ \text { SW } \\ 300 \end{gathered}$ | 1 | Reserved |  |  |  |  |  | 0 |  |
|  | 2 | Reserved |  |  |  |  |  | 0 |  |
|  | 3 | Reserved |  |  |  |  |  | 0 |  |
|  | 4 | Reserved |  |  |  |  |  | 0 |  |
|  | 5 | Reserved |  |  |  |  |  | 0 |  |
|  | 6 | Reserved |  |  |  |  |  | 0 |  |
|  | 7 | Reserved |  |  |  |  |  | 0 |  |
|  | 8 | Reserved |  |  |  |  |  | 0 |  |

### 4.2. Soft switch function description

SW1 No. 1, No. 2 Reserved

## SW1 No. 3, No. 4 Receive mode

TEL, FAX or TEL/FAX mode is set.
SW1 No. $5 \sim$ No. 8 Reserved

## SW2 No. 1 Reserved

## SW2 No. 2 Forced 4800bps reception

When line conditions warrant that receptions take place at 4800 BPS repeatedly. It may improve the success of receptions by setting at 4800BPS. This improves the receiving document quality and reduces handshake time due to fallback during training.

SW2 No. 3, No. 4 CED tone signal interval
For international communication, the 2100 Hz CED tone may act as an echo suppressor switch, causing a communication problem. Though this soft switch is normally set to "0", it should be set to "1" so as to change the time between CED tone and DIS signal from 75 ms to 500 ms to eliminate the communication problem caused by echo.
SW2 No. 5 ~ No. 8 Reserved

## SW3 No. 1 Length limitation of receive

Used to set the maximum page length. To avoid possible paper jam, the page length is normally limited to 1.5 meters for receive.

## SW3 No. 2 Footer print

When set to "1", the date of reception, the sender machine No, and the page No. are automatically recorded at the end of reception.

## SW3 No. 3 ~ No. 6 Reserved

## SW3 No. 7 CSI transmission

CSI signal contains the sender's phone number registered in the machine. If this switch is set to "1", no sender's name will be printed at the receiving side.

## SW3 No. 8 Action when RTN is received

The operation is set when the RTN signal is received in the G3 transmission mode.

SW4 No. 1 Reserved

## SW4 No. 2 Dial pausing (sec/pause)

Pauses can be inserted between telephone numbers of direct dial connection. Selection of 4 sec or 2 sec pause is available.

## SW4 No. 3 Pulse format of D.P.

Used to set pulse format of D.P. mode.
SW4 No. 4 DIS receive acknowledgement during G3 transmission
Used to make a choice of whether reception of NSF (DIS) is acknowledged after receiving two NSFs (DISs) or receiving one NSF (two DISs). It may be useful for overseas communication to avoid an echo suppression problem, if set to 1 .

## SW4 No. 5 Non-modulation carrier for V29 transmission mode

Though transmission of a non-modulated carrier is not required for transmission by the V29 modem according to the CCITT recommendation, it may be permitted to send a non-modulated carrier before the image signal to avoid an echo suppression problem. It may be useful for overseas communication to avoid an echo suppression problem, if set to 1.

## SW4 No. 6 Reserved

## SW4 No. 7 Protocol monitor

Normally set to " 0 ". If set to "1", communication can be checked, in case of troubles, without using a G3 tester or other tools. When communication FSK data transmission or reception is made, the data is taken into buffer. When communication is finished, the data analyzed and printed out. When data is received with the line monitor (SW-4 No. 8 ) set to " 1 " the reception level is also printed out.

## SW4 No. 8 Line monitor

Normally set to " 0 ". If set to " 1 ", the transmission speed and the reception level are displayed on the LCD. Used for line tests.

SW5 No. 1 ~ No. 8 Reserved

## SW6 No. 1, No. 2 Reserved

SW6 No. 3 Digital cable equalizer for reception
Digital cable equalizer for reception
0: OFF
1: ON

## SW6 No. 4 Sender's information transmit

When it is set at 0 , sender's name, sending page number and so on are automatically printed in the recording paper on the receiving side during transmission. Thus, the sender can be known on the receiving side.

## SW6 No. 5 ~ No. 8 Reserved

## SW7 No. 1 H2 mode

Used to determine reception of H 2 mode ( 15 sec transmission mode). When set to OFF, H2 mode reception is inhibited even though the transmitting machine has H 2 mode function.

## SW7 No. 2 Reserved

## SW7 No. 3 ECM mode

Used to determine ECM mode function.

## SW7 No. 4 ECM MMR mode

MMR (Modified MR) selects presence of the compression function.
SW7 No. 5 ~ No. 8 Signal transmission level
Used to control the signal transmission level in the range of -0 dB to 31dB.

SW8 No. 1 ~ No. 4 Recall interval (FAX)
Choice is made for a recall interval for speed and rapid dial numbers. Use a binary number to program this. If set to 0 accidentally, 1 will be assumed.

SW8 No. 5 ~ No. 8 Recall times (FAX)
Choice is made as to how many recall times should be made. Use a binary number to program this.

SW9 No. 4 Sort Copy function
Used for sort copy.
SW9 No. 5 ~ No. 7 Reserved

## SW9 No. 8 Dial mode

Switch the type according to the telephone circuit connected to the facsimile.

SW10 No. 1, No. 2 Reserved

## SW10 No. 3 CNG detection

When setting to "1", the CNG signal detection function.
SW10 No. 4 Number of CNG detection
Used for detection of CNG in 2 or 3 pulses.
SW10 No. 5 Signal detect time in TEL/FAX mode
This setting is used for the time of the pseudo ringer off interval in the TEL/FAX mode.

SW10 No. 6 Reserved

SW10 No. 7, No. 8 CNG detection time after ringing
CNG detection time after completion of ringing.
SW11 No. 1, No. 2 Reserved

## SW11 No. 3 Time display format

When this switch is set to " 0 ", time is displayed in 12 -hour system. When set to "1", 24-hour system.

## SW11 No. 4 Date display format

Used to select date display/print formats.
0: DAY-Month-Year
1: Month-Day-Year
SW11 No. 5, No. 6 Pseudo ringing time at TEL/FAX automatic switching mode

Choice is made as to how long to rumble the dummy ringer on TEL/ FAX automatic switching mode.

SW11 No. 7 Reserved

## SW11 No. 8 CED detection time

It is effective to change this if detection error frequently occurs by setting the CED automatic detection time.

## SW12 No. 1 MH fixed

1: Both sending and reception are forcibly fixed to MH .
0 : Adjusted to the performance of the other machine.
SW12 No. 2 ~ No. 5 Reserved

## SW12 No. 6 Key tone

Used to toggle the key sound between on and off.
SW12 No. 7, No. 8 Reserved

SW14 No. 1 ~ No. 4 Reserved

## SW14 No. 5 ~ No. 8 Modem Speed

Used to determine the initial modem speed. The default is 14400BPS(V.17). It may be necessary to program it to a slower speed when frequent line fallback is encountered, in order to save the time required for the fallback procedure.

SW15 No. 1 ~ No. 8 Reserved
Pauses can be inserted between telephone numbers of direct dial connection. Selection of 4 sec or 2 sec pause is available.

SW16 No. 1 Digital cable equalizer for transmission
Digital cable equalizer for transmission
0 : OFF
1: ON
SW16 No. 2, No. 3 Reserved

## SW16 No. 4 Flash send time

Used to select length of flash.
SW16 No. 5, No. 6 Flash short time
Selects the flash send time when the flash send time of SW16 No. 4 is set to short time.

SW16 No. 7, No. 8 Flash long time
Selects the flash send time when the flash send time of SW16 No. 4 is set to long time.

## SW17 ~ SW20 No. 1 ~ No. 8 Reserved

## SW21 No. 1, No. 2 Line density selection

Used to set the transmission mode which is automatically selected when the Resolution key is not pressed. In the copy mode, however, the fine mode is automatically selected unless the Resolution key is manually set to another mode.

SW21 No. 3 ~ No. 5 Communication results printout
It is possible to obtain communication results after each transaction. Normally, the switch is set (No. 3: 0, No. 4: 0, No. 5: 1) so that the communication result is produced only a communication error is encountered. If No. 3 was set to 1 , No. 4 was set to 1 and No. 5 was set to 0 , the communication result will be produced every time a communication is done, even if the communication was successful. If No. 3 was set to 0 , No. 4 to 1 and No. 5 to 0 , the communication result will be produced every transmission. Setting No. 3 to 1 No. 4 to 0 and No. 5 to 0 will disable this function. No transaction report will be printed. If No. 3 was set to 0 , No. 4 to 0 and No. 5 to 0 , the communication result is produced only after a memory transmission or when a communication error is encountered.

## SW21 No. 6 Caller ID function

Used for Caller ID function.

## SW21 No. 7 Activity report print

This soft switch is used to select: whether or not to print out the activity report when the memory is full. An activity report can be printed when the following key entry command is mode.
"FUNCTION ", " $\downarrow$ ", " $\downarrow$ ", " $\rightarrow$ "
After producing the activity report, all the data in the memory will be cleared. When the switch function is set to "0" (no), the data in the memory will be deleted from the oldest as it reaches the maximum memory capacity.

SW21 No. 8 Daylight saving (Summer time)
Used to set YES/NO of holding function by the Hold key.

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SW22 No. 1 Activate Receive Fax to E-mail
The Receive Fax to E-mail function ON/OFF is set.
SW22 No. 2 Print setting of received fax for Receive Fax to E-mail This soft switch is used to select before.
0 : Received FAX is always printed when Receive Fax to E-mail function worked.
1: Received FAX is printed only transfer error when Receive Fax to Email function worked.

## SW22 No. 3 Report setting for Receive Fax to E-mail

This soft switch is used to select before.
0: Transfer result is always printed when Receive Fax to E-mail function worked.

1: Transfer result is printed only transfer error when Receive Fax to Email function worked.

## SW22 No. 4 Activate Secure Receive

The Secure receive function ON/OFF is set.
SW22 No. 5 ~ No. 8 Reserved

SW23 No. 1 Ink save mode
If set to " 1 ", printing is done with half of ink.
SW23 No. 2 Receive reduce setting
If set to 1 , it is reduced automatically when receiving.
SW23 No. 3, No. 4 Paper size
The paper size is set.
SW23 No. 5 Half tone copy print swath direction
The direction of the half tone copy printing is set.
SW23 No. 6 ~ No. 8 Reserved

SW24~SW31 No. 1 ~ No. 8 Reserved

SW32 No. 1 ~ No. 3 Reserved

## SW32 No. 4 Error criterion

Used to select error criterion for sending back RTN when receiving image data.

SW32 No. 5, No. 6 EOL (End of Line) detection timer
Used to make a choice of whether to use the 25 -second or 13 -second timer for detection of End of line This is effective to override communication failures with some facsimile models that have longer End of line detection.

SW32 No. 7, No. 8 Reserved

SW33 ~ SW38 No. 1 ~ No. 8 Reserved

SW39 No. 1 ~ No. 6 Reserved

## SW39 No. 7 Make/Break ratio in D.P.

Used to set pulse ratio of D.P. mode.

## SW39 No. 8 Pre pause time in D.P.

This setting is used for the pause time before dial pulse start.

SW40 No. 1 ~ No. 3 Inter digit pause time
Used to set the pause time between pulse.
SW40 No. 4 Change to PB from PB by " $\times$ " key.
When setting to 1 , the mode is changed by pressing the key from the pulse dial mode to the tone dial mode.

SW40 No. 5 ~ No. 8 Reserved

## SW41 No. 1, No. 2 DTMF detect time

Used to set detect time of DTMF (Dual Tone Multi Frequency) used in remote reception $(5 * *)$. The longer the detection time is, the error detection is caused by noises.

SW41 No. 3 Protection of remote reception ( $5 \times \times$ ) detect
Used to set the function of remote reception ( $5 * *$ ). When set to " 1 ", the remote reception function is disabled.

SW41 No. 4 ~ No. 7 Remote operation code figure by external TEL(0-9)
Remote operation codes can be changed from 0 through 9 . If set to greater than 9 , it defaults to 9 . The " 5 " is not changed.
SW41 No. 8 Reserved

SW42 ~ SW50 No. 1 ~ No. 8 Reserved

SW51 No. 1 ~ No. 5 Reserved

SW51 No. 6 Busy tone detect when TEL/FAX mode
This setting is used to enable or disable busy tone detection in the TEL/FAX mode.

## SW51 No. 7 Dial tone detect when TEL/FAX mode

This setting is used to enable or disable dial tone detection in the TEL/ FAX mode.

SW51 No. 8 Reserved

## SW52 ~ SW60 No. 1 ~ No. 8 Reserved

SW61 No. 1, No. 2 Reserved
Ringer volume:
The calling sound volume of Cl signal receiving is set.
SW61 No. 3, No. 4 Ringer volume
This allows selection of MTF correction (dimness correction) in the half tone mode.

When "NO" (=1) is selected, the whole image becomes soft and mild.
Clearness of characters will be reduced. Normally set to "YES" (=0).
SW61 No. 5, No. 6 Speaker volume
The line monitor volume is set.
SW61 No. 7, No. 8 Reserved

SW62 ~ SW109 No. 1 ~ No. 8 Reserved

SW110 No. 1 ~ No. 3 Connect configuration for LAN controller Used to the communication speed when connecting to LAN.

SW110 No. 4 ~ No. 8 Reserved

SW111 No. 1 ~ No. 4 Detect time of LAN link ON
Used to change the setting for the link ON detection time when connecting a LAN cable.

## SW111 No. 5 ~ No. 8 Detect time of LAN link OFF

Used to change the setting for the link OFF detection time when connecting a LAN cable.

## SW112 No. 1 Default file format for network scan

SW setting the default of the compression format and the file type required for generating the image data from network scanned data.

SW112 No. 2, No. 3 Default resolution for network scan
SW setting the default resolution on network scanning.
SW112 No. 4 ~ No. 7 Reserved

SW112 No. 8 SMTP authentication type
Used to switching the SMTP authentication type.
" 0 ": The authentication type which is not supported by the server is disabled.
"1": Authenticated from "LOGIN" regardless of the authentication type of the server.

SW113 No. 1 ~ No. 8 Reserved

SW114 No. 1 ~ No. 5 DTMF output level (High)
To set the level to output high group DTMF signals. -15 to 0 dBm (0.5dBm unit)

SW114 No. 6 ~ No. 8 Reserved

SW115 No. 1 ~ No. 5 DTMF output level (Low)
To set the level to output low group DTMF signals. -15 to 0 dBm ( 0.5 dBm unit)

SW115 No. 6 ~ No. 8 Reserved

SW116 No. 1 ~ No. 3 Dial tone detection frequency
To select frequency range of signals to be detected as Dial Tone.
SW116 No. 4, No. 5 Busy tone detection frequency
To select frequency range of signals to be detected as Busy Tone.
SW116 No. 6 ~ No. 8 Reserved

SW117 No. 1 ~ No. 4 Number of rings for auto-receive (0: No ring receive)

When the machine is set in the auto receive mode, the number of rings before answering can be selected. It may be set from zero to nine rings using a binary number. If the soft switch was set to 1 , a direct connection is made to the facsimile. If it was above 9 , receive rings are set to 9 .

## SW117 No. 5 ~ No. 8 Distinctive ringring

When set to "0001", machine recognize the Cl signal FAX ringing or TEL ringing automatically.

SW118 No. 1 ~ No. 8 Reserved

SW119 No. 1 ~ No. 3 Reserved

SW119 No. 4 ~ No. 6 CI signal min. off time
Used to set the maximum length of Cl signal off time, which is used to determine a sequence of Cl signals.

SW119 No. 7, No. 8 Reserved

SW120 No. 1 ~ No. 4 Reserved

SW120 No. 5 ~ No. 8 CI Signal Frequency
Used to set a frequency that is considered as the Cl signal.
SW121~SW140 No. 1 ~ No. 8 Reserved

SW141 No. 1, No. 2 Delay timer before line connect in auto dial
Set a time period between start of dial operation and line connection in the auto dial mode.

SW141 No. 3, No. 4 Delay timer after line connect in auto dial
Set a time period between dial-up line connection and dial data transmission in the auto dial mode.

SW141 No. 5, No. 6 Calling time
Set the call time for dialing in the auto dial mode.
SW141 No. 7, No. 8 CNG timing
Set a time period between dialing in the auto dial mode and CNG signal transmission.

SW142 No. 1 Dial tone detection (before auto dial)
When set to " 1 ", a number is dialed after detecting the dial tone.
SW142 No. 2 Busy tone detection (after auto dial)
When set to "1", the busy tone is detected after dialing a number.
SW142 No. 3 Recall Control (for FCC part68)
Recalling fixed only one time when dialing was unsuccessful without detecting busy tone signal.

SW142 No. 4 ~ No. 8 Reserved

SW143 ~ SW195 No. 1 ~ No. 8 Reserved

SW196 No. 1 ~ No. 3 Reserved

SW196 No. 4, No. 5 Busy tone detect pulse in TEL/FAX mode
This setting is used for the detection pulse number until busy tone is detected in the TEL/FAX mode.

SW196 No. 6 Dial tone detect time in TEL/FAX mode
This setting is used for the continuous detection time until dial tone is detected in the TEL/FAX mode.

SW196 No. 7, No. 8 Reserved

SW197 ~ SW300 No. 1 ~ No. 8 Reserved

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## [3] Troubleshooting

Refer to the following actions to troubleshoot any of the problems mentioned in 1-4.
[1] A communication error occurs.
[2] Image distortion produced.
[3] Unable to do overseas communication.
[4] Communication speed slow due to FALLBACK.

- Slow down the transmission speed SOFT SWITCH A2-1, 2, 3, 4. May be used in case [2] [3].
- Replace the LIU PWB. May be used in all cases.
- Replace the control PWB. May be used in all cases.
- If transmission problems still exist on the machine, use the following format and check the related matters.
- In crease the transmission level SOFT SWITCH A4-1, 2, 3, 4, 5. May be used in case [1] [2] [3].
- Decrease the transmission level SOFT SWITCH A4-1, 2, 3, 4, 5. May be used in case [3].
- Apply line equalization SOFT SWITCH A2-1, 2, 3, 4. May be used in case [1] [2] [3] [4].


* Please complete this report before calling the "TAC" hotline if problem still occurs.


## [4] Error code table

## 1. Communication error code table

### 1.1. G3 Transmission

| Code | Final received signal | Error Condition (Receiver side) |
| :---: | :--- | :--- |
| 0 | Incomplete signal frame | Cannot recognize bit stream after flag |
| 1 | NSF, DIS | Cannot recognize DCS signal by echo etc. <br> Cannot recognize NSS signal (FIF code etc.) |
| 2 | CFR | Disconnects line during reception (carrier missing etc.) |
| 3 | FTT | Disconnects line by fall back |
| 4 | MCF | Disconnects line during reception of multi page <br> Cannot recognize NSS, DCS signal in the case of mode change |
| 5 | PIP or PIN | The line is hung up without replying to telephone request from the receiving party. |
| 6 | RTN or RTP | Cannot recognize NSS, DCS signal after transmit RTN or RTP signal. |
| 7 | No signal or DCN | No response in receiver side or DCN signal received* (transmitter side) |
| 8 | - | Owing to error in some page the error could not be corrected although the specified number of <br> error retransmissions were attempted. |
| 11 | - | Error occurred after or while reception by the remote (receiving) machine was revealed to be <br> impossible. |
| 12 | - | Error occurred just after fallback. |
| 13 | - | Error occurred after a response to retransmission end command was received. |

### 1.2. G3 Reception

| Code | Final received signal |  |
| :---: | :--- | :--- |
| 0 | Incomplete signal frame | Cannot recognize bit stream after flag |
| 1 | NSS, DCS | Cannot recognize CFR or FTT signal <br> Disconnects line during transmission (line error) |
| 2 | NSC, DTC | Cannot recognize NSS signal (FIF code etc.) |
| 3 | EOP | Cannot recognize MCF, PIP, PIN, RTN, RTP signal |
| 4 | EOM | Cannot recognize MCF, PIP, PIN, RTN, RTP signal in the case of mode change |
| 5 | MPS | The line is hung up without replying to communication request. |
| 6 | PR1-Q | Cannot recognize PIP, PIN signal in the case of TALK request |
| 7 | No signal or DCN | No response in transmitter (cannot recognize DIS signal) or DCN signal received* (receiver side) |
| 8 | - | Error occurred upon completion of reception of all pages. |
| 9 | - | Error occurred when mode was changed or Transmission/Reception switching was performed. |
| 10 | - | Error occurred during partial page or physical page reception. |
| 11 | - | Error occurred after or during inquiry from the remote (transmitting) machine as to whether recep- <br> tion is possible or not. |
| 12 | - | Error occurred during or just after fallback. |
| 13 | - | Error occurred after the retransmission end command was received. |

## CHAPTER 3. MECHANISM BLOCKS

## [1] General description

## 1. Document feed block and diagram



Fig. 1

## 2. Document feed operation

1) The original, which is set in the document hopper, feeds automatically when the Front sensor is activated. This in turn activates the drive motor which drives the paper feed roller. The document stops when the lead edge is detected by the Original sensor.
2) The lead edge of the original is fed a specified number of pulses after the lead edge of the document is detected for the reading process to begin.
3) The trailing edge of the original is fed a specific number of pulses after the trailing edge of the document deactivates the Original sensor. The read process then stops and the original is discharged.
4) When the Front sensor is in the OFF state (any document is not set up in the hopper guide), the drive will be stopped when the document is discharged.

## 3. Hopper mechanism

### 3.1. General view



Fig. 2
The hopper section contains document guides that are used to adjust the hopper to the width of the original document. This ensures that the original feeds straight into the fax machine for scanning.
Document width: 70 mm to 216 mm ( 70 mm size to Letter longitudinal size)
NOTE: Adjust the document guide after setting up the document.

### 3.2. Automatic document feed

1) Use of the paper feed roller and separate plate ensures error-free transport and separation of documents. The paper feed spring presses the document to the paper feed roller to assure smooth feeding of the document.
2) Document separation method: Separate plate


Fig. 3

### 3.3. Documents applicable for automatic feed

|  | 10 sheets | 1 sheet (Manual) |
| :--- | :--- | :--- |
| Paper weight | 20 lbs. | $14 \mathrm{lbs} \sim 42 \mathrm{lbs}$. |
|  | $\left(74.3 \mathrm{~g} / \mathrm{m}^{2}\right)$ | $\left(52 \mathrm{~g} / \mathrm{m}^{2} \sim 157 \mathrm{~g} / \mathrm{m}^{2}\right)$ |
|  | at the environment |  |
|  | $23 \pm 5^{\circ} \mathrm{C}$ |  |
|  | $55 \pm 10 \%$ | $0.06 \mathrm{~mm} \sim 0.18 \mathrm{~mm}$ |
| Paper thickness (ref.) | 0.09 mm |  |
| Paper size | A4 $(210 \mathrm{~mm} \times 297 \mathrm{~mm})$ |  |
| Feeder capacity | A4: 10 sheets max. |  |

NOTE: Double-side coated documents and documents on facsimile recording paper should be inserted manually. The document feed quantity may be changed according to the document thickness.

Documents corresponding to a paper weight heavier than $70 \mathrm{~kg}(80 \mathrm{~g} /$ $\mathrm{m}^{2}$ ) and lighter than $135 \mathrm{~kg}\left(157 \mathrm{~g} / \mathrm{m}^{2}\right)$ are acceptable for manual feed. Documents heavier than $135 \mathrm{~kg}\left(157 \mathrm{~g} / \mathrm{m}^{2}\right)$ in terms of the paper weight must be duplicated on a copier to make it operative in the facsimile.

### 3.4. Loading the documents

1. Make sure that the documents are of suitable size and thickness, and free from creases, folds, curls, wet glue, wet ink, clips, staples and pins.
2. Place documents face down in the hopper.
i) Adjust the document guides to the document size.
ii) Align the top edge of documents and gently place them into the hopper. The first page under the stack will be taken up by the feed roller to get ready for transmission.
NOTE: 1) Curled edge of documents, if any, must be straightened out.
2) Do not load the documents of different sizes and/or thicknesses together.


Fig. 4

### 3.5. Documents requiring use of document carrier

1) Documents smaller than $148 \mathrm{~mm}(\mathrm{~W}) \times 140 \mathrm{~mm}(\mathrm{~L})$.
2) Documents thinner than the thickness of 0.06 mm .
3) Documents containing creases, folds, or curls, especially those whose surface is curled (maximum allowable curl is 5 mm ).
4) Documents containing tears.
5) Carbon-backed documents. (Insert a white sheet of paper between the carbon back and the document carrier to avoid transfer of carbon to the carrier.)
6) Documents containing an easily separable writing material (e.g., those written with a lead pencil).
7) Transparent documents.
8) Folded or glued documents.

Document in document carrier should be inserted manually into the feeder.

## 4. Document release

### 4.1. General description

To correct a jammed document or to clean the document running surface, pull the insertion side of document center of the operation panel. To open the upper document guide, the operation panel must be opened first.

## 5. Recording block

1) The PU ROLLER is rotated by the DC motor to feed the recording paper. The SEPARATE RUBBER feeds one by one from the top paper.
2) The recording paper contacts the P-IN SENSOR.
3) The PAPER FEED ROLLER rotates in the reverse direction of paper feed (refer to the figure of PU ROLLER). The tip of recording paper is set parallel to the PAPER FEED ROLLER when it reaches to the PAPER FEED ROLLER and the PRESS ROLLER after through the PU ROLLER.
4) Then the PAPER FEED ROLLER rotates in the paper feed direction to feed the recording paper downwards. (refer to the figure of PAPER FEED)
5) The CARTRIDGE prints the recording paper, which is then ejected by the EXIT ROLLER and STAR WHEEL.

### 5.1. General view



Fig. 5

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## [2] Ink jet printer

## 1. Overall troubleshooting of Printer

```
- BOTH CARRIER MOTOR AND FEED MOTOR
    ARE NG.
```



```
- FEED MOTOR IS NG (CARRIER MOTOR IS
    GOOD)
```



```
- CARRIER MOTOR IS NG (FEED MOTOR IS
    GOOD)
```



```
- MISSING NOZZLES (RESULT OF CHECK PATTERN
OR CLEAN NOZZLES)
```



## UX-B800A

## [3] Disassembly and assembly procedures

- This chapter mainly describes the disassembly procedures. For the assembly procedures, reverse the disassembly procedures.
- Easy and simple disassembly/assembly procedures of some parts and units are omitted. For disassembly and assembly of such parts and units, refer to the Parts List.
- The numbers in the illustration, the parts list and the flowchart in a same section are common to each other.
- To assure reliability of the product, the disassembly and the assembly procedures should be performed carefully and deliberately.


## 1. Operation panel unit



Fig. 1

## 2. Top cover/Back cover

## NOTE:

1. Remove the back cover and then the top cover.
2. Insert the hook switch lever into the top cover, then install the back cover.

Parts list (Fig. 2)

| No. | Part name |  |
| :---: | :--- | :---: |
| 1 | Mechanism unit | 1 |
| 2 | Platen roller | 1 |
| 3 | Screw $(3 \times 10)$ | 7 |
| 4 | Hook | 2 |
| 5 | Top cover | 1 |
| 6 | Screw $(3 \times 10)$ | 2 |
| 7 | Hook | 2 |
| 8 | Back cover | 1 |



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## 3. Paper hopper unit/PU guide upper



Fig. 3

NOTE:
For disassembly of the inside of the unit, refer to the exploded view in the parts guide.
Parts list (Fig. 4)

| No. | Part name | Qty |
| :---: | :--- | :---: |
| 1 | Mechanism unit | 1 |
| 2 | Screw $(3 \times 10)$ | 5 |
| 3 | PU guide lower unit | 1 |
| 4 | Screw $(3 \times 10)$ | 3 |
| 5 | Earth plate | 1 |
| 6 | Connector | 2 |
| 7 | Screw $(3 \times 10)$ | 2 |
| 8 | TX drive frame unit | 1 |
| 9 | RF roller shaft | 1 |



Fig. 4

## 5. Cable guide/CIS unit

## NOTE:

For disassembly of the inside of the unit, refer to the exploded view in the parts guide.
Parts list (Fig. 5)

| No. | Part name | Qty |
| :---: | :--- | :---: |
| 1 | Mechanism unit | 1 |
| 2 | Screw $(3 \times 10)$ | 1 |
| 3 | Cable guide | 1 |
| 4 | Hook | 2 |
| 5 | CIS unit | 1 |



## 6. Control PWB unit/Printer unit



Fig. 6

## 7. LIU PWB unit/Power supply PWB unit



Fig. 7

## 8. Wire treatment



Fig. 8

## [1] Block diagram



## [2] Wiring diagram




## CHAPTER 5. CIRCUIT DESCRIPTION

## [1] Circuit description

## 1. General description

The compact design of the control PWB is obtained by using CONEXANT fax engine in the main control section and high density printing of surface mounting parts. Each PWB is independent according to its function as shown in Fig. 1.

## 2. PWB configuration



Fig. 1

### 2.1. Control PWB

The Control PWB controls peripheral PWBs, mechanical parts, transmission, and performs overall control of the unit. This machine employs 1 chip fax engine (CX93310) which is installed on the Control PWB.

### 2.2. LIU PWB

This PWB controls connection of a telephone line to the unit.

### 2.3. Power supply PWB

This PWB provides voltages of +5 V and +30 V to the other PWBs.

### 2.4. Panel PWB

The panel PWB allows input of the operation keys.

### 2.5. LCD UNIT

This unit controls the LCD display.

## 3. Operational description

Operational descriptions are given below:

- Transmission operation

When a document is loaded in stand-by mode, the state of the document sensor is sensed via the 1 chip fax engine (CX93310). With depression of the START key in the off-hook state, transmission takes place. Then, the procedure is sent out from the modem (CX20452) and the motor is rotated to move the document down to the scan line. In the scan processor, the signal scanned by the CIS is sent to the CX20556 to convert the analog signal into binary data. This binary data is transferred from the scan processor to the image buffer within the RAM and encoded and stored in the transmit buffer of the RAM. The data is then converted from parallel to serial form by the modem (CX20452) where the serial data is modulated and sent onto the line.

- Receive operation

There are two ways of starting reception, manual and automatic. Depression of the START key in the off-hook mode in the case of manual receive mode, or CI signal detection by the LIU in the automatic receive mode. First, the CX93310 controls the procedure signals from the modem to be ready to receive data. When the program goes into phase $C$, the serial data from the modem is converted to parallel form in the modem interface of the 1 chip fax engine (CX93310) which is stored in the receive buffer of the RAM. The data in the receive buffer is decoded software-wise to reproduce it as binary image data in the image buffer. The data is sent to Printer unit by serial data. CX93310 controls printing system.

- Copy operation

To make a copy on this facsimile, the COPY key is pressed when the machine is in stand-by with a document on the document table and the telephone set is in the on-hook state. First, depression of the COPY key advances the document to the scan line. Similar to the transmitting operation, the image signal from the CIS is converted to a binary signal in the DMA mode via the CX20556 which is then sent to the image buffer of the RAM. Next, the data is transferred to CX93310 and modify the data to send to Printer unit CX93310 controls Feed Motor and Carrier Motor.

## UX-B800A

## [2] Circuit description of control PWB

## 1. General description

Fig. 2 shows the functional blocks of the control PWB, which is composed of 5 blocks.


Fig. 2 Control PWB functional block diagram

## 2. Description of each block

### 2.1. Main control block

The main control block is composed of CONEXANT 1 chip fax engine (CX93310), FLASH (16Mbit), SDRAM (64Mbit), LAN Controller (LAN91C113).

Devices are connected to the bus to control the whole unit
2.1.1 CX93310 (IC100): pin-216 LQFP (FAX CONTROLLER)

THis is a microcomputer with 32bit microprocessor core.

### 2.1.2 SST25VF016B (IC206): pin-8 SOIC (FLASH)

FLASH of 16Mbit equipped with software for the main CPU.

### 2.1.3 EDS6416AHTA-75-E (IC204): pin-54 TSOP (SDRAM)

On power on sequence, the firmware being compressed and stored in SDRAM memory (IC204) is decompressed to this device. After decompression, this device is used as a program execution memory. It is also used as various work memories and communication buffer etc.

### 2.1.4 LAN91C113 (IC702): pin-128 TQFP (LAN CONTROLLER)

The interface to LAN. It is connected with an external bus interface (8bit) of CX93310 (IC100).

### 2.1.5 SST25VF016B (IC201): pin-8 SOIC (FLASH)

For storage of data of various registration data and receiving image such as TEL No.

### 2.2. IC100 (CX93310) Hardware description

### 2.2.1 Features

- 32-bit CPU provides convenient functional customization
- Embedded Image Processing DSP
- Scan noise removal
- Dark level and shading correction
- Linearization
- Resolution conversion
- Sharpening
- Error diffusion
- Background removal
- JPEG and JBIG compression/decompression
- FlexIO ${ }^{\text {TM }}$ supports a variety of system configurations
- CIS and CCD control
- Scanner AFE control
- Direct thermal, thermal transfer, inkjet and laser print engine direct control
- Motor control
- ITU-T V. 17 facsimile modem function, with T.30, T.4, T.6, T. 42 support
- Sleep mode to reduce power consumption
- Real Time Clock with Battery Backup
- Single voltage (3.3V) power with internal voltage regulator
- 5 V -tolerant IO ports
- Compact lead-free (Pb-free) packages
- CX93310 Fax Engine Controller: 216-pin LQFP


### 2.2.2 System functions

1) Scan

Scanner control signals are provided by a $\mathrm{FlexIO}^{\text {TM }}$ port to support common CCD and CIS scanners. Digital scan data is also input over this interface from the external video data ADC device.

## 2) Print

Printer control signals are provided by a FlexIO port to support common direct thermal, thermal transfer, and inkjet print mechanisms.

## 3) Copy

Simultaneous scan and print operations provide a copy function, which includes support for image scaling (expansion to $200 \%$ and reduction to $33 \%$ ).
4) GPIOs

General purpose inputs/outputs (GPIOs) are provided that may be used to support common interfaces such as an operator panel.

## 5) Modem

The embedded modem function provides synchronous 14400 bps half-duplex modem with error detection and DTMF generation/reception. It provides data transmission/reception from regular PSTN lines, PBX, or private lines, when interfaced to DAA (Data Access Arrangement) circuitry via the CONEXANT CXxxxxx Integrated Analog device.
The modem can operate at any standard V. 17 data speed up to 14400 bps as well as in $V .21$ and V .23 modes.
The modem is designed for use in Group 3 facsimile machines. It satisfies the requirements specified in ITU-T recommendations V.17, V.29, V.27ter, V. 21 Channel 2, and T.4, and meets the signaling requirements of T.30. It also performs HDLC framing according to T. 30 at all speeds.

## 6) Concurrent operation

Several modes of concurrent operation are supported, such as scanning to memory while receiving/printing a facsimile reception, without operator intervention.

IC100: RH-IX2505XHPZ (CX93310) (1/4)

| $\begin{aligned} & \hline \text { Pin } \\ & \text { No. } \end{aligned}$ | Pin Name | I/O | $\begin{gathered} \hline \text { Def. } \\ \text { I/O } \end{gathered}$ | 5VT | Slew Rate | Drv. Stre. | Hys | $\begin{aligned} & \hline \text { PU/ } \\ & \text { PD } \end{aligned}$ | Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Reset, Main Clock, Voltage Regulator and Test Mode |  |  |  |  |  |  |  |  |  |
| 5 | RESET\# | B | 1 | 5VT |  | 4 | H | PU | Reset (active low) |
| 12 | XIN/EXTCLK | I | 1 |  |  |  |  |  | Crystal/Oscillator Input (10-60MHz) |
| 13 | XOUT | 0 | 0 |  |  |  |  |  | Crystal Output |
| 7 | VREG_CTRL | 0 | 0 |  |  |  |  |  | Voltage Regulator control output |
| 8 | VREG_ENABLE | I | I | 5VT |  |  |  |  | Voltage Regulator Enable |
| 6 | TEST_MODE | I | 1 | 5VT |  |  |  | PD | Test Mode |
| ARC JTAG |  |  |  |  |  |  |  |  |  |
| 1 | JTAG_CPU_TCK | 1 | 1 | 5VT |  |  |  | PU | ARC JTAG clock |
| 2 | JTAG_CPU_TDI | 1 | 1 | 5VT |  |  |  | PU | ARC JTAG data in. |
| 3 | JTAG_CPU_TDO | 0 | 0 |  | S | 8 |  |  | ARC JTAG data out |
| 4 | JTAG_CPU_TMS | I | I | 5VT |  |  |  | PU | ARC JTAG test mode |
| GPIO |  |  |  |  |  |  |  |  |  |
| 18 | IOPIN_GRP_0[0]/GPIO[0] | B | 1 | 5VT | S | 16 |  |  | General Purpose I/O |
| 19 | IOPIN_GRP_0[1]/GPIO[1] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 20 | IOPIN_GRP_0[2]/GPIO[2] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 21 | IOPIN_GRP_0[3]/GPIO[3] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 22 | IOPIN_GRP_O[4]/GPIO[4] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 23 | IOPIN_GRP_0[5]/GPIO[5] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 24 | IOPIN_GRP_0[6]/GPIO[6] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 25 | IOPIN_GRP_0[7]/GPIO[7] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 26 | IOPIN_GRP_1[0]/GPIO[8] | B | 1 | 5VT | S | 8 |  | PU | General Purpose I/O |
| 27 | IOPIN_GRP_1[1]/GPIO[9] | B | 1 | 5VT | S | 8 |  | PU | General Purpose I/O |
| 28 | IOPIN_GRP_1[2]/GPIO[10] | B | 1 | 5VT | S | 8 |  | PU | General Purpose I/O |
| 29 | IOPIN_GRP_1[3]/GPIO[11] | B | 1 | 5VT | S | 8 |  | PU | General Purpose I/O |
| 32 | IOPIN_GRP_1[4]/GPIO[12] | B | 1 | 5VT | S | 8 | H | PU | General Purpose I/O |
| 33 | IOPIN_GRP_1[5]/GPIO[13] | B | 1 | 5 VT | S | 8 | H | PU | General Purpose I/O |
| 34 | IOPIN_GRP_1[6]/GPIO[14] | B | 1 | 5VT | S | 8 | H | U | General Purpose I/O |
| 37 | IOPIN_GRP_1[7]/GPIO[15] | B | 1 | 5VT | S | 8 | H | U | General Purpose I/O |
| 38 | IOPIN_GRP_2[0]/GPIO[16] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 39 | IOPIN_GRP_2[1]/GPIO[17] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 40 | IOPIN_GRP_2[2]/GPIO[18] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 41 | IOPIN_GRP_2[3]/GPIO[19] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 42 | IOPIN_GRP_2[4]/GPIO[20] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 43 | IOPIN_GRP_2[5]/GPIO[21] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 44 | IOPIN_GRP_2[6]/GPIO[22] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 47 | IOPIN_GRP_2[7]/GPIO[23] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 48 | IOPIN_GRP_3[0]/GPIO[24] | B | I | 5VT | S | 16 |  |  | General Purpose I/O |
| 49 | IOPIN_GRP_3[1]/GPIO[25] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 50 | IOPIN_GRP_3[2]/GPIO[26] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 51 | IOPIN_GRP_3[3]/GPIO[27] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 52 | IOPIN_GRP_3[4]/GPIO[28] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 53 | IOPIN_GRP_3[5]/GPIO[29] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 54 | IOPIN_GRP_3[6]/GPIO[30] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 55 | IOPIN_GRP_3[7]/GPIO[31] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 56 | IOPIN_GRP_4[0]/GPIO[32] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 57 | IOPIN_GRP_4[1]/GPIO[33] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 58 | IOPIN_GRP_4[2]/GPIO[34] | B | 1 | 5 VT |  | 4 |  |  | General Purpose I/O |
| 59 | IOPIN_GRP_4[3]/GPIO[35] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 62 | IOPIN_GRP_4[4]/GPIO[36] | B | 1 | 5VT |  | 4 | H |  | General Purpose I/O |
| 63 | IOPIN_GRP_4[5]/GPIO[37] | B | 1 | 5VT |  | 4 | H |  | General Purpose I/O |
| 64 | IOPIN_GRP_4[6]/GPIO[38] | B | I | 5VT |  | 4 | H |  | General Purpose I/O |
| 65 | IOPIN_GRP_4[7]/GPIO[39] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 66 | IOPIN_GRP_5[0]/GPIO[40] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 67 | IOPIN_GRP_5[1]/GPIO[41] | B | I | 5VT | S | 8 |  |  | General Purpose I/O |
| 68 | IOPIN_GRP_5[2]/GPIO[42] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 69 | IOPIN_GRP_5[3]/GPIO[43] | B | 1 | 5 VT | S | 8 |  |  | General Purpose I/O |


| $\begin{aligned} & \hline \text { Pin } \\ & \text { No. } \end{aligned}$ | Pin Name | 1/0 | $\begin{gathered} \hline \text { Def. } \\ \text { I/O } \end{gathered}$ | 5VT | Slew Rate | Drv. Stre. | Hys | $\begin{aligned} & \hline \text { PU/ } \\ & \text { PD } \end{aligned}$ | Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 70 | IOPIN_GRP_5[4]/GPIO[44] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 71 | IOPIN_GRP_5[5]/GPIO[45] | B | I | 5VT | S | 8 |  |  | General Purpose I/O |
| 72 | IOPIN_GRP_5[6]/GPIO[46] | B | I | 5VT | S | 8 |  |  | General Purpose I/O |
| 75 | IOPIN_GRP_5[7]/GPIO[47] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 76 | IOPIN_GRP_6[0]/GPIO[48] | B | 1 | 5VT | S | 16 |  |  | General Purpose I/O |
| 79 | IOPIN_GRP_6[1]/GPIO[49] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 80 | IOPIN_GRP_6[2]/GPIO[50] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 81 | IOPIN_GRP_6[3]/GPIO[51] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 82 | IOPIN_GRP_6[4]/GPIO[52] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 83 | IOPIN_GRP_6[5]/GPIO[53] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 84 | IOPIN_GRP_6[6]/GPIO[54] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 85 | IOPIN_GRP_6[7]/GPIO[55] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 86 | IOPIN_GRP_7[0]/GPIO[56] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 89 | IOPIN_GRP_7[1]/GPIO[57] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 90 | IOPIN_GRP_7[2]/GPIO[58] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 91 | IOPIN_GRP_7[3]/GPIO[59] | B | 1 | 5 VT |  | 4 |  |  | General Purpose I/O |
| 92 | IOPIN_GRP_7[4]/GPIO[60] | B | 1 | 5VT |  | 4 | H |  | General Purpose I/O |
| 93 | IOPIN_GRP_7[5]/GPIO[61] | B | 1 | 5VT |  | 4 | H |  | General Purpose I/O |
| 94 | IOPIN_GRP_7[6]/GPIO[62] | B | I | 5VT |  | 4 | H |  | General Purpose I/O |
| 97 | IOPIN_GRP_7[7]/GPIO[63] | B | 1 | 5VT |  | 4 | H |  | General Purpose I/O |
| 98 | IOPIN_GRP_8[0]/GPIO[64] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 99 | IOPIN_GRP_8[1]/GPIO[65] | B | I | 5 VT | S | 8 |  |  | General Purpose I/O |
| 100 | IOPIN_GRP_8[2]/GPIO[66] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 101 | IOPIN_GRP_8[3]/GPIO[67] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 102 | IOPIN_GRP_8[4]/GPIO[68] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 103 | IOPIN_GRP_8[5]/GPIO[69] | B | I | 5VT | S | 8 |  |  | General Purpose I/O |
| 104 | IOPIN_GRP_8[6]/GPIO[70] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 105 | IOPIN_GRP_8[7]/GPIO[71] | B | 1 | 5 VT | S | 8 |  |  | General Purpose I/O |
| 106 | IOPIN_GRP_9[0]/GPIO[72] | B | I | 5VT | S | 16 |  |  | General Purpose I/O |
| 107 | IOPIN_GRP_9[1]/GPIO[73] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 108 | IOPIN_GRP_9[2]/GPIO[74] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 109 | IOPIN_GRP_9[3]/GPIO[75] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 112 | IOPIN_GRP_9[4]/GPIO[76] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 113 | IOPIN_GRP_9[5]/GPIO[77] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 114 | IOPIN_GRP_9[6]/GPIO[78] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 115 | IOPIN_GRP_9[7]/GPIO[79] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 116 | IOPIN_GRP_10[0]/GPIO[80] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 117 | IOPIN_GRP_10[1]/GPIO[81] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 118 | IOPIN_GRP_10[2]/GPIO[82] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 119 | IOPIN_GRP_10[3]/GPIO[83] | B | I | 5VT | S | 8 |  |  | General Purpose I/O |
| 120 | IOPIN_GRP_10[4]/GPIO[84] | B | 1 | 5VT | S | 8 | H |  | General Purpose I/O |
| 121 | IOPIN_GRP_10[5]/GPIO[85] | B | I | 5VT | S | 8 | H |  | General Purpose I/O |
| 122 | IOPIN_GRP_10[6]/GPIO[86] | B | I | 5VT | S | 8 | H |  | General Purpose I/O |
| 123 | IOPIN_GRP_10[7]/GPIO[87] | B | 1 | 5VT | S | 8 | H |  | General Purpose I/O |
| 124 | IOPIN_GRP_11[0]/GPIO[88] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 129 | IOPIN_GRP_11[1]/GPIO[89] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 130 | IOPIN_GRP_11[2]/GPIO[90] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 131 | IOPIN_GRP_11[3]/GPIO[91] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 132 | IOPIN_GRP_11[4]/GPIO[92] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 133 | IOPIN_GRP_11[5]/GPIO[93] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 134 | IOPIN_GRP_11[6]/GPIO[94] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 135 | IOPIN_GRP_11[7]/GPIO[95] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 136 | GPIO_116_96[0]/GPIO[96] | B | I | 5VT | S | 8 |  |  | General Purpose I/O |
| 137 | GPIO_116_96[1]/GPIO[97] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 138 | GPIO_116_96[2]/GPIO[98] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 139 | GPIO_116_96[3]/GPIO[99] | B | I | 5VT | S | 8 |  |  | General Purpose I/O |
| 140 | GPIO_116_96[4]/GPIO[100] | B | 1 | 5VT | S | 8 |  | PU | General Purpose I/O |

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| Pin No. | Pin Name | 1/0 | $\begin{aligned} & \hline \text { Def. } \\ & \text { I/O } \end{aligned}$ | 5VT | Slew Rate | Drv. Stre. | Hys | $\begin{aligned} & \hline \text { PU/ } \\ & \text { PD } \end{aligned}$ | Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 141 | GPIO_116_96[5]/GPIO[101] | B | I | 5VT | S | 8 |  | PU | General Purpose I/O |
| 142 | GPIO_116_96[6]/GPIO[102] | B | 1 | 5 VT | S | 8 |  |  | General Purpose I/O |
| 147 | GPIO_116_96[7]/GPIO[103] | B | 1 | 5VT | S | 8 |  |  | General Purpose I/O |
| 148 | GPIO_116_96[8]/GPIO[104] | B | 1 | 5VT | S | 8 |  | PU | General Purpose I/O |
| 149 | GPIO_116_96[9]/GPIO[105] | B | I | 5VT | S | 8 |  | PU | General Purpose I/O |
| 150 | GPIO_116_96[10]/GPIO[106] | B | 1 | 5VT | S | 8 |  | PU | General Purpose I/O |
| 151 | GPIO_116_96[11]/GPIO[107] | B | I | 5VT | S | 16 |  |  | General Purpose I/O |
| 152 | GPIO_116_96[12]/GPIO[108] | B | I | 5VT |  | 4 | H | PU | General Purpose I/O |
| 153 | GPIO_116_96[13]/GPIO[109] | B | 1 | 5VT |  | 4 | H | PU | General Purpose I/O |
| 154 | GPIO_116_96[14]/GPIO[110] | B | 1 | 5VT |  | 4 |  | PU | General Purpose I/O |
| 155 | GPIO_116_96[15]/GPIO[111] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 156 | GPIO_116_96[16]/GPIO[112] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 157 | GPIO_116_96[17]/GPIO[113] | B | I | 5VT |  | 4 |  |  | General Purpose I/O |
| 158 | GPIO_116_96[18]/GPIO[114] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 161 | GPIO_116_96[19]/GPIO[115] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| 162 | GPIO_116_96[20]/GPIO[116] | B | 1 | 5VT |  | 4 |  |  | General Purpose I/O |
| SDR SDRAM Interface |  |  |  |  |  |  |  |  |  |
| 163 | DQ[0] | B | 1 | 5VT |  | 8 |  |  | SDRAM Data |
| 164 | DQ[1] | B | 1 | 5VT |  | 8 |  |  | SDRAM Data |
| 165 | DQ[2] | B | 1 | 5VT |  | 8 |  |  | SDRAM Data |
| 166 | DQ[3] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 167 | DQ[4] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 168 | DQ[5] | B | 1 | 5VT |  | 8 |  |  | SDRAM Data |
| 169 | DQ[6] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 172 | DQ[7] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 173 | DQ[15] | B | 1 | 5VT |  | 8 |  |  | SDRAM Data |
| 174 | DQ[14] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 175 | DQ[13] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 176 | DQ[12] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 177 | DQ[11] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 178 | DQ[10] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 179 | DQ[9] | B | I | 5VT |  | 8 |  |  | SDRAM Data |
| 182 | DQ[8] | B | 1 | 5VT |  | 8 |  |  | SDRAM Data |
| 183 | DQM[1] | 0 | 0 |  |  | 8 |  |  | SDRAM Data Mask |
| 184 | DQM[2] | 0 | 0 |  |  | 8 |  |  | SDRAM Data Mask |
| 185 | CLK | 0 | 0 |  |  | 12 |  |  | SDRAM Clock |
| 186 | CKE | 0 | 0 |  |  | 8 |  |  | SDRAM Clock Enable |
| 187 | WE\# | 0 | 0 |  |  | 8 |  |  | SDRAM Write Enable |
| 188 | CAS\# | 0 | 0 |  |  | 8 |  |  | SDRAM Column Address Select |
| 189 | RAS\# | 0 | 0 |  |  | 8 |  |  | SDRAM Row Address Select |
| 194 | BA[0] | 0 | 0 |  |  | 8 |  |  | SDRAM Bank Active |
| 195 | BA[1] | 0 | 0 |  |  | 8 |  |  | SDRAM Bank Active |
| 196 | M_ADDR[0] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 197 | M_ADDR[1] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 198 | M_ADDR[2] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 199 | M_ADDR[3] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 200 | M_ADDR[12] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 201 | M_ADDR[11] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 204 | M_ADDR[10] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 205 | M_ADDR[9] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 206 | M_ADDR[8] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 207 | M_ADDR[7] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 208 | M_ADDR[6] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 209 | M_ADDR[5] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |
| 210 | M_ADDR[4] | 0 | 0 |  |  | 8 |  |  | SDRAM Address |

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| $\begin{aligned} & \hline \text { Pin } \\ & \text { No. } \\ & \hline \end{aligned}$ | Pin Name | I/O | $\begin{array}{\|c} \hline \text { Def. } \\ \text { I/O } \\ \hline \end{array}$ | 5VT | Slew <br> Rate | Drv. Stre. | Hys | $\begin{aligned} & \hline \text { PU/ } \\ & \text { PD } \end{aligned}$ | Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Real Time Clock |  |  |  |  |  |  |  |  |  |
| 211 | VDD_RTC | $\begin{gathered} \hline \mathrm{PW} \\ \mathrm{R} \\ \hline \end{gathered}$ |  |  |  |  |  |  | 3.3V Battery Power |
| 212 | RTC_XIN | I | 1 |  |  |  |  |  | 32 KHz Crystal/Oscillator input |
| 213 | RTC_XOUT | 0 | 0 |  |  |  |  |  | 32 KHz Crystal output |
| 214 | CORE_PWR_DET | 1 | I |  |  |  | H |  | Power detection signal |
| 215 | BATRST\# | 1 | I |  |  |  | H |  | Battery reset (active low) |
| 216 | VSS_RTC | $\begin{gathered} \hline \mathrm{GN} \\ \mathrm{D} \end{gathered}$ |  |  |  |  |  |  | Battery Ground |
| Power and Ground |  |  |  |  |  |  |  |  |  |
| 9 | VREG_VDD | $\begin{gathered} \hline \mathrm{PW} \\ \mathrm{R} \end{gathered}$ |  |  |  |  |  |  | 3.3V Voltage Regulator Power |
| 10 | AVSS/ARCPLL_VSS | $\begin{gathered} \text { GN } \\ \mathrm{D} \\ \hline \end{gathered}$ |  |  |  |  |  |  | Voltage Regulator Ground/PLL Ground |
| 11 | ARCPLL_VDD | $\begin{gathered} \hline \mathrm{PW} \\ \mathrm{R} \end{gathered}$ |  |  |  |  |  |  | 1.25V PLL Power |
| $\begin{aligned} & \hline 17 \\ & 35 \\ & 73 \\ & 87 \\ & 128 \\ & 143 \\ & 193 \\ & \hline \end{aligned}$ | CORE_VDD | $\begin{gathered} \hline \mathrm{PW} \\ \mathrm{R} \end{gathered}$ |  |  |  |  |  |  | 1.25V Core Power |
| $\begin{aligned} & 14 \\ & 31 \\ & 45 \\ & 60 \\ & 68 \\ & 78 \\ & 95 \\ & 110 \\ & 125 \\ & 146 \\ & 159 \\ & 170 \\ & 190 \\ & 202 \\ & \hline \end{aligned}$ | IO_VDD | $\begin{gathered} \hline \mathrm{PW} \\ \mathrm{R} \end{gathered}$ |  |  |  |  |  |  | 3.3V Power |
| $\begin{gathered} \hline 16 \\ 36 \\ 74 \\ 88 \\ 127 \\ 144 \\ 192 \\ \hline \end{gathered}$ | CORE_VSS | $\begin{gathered} \mathrm{GN} \\ \mathrm{D} \end{gathered}$ |  |  |  |  |  |  | Core Ground |
| 15 <br> 15 <br> 30 <br> 46 <br> 61 <br> 77 <br> 96 <br> 111 <br> 126 <br> 145 <br> 160 <br> 171 <br> 180 <br> 181 <br> 191 <br> 203 | IO_VSS | $\begin{gathered} \hline \mathrm{GN} \\ \mathrm{D} \end{gathered}$ |  |  |  |  |  |  | I/O Ground |

### 2.3. IC301 (CX20452) Modem block

### 2.3.1 Summary

The CONEXANT CX20452 Codec is an Integrated Analog (IA) circuit providing modem digital-to-analog and analog-to-digital conversion. It is packaged in a 24 -pin QFN. Ordering information is listed in Table 1-1.
The CX20452 Codec can serve as a line interface device between a CONEXANT® Modem Data Pump (MDP) and the PSTN. It can also serve as a voice interface between the MDP and a microphone and speaker.
The MDP communicates with the CX20452 Codec via a serial interface. The CX20452 Codec has an 8 -bit register that is used to configure the device. This register may be written to or read from via the serial interface.

The CX20452 Codec receive path consists of line and microphone gain stages, line-input and mic-input anti-aliasing filters, a second order deltasigma ADC, and a third-order sinc decimation filter. The transmit path consists of a third-order interpolation filter, a second-order delta-sigma DAC, a first-order lowpass switched capacitor filter, a second order lowpass continuous-time filter, a line-output driver, and a speaker-output driver. It has its own control registers, timing logic, serial interface, references, and microphone bias circuit.

The device is designed to run at a clock rate of 1.9584 MHz . Adjustable oversampling ratios (OSRs) are provided to allow for other master clock frequencies. Local and remote loopbacks are available for functionality and ease of testing.

The CX20452 Codec operates with digital power supply (VDD) and analog power supply (AVDD) $=3.3 \mathrm{~V}$.

| Order No./Part No. |  |
| :--- | :--- |
| CX20452-A Package |  |

Table 1-1. Ordering Information

### 2.3.2 Functional description



Fig. 3

## 1) Microphone input

This input provides programmable gain for a microphone signal and low pass filtering to avoid aliasing before the signal is converted into a digital format. The microphone can be connected to this device as single-ended. A programmable gain amplifier provides four selectable gain settings from 6 to 26 dB , in steps of 5 dB . The input impedance is $150 \mathrm{k} \Omega$ minimum.

## 2) Line input

This input provides low pass filtering to avoid aliasing before the signal is converted into a digital format. The line input is single-ended and must be AC coupled into the device. The filter structure realizes a two-pole LPF filter. The gain on the line input is selectable at 6 dB or 16 dB . The input impedance is $150 \mathrm{k} \Omega$ minimum. Either Line input or Microphone input can be selected.

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## 3) $\mathrm{A} / \mathrm{D}$ converter

The ADC input provides a $0 /-4 \mathrm{dBm}$ low pass filter. The ADC is a second order sigma-delta type ADC which samples at a rate between 1.024 MHz and 2.048 MHz to produce a programmable baseband sample, depending on the decimation ratio programmed in the decimation filter and the clock value.

## 4) D/A converter

The incoming digital signal from the MDP is fed to a low pass interpolation filter and then to a second order delta-sigma type DAC. The DAC analog output drivers a switched capacitor analog filter. The output of this filter is passed to a passive continuous-time second order low pass filter that removes signal images around the switched capacitor clock frequency.

## 5) Speaker output

The SPKR_OUT output signal can drive a $150 \Omega$ resistive load. The speaker driver is intended to buffer and drive the low impedance speaker load with the signal selected on its input. The microphone input, or the line input, or the transmit output signal can be selected to be the speaker output. The speaker driver output can be attenuated or muted. It also has power down mode.

## 6) Line output

The main purpose of the line output stage is to buffer the signal and drive the low impedance line load with the signal selected on its input. The microphone input, or the line input, or the transmit output signal can be selected to be the line output. The line drive provides a $600 \Omega$ differential output.

## 7) Power up condition

Upon application of the both AVDD and VDD power, the power up sequence consists of:

1. the internal reset circuit becoming activated.
2. The VREF and VC generators powering up.

This power-on sequence takes approximately 50 ms .

### 2.4. IC702 (LAN91C113) Hardware description

### 2.4.1 General description

The SMSC LAN91C113 is designed to facilitate the implementation of a third generation of Fast Ethernet connectivity solutions for embedded applications. For this third generation of products, flexibility and integration dominate the design requirements. The LAN91C113 is a mixed signal Analog/ Digital device that implements the MAC and PHY portion of the CSMA/CD protocol at 10 and 100 Mbps . The design will also minimize data throughput constraints utilizing a 16 -bit or 8 -bit bus Host interface in embedded applications.

The total internal memory FIFO buffer size is 8 Kbytes, which is the total chip storage for transmit and receive operations.
The SMSC LAN91C113 is software compatible with the LAN9000 family of products.
Memory management is handled using a patented optimized MMU (Memory Management Unit) architecture and a 16 -bit wide internal data path. This I/O mapped architecture can sustain back-to-back frame transmission and reception for superior data throughput and optimal performance. It also dynamically allocates buffer memory in an efficient buffer utilization scheme, reducing software tasks and relieving the host CPU from performing these housekeeping functions.
The SMSC 91C113 provides a flexible slave interface for easy connectivity with industry-standard buses. The Bus Interface Unit (BIU) can handle synchronous as well as asynchronous transfers, with different signals being used for each one. Asynchronous bus support for ISA is supported even though ISA cannot sustain 100 Mbps traffic. Fast Ethernet data rates are attainable for ISA-based nodes on the basis of the aggregate traffic benefits.
Two different interfaces are supported on the network side. The first Interface is a standard Magnetics transmit/receive pair interfacing to 10/100BaseT utilizing the internal physical layer block. The second interface follows the MII (Media Independent Interface) specification standard, consisting of 4 bit wide data transfers at the nibble rate. This interface is applicable to 10 Mbps standard Ethernet or 100 Mbps Ethernet networks. Three of the LAN91C113's pins are used to interface to the two-line MII serial management protocol.

The SMSC LAN91C113 integrates IEEE 802.3 Physical Layer for twisted pair Ethernet applications. The PHY can be configured for either 100 Mbps (100Base-TX) or 10 Mbps (10Base-T) Ethernet operation. The Analog PHY block consists of a 4B5B>Manchester encoder/decoder, scrambler/descrambler, transmitter with wave shaping and output driver, twisted pair receiver with on chip equalizer and baseline wander correction, clock and data recovery, Auto-Negotiation, controller interface (MII), and serial port (MI). Internal output wave shaping circuitry and on-chip filters eliminate the need for external filters normally required in 100Base-TX and 10Base-T applications.
The LAN91C113 can automatically configure itself for 100 or 10 Mbps and Full or Half Duplex operation with the on-chip Auto-Negotiation algorithm. The LAN91C113 is ideal for media interfaces for embedded application desiring Ethernet connectivity as well as 100Base-TX/10Base-T adapter cards, motherboards, repeaters, switching hubs. The LAN91C113 operates from a single 3.3 V supply. The inputs and outputs of the host interface are 5 V tolerant and will directly interface to other 5 V devices.

### 2.4.2 Description of pin functions

## IC702: VHILN91C113-1 (LAN91C113)

| PIN NO. |  | NAME | SYMBOL | BUFFER <br> TYPE | DESCRIPTION |
| :--- | :--- | :---: | :---: | :---: | :--- |
| TQFP | QFP |  | Address | A4-A15 | $\mathrm{I}^{* *}$ |
| $81-92$ | $83-94$ | Address | A1-A3 | $\mathrm{I}^{* *}$ | Input. Decoded by LAN91C113 to determine access <br> to its registers. |
| $78-80$ | $80-82$ | Address Enable | AEN | $\mathrm{I}^{\star *}$ | Input. Used by LAN91C113 for internal register <br> selection. Used as an address qualifier. Address decod- <br> ing is only enabled when AEN is low. |
| 41 | 43 |  |  |  |  |


| PIN NO. |  | NAME | SYMBOL | BUFFER TYPE | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
| TQFP | QFP |  |  |  |  |
| 94-95 | 96-97 | nByte Enable | nBE0-nBE1 | \|** | Input. Used during LAN91C113 register accesses to determine the width of the access and the register(s) being accessed. |
| $\begin{aligned} & 107-104,102-99, \\ & 76-73,71-68 \end{aligned}$ | $\begin{aligned} & \hline 109-106,104- \\ & 101,78-75,73-70 \end{aligned}$ | Data Bus | D0-D15 | 1/O24** | Bidirectional. 16 bit data bus used to access the LAN91C113's internal registers. Data bus has weak internal pullups. Supports direct connection to the system bus without external buffering. |
| 30 | 32 | Reset | RESET | IS** | Input. When this pin is asserted high, the controller performs an internal system (MAC \& PHY) reset. It programs all the registers to their default value, the controller will read the EEPROM device through the EEPROM interface. (Note 5.1) This input is not considered active unless it is active for at least 100 ns to filter narrow glitches. |
| 37 | 39 | nAddress Strobe | nADS | IS** | Input. For systems that require address latching, the rising edge of nADS indicates the latching moment for A1-A15 and AEN. All LAN91C113 internal functions of A1-A15, AEN are latched except for nLDEV decoding. |
| 35 | 37 | nCycle | nCYCLE | \|** | Input. This active low signal is used to control LAN91C113 synchronous bus cycles. For write operation, this signal should be asserted one bus clock prior to data valid. For read operation, this signal should be asserted two bus clocks prior to data valid. |
| 36 | 38 | Write/nRead | W/nR | IS** | Input. Defines the direction of synchronous cycles. Write cycles when high, read cycles when low. |
| 40 | 42 | nVL Bus Access | nVLBUS | I with pullup** | Input. When low, the LAN91C113 synchronous bus interface is configured for Local Bus mode accesses. Otherwise, the LAN91C113 is configured for EISA accesses. Does not affect the asynchronous bus interface. |
| 42 | 44 | Local Bus Clock | LCLK | \|** | Input. Used to interface synchronous buses. Maximum frequency is 50 MHz . This pin should be tied high if it is in asynchronous mode. |
| 38 | 40 | Asynchronous Ready | ARDY | OD16 | Open drain output. ARDY may be used when interfacing asynchronous buses to extend accesses. Its rising (access completion) edge is controlled by the XTAL1 clock and, therefore, asynchronous to the host CPU or bus clock. |
| 43 | 45 | nSynchronous Ready | nSRDY | 016 | Output. This output is used when interfacing synchronous buses and $\mathrm{nVLBUS}=0$ to extend accesses. This signal remains normally inactive, and its falling edge indicates completion. This signal is synchronous to the bus clock LCLK. |
| 46 | 48 | nReady Return | nRDYRTN | \|** | Input. This input is used to complete synchronous read cycles. |
| 29 | 31 | Interrupt | INTR0 | O24 | Interrupt Output - Used to interrupt the Host on a status event. Note: The selection bits used to determined by the value of INT SEL 1-0 bits in the Configuration Register are no longer required and have been set to reserved in this revision of the FEAST family of devices. |
| 45 | 47 | nLocal Device | nLDEV | 016 | Output. This active low output is asserted when AEN is low and A4-A15 decode to the LAN91C113 address programmed into the high byte of the Base Address Register. nLDEV is a combinatorial decode of unlatched address and AEN signals. |
| 31 | 33 | nRead Strobe | nRD | IS** | Input. Used in asynchronous bus interfaces. |
| 32 | 34 | nWrite Strobe | nWR | IS** | Input. Used in asynchronous bus interfaces. |
| 9 | 11 | EEPROM Clock | EESK | O4 | Output. 4 usec clock used to shift data in and out of the serial EEPROM. |
| 10 | 12 | EEPROM Select | EECS | O4 | Output. Serial EEPROM chip select. Used for selection and command framing of the serial EEPROM. |
| 7 | 9 | EEPROM Data Out | EEDO | O4 | Output. Connected to the DI input of the serial EEPROM. |
| 8 | 10 | EEPROM Data In | EEDI | I with pulldown** | Input. Connected to the DO output of the serial EEPROM. |


| PIN NO. |  | NAME | SYMBOL | BUFFER TYPE | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
| TQFP | QFP |  |  |  |  |
| 3-5 | 5-7 | I/O Base | IOS0-IOS2 | I with pullup** | Input. External switches can be connected to these lines to select between predefined EEPROM configurations. |
| 6 | 8 | Enable EEPROM | ENEEP | I with pullup** | Input. Enables (when high or open) LAN91C113 accesses to the serial EEPROM. Must be grounded if no EEPROM is connected to the LAN91C113. |
| 127, 128 | 1,2 | Crystal 1 Crystal 2 | XTAL1 <br> XTAL2 | Iclk | An external 25 MHz crystal is connected across these pins. If a TTL clock is supplied instead, it should be connected to XTAL1 and XTAL2 should be left open. |
| $\begin{aligned} & 1,33,44,62,77, \\ & 98,110,120,96, \\ & 97 \end{aligned}$ | $\begin{aligned} & 3,35,46,64,79, \\ & 100,112,122,98, \\ & 99 \end{aligned}$ | Power | VDD | - | +3.3V Power supply pins. |
| 11, 16 | 13, 18 | Analog Power | AVDD | - | +3.3V Analog power supply pins. |
| $\begin{aligned} & 24,39,52,57,67, \\ & 72,93,103,108, \\ & 117 \end{aligned}$ | $\begin{aligned} & 26,41,54,59,69, \\ & 74,95,105,110, \\ & 119 \end{aligned}$ | Ground | GND | - | Ground pins. |
| 13, 19 | 15, 21 | Analog Ground | AGND | - | Analog Ground pins |
| 21 | 23 | Loopback | O4 | - | Output. Active when LOOP bit is set (TCR bit 1). |
| 20 | 22 | nLink Status | nLNK | I with pullup | Input General-purpose input port used to convey LINK status (EPHSR bit 14). |
| 28 | 30 | nCNTRL | nCNTRL | 012 | General Purpose Control Pin |
| 47 | 49 | X25out | X25out | 012 | 25Mhz Output to external PHY |
| 111 | 113 | Transmit Enable 100 Mbps | TXEN100 | O12 | Output to MII PHY. Envelope to 100 Mbps transmission. |
| 119 | 121 | Carrier Sense 100 Mbps | CRS100 | I with pulldown | Input from MII PHY. Envelope of packet reception used for deferral and backoff purposes. |
| 125 | 127 | Receive Data Valid | RX_DV | I with pulldown | Input from MII PHY. Envelope of data valid reception. Used for receive data framing. |
| 112 | 114 | Collision Detect 100 Mbps | COL100 | I with pulldown | Input from MII PHY. Collision detection input. |
| 113-116 | 115-118 | Transmit Data | TXD3-TXD0 | O12 | Outputs. Transmit Data nibble to MII PHY. |
| 109 | 111 | Transmit Clock | TX25 | I with pullup | Input. Transmit clock input from MII. Nibble rale clock ( 25 MHz for $100 \mathrm{Mbps} \& 2.5 \mathrm{MHz}$ for 10 Mbps ). |
| 118 | 120 | Receive Clock | RX25 | I with pullup | Input. Receive clock input from MII PHY. Nibble rate clock. ( 25 MHz for $100 \mathrm{Mbps} \& 2.5 \mathrm{MHz}$ for 10 Mbps ). |
| 121-124 | 123-126 | Receive Data | RXD3-RXD0 | I with pullup | Inputs. Received Data nibble from MII PHY. |
| 25 | 27 | Management Data Input | MDI | I with pulldown | MII management data input. |
| 26 | 28 | Management Data Output | MDO | O4 | MII management data output. |
| 27 | 29 | Management Clock | MCLK | O4 | MII management clock. |
| 126 | 128 | Receive Error | RX_ER | I with pulldown | Input. Indicates a code error detected by PHY. Used by the LAN91C113 to discard the packet being received. The error indication reported for this event is the same as a bad CRC (Receive Status Word bit 13). |
| 2 | 4 | nChip Select Output | nCSOUT | O4 | Output. Chip Select provided for mapping of PHY functions into LAN91C113 decoded space. Active on accesses to LAN91C113's eight lower addresses when the BANK SELECTED is 7 . |
| 12 | 14 | External Resistor | RBIAS | NA | Transmit Current Set. An external resistor connected between this pin and GND will set the output current for the TP transmit outputs. |
| 14 | 16 | - | TPO+ | O/l | Twisted Pair Transmit Output, Positive. |
| 15 | 17 | - | TPO- | O/I | Twisted Pair Transmit Output, Negative. |
| 17 | 19 | - | TPI+ | I/O | Twisted Pair Receive Input, Positive. |
| 18 | 20 | - | TPI- | I/O | Twisted Pair Receive Input, Negative. |
| 22 | 24 | - | nLEDA | OD24 | PHY LED Output |
| 23 | 25 | - | nLEDB | OD24 | PHY LED Output |
| $\begin{aligned} & \hline 34,48-51,53- \\ & 56,58-61,63-66 \\ & \hline \end{aligned}$ | $\begin{array}{\|l\|} \hline 36,50-53,55- \\ 58,60-63,65-68 \end{array}$ | - | NC | - | No Connection |

### 2.5. IC600 (14D0830) Hardware description

This specfication describes a multifunction analog ASIC to be used in inkjet printer applications. This ASIC integrates two Switching Voltage Regulator circuits, two motor drive circuits and a Reset circuit in a single IC.

### 2.5.1 Switching voltage regulators

The ASIC is to support two, buck type, switching, voltage regulators. Each is of the comparitor type, without linear error amplifiers or external compensation networks. For each regulator, the ASIC is to contain: a comparitor with internal reference, internal oscillator, output pass switch, internal current sense and current limit. An external flyback diode, inductor, and filter capacitor is to be used. Output voltage is to be set with an external voltage divider network. VPH_Reg is to be disabled through serial command. VPH_Reg may also be disabled by means of an inhibit pin. Vcc_Reg is to function at all times. Each regulator has a 1 ma minimum load.
The regulators are to be designed to support the voltages and currents listed below using the internal pass switch and sense resistor. VPH_Reg will also have the capability to support higher currents than the internal switch supports, through the addition of an external MTD3055V MOSFET and current sense resistor. A pin will be brought out from the gate drive of the internal switch to drive the gate of the external MOSFET. The external sense resistor will be wired as suggested by the ASIC vender.
Intended voltages and loads:
VPH_Reg: 3A DC Load (Peak/Ave)
+/- 2\% Reg reference tolerance
Can be disabled through serial command or inhibit pin.
Output voltage can be set from VREF to 13 V using the external resistor divider network.
Vcc_Reg: 1000ma DC Load (Not used)
(+/- $2 \%$ ) Reg.
Output voltage is intended to supply machine Vcc at 5 or 3.3 V . Voltage is set with an external voltage divider.

### 2.5.2 Load detect

For VPH_REG, the DC current from the regulator shall be indicated through an output pin.

### 2.5.3 2ea. DC motor drivers

Bi-directional, pulse width modulated each with an external PWM input. These motor drives must be able to support a minimum output of 1.6 A for 100 ms . For thermal budgeting, the maximum average current will be 450 mA 'long term average' with an average of 750 mA over periods of up to 1 sec . At the current limit (1.6A or greater), the motor drive should 'chop' at the limited current level. The DC motor direction is serially controlled.

### 2.5.4 1ea. Configurable motor driver

This motor drive is selectable to be operated in one of two modes, DC or stepper. In DC motor mode, the drive is to operate in the same manner as the DC motor drivers previously described. In stepper mode, the drive is to drive a bipolar stepper motor with average current levels of 183,367 , and 550 mA per phase with quarter step mode capability. Phase and current information will be sent over the serial port.
This machine uses "Stepper mode" for TX motor driving. And uses "DC motor mode" for Carrier motor \& Feed motor driving.

IC600 (14D0830) Pin descriptions (1/2)

| $\begin{aligned} & \hline \text { PIN } \\ & \text { NO. } \end{aligned}$ | PIN NAME | PIN DESCRPTION |
| :---: | :---: | :---: |
| 1 | DC1_A or DC1+ | Pos output for DC mtr drvr \#1 |
| 2 | GND | Ground |
| 3 | DC1B or DC1- | Neg output for DC Mtr drvr \#1 |
| 4 | DC2B or DC2- | Neg output for DC Mtr drvr \#2 |
| 5 | GND | Ground |
| 6 | DC2_A or DC2+ | Pos output for DC mtr drvr \#2 |
| 7 | VBULK | 20 to 33V Input |
| 8 | MD2_PWM | Pwm pin for DC mtr drvr \#2 |
| 9 | GND | Ground |
| 10 | NVPH_Inhibit | Inhibit pin for VPH reg, active low |
| 11 | nRESET | nRESET input/output pin |
| 12 | SDATA | Serial data input |
| 13 | SCLK | Serial data input clock |
| 14 | nCS | Serial input register chip select |
| 15 | OASUPPLY | Opamp supply voltage |
| 16 | OA10UT | Output for OpAmp \#1 |
| 17 | OA1+ or oa1plus | Noninverting input for opamp \#1 |
| 18 | OA1- or oa1minus | Inverting input for opamp \#1 |
| 19 | OA2OUT | Output for OpAmp \#2 |
| 20 | OA2+ or oa2plus | Noninverting input for opamp \#2 |
| 21 | OA2- or oa2minus | Inverting input for opamp \#2 |
| 22 | OA3OUT | Output for OpAmp \#3 |
| 23 | OA3+ or oa3plus | Noninverting input for opamp \#3 |
| 24 | OA3- or oa3minus | Inverting input for opamp \#3 |
| 25 | OAGND | Opamp ground |
| 26 | VBULK | - |
| 27 | MD3_OUT_A_A or cm1A or MD3_OUT_A+ | Phase A+ output for motor driver \#3 |
| 28 | MD3_R_SENSE_A or rsense1 | Phase A sense pin. Ground solidly to ASIC ground, if MD3 used in DC mtr config. |
| 29 | MD3_OUT_A_B or cm1B or MD3 OUT A- | Phase A- output for motor driver \#3 |
| 30 | $\begin{gathered} \hline \text { MD3_OUT_B_B or } \\ \text { cm2B or } \\ \text { MD3_OUT_B- } \\ \hline \end{gathered}$ | Phase B- output for motor driver \#3 |
| 31 | MD3_R_SENSE_B or rsense2 | Phase B sense pin. Ground solidly to ASIC ground, if MD3 used in DC mtr config. |
| 32 | MD3_OUT_B_A or cm2A or MD3_OUT_B+ | Phase B+ output for motor driver \#3 |
| 33 | VBULK | - |
| 34 | MD3_MODE or mode | Mode pin input for mtr drvr \#3 |
| 35 | MD3_PWM | Pwm pin for DC mtr drvr \#3 |
| 36 | SWITCH_OUT | Source pin for printhead Vcc switch |
| 37 | SWITCH_IN | Drain pint for printhead Vcc switch. Also serves as the power supply for the Vcc Base Drive circuit. |
| 38 | BASE_DR_FB or v1p8fb | feedback pin for NPN base drive. Tie high if not used. |
| 39 | BASE_DR_OUT or v1p8base | Output drive pin for NPN base drive |
| 40 | CPL | Charge pump pin |
| 41 | CPH | Charge pump pin |
| 42 | VP | Charge pump storage cap pin |
| 43 | GND | - |
| 44 | VCC_FB | Feedback pin for Vcc Reg. Tie high if not used. |

IC600 (14D0830) Pin descriptions (2/2)

| PIN <br> NO. | PIN NAME | PIN DESCRPTION |
| :---: | :---: | :--- |
| 45 | VBULK | Source pin for Vcc Reg. Place a 0.1uF <br> decoupling cap close to this pin. |
| 46 | VCC_SOURCE or <br> vccs | Source pin for Vcc reg. Flyback diode <br> should be placed close to this pin. |
| 47 | VBULK | Source pin for VPH regulator. Place <br> $0.1 u F ~ d e c o u p l i n g ~ c a p ~ c l o s e ~ t o ~ t h i s ~ p i n . ~$ |
| 48 | VBULK | Source pin for VPH regulator. Place <br> 0.1uF decoupling cap close to this pin. |
| 49 | R_SENSE_VPH or <br> vphdsense | Pin connect for ext sense resister, if <br> used. |
| 50 | VPH_SOURCE or <br> vphs | Source pin for VPH regulator. Flyback <br> diode should be placed close to this <br> pin. |
| 51 | VBULK | Source pin for VPH regulator. Flyback <br> diode should be placed close to this <br> pin. |
| 52 | VPH_GATE or | Ext gate drive output. N/C if not used. |
| vphg |  |  |


| PIN <br> NO. | PIN NAME | PIN DESCRPTION |
| :---: | :---: | :--- |
| 53 | VPHSCLAMP | VPH reg source clamp |
| 54 | VPH_LD_DET or <br> vphload | Load detect output pin |
| 55 | VPH_FB or vphfb | Feedback pin for the VPH reg |
| 56 | LED1 | Open collector output |
| 57 | LED2 | Open collector output |
| 58 | LED3 | Open collector output |
| 59 | TRML_SNS or <br> thermwarn | Thermal warning open collector output |
| 60 | DO1 | Open collector output |
| 61 | DO2 | Open collector output |
| 62 | DO3 | Open collector output |
| 63 | MD1_PWM or <br> dc1pwm | Pwm pin input for mtr drvr \#1 |
| 64 | VBULK | - |
| 65 | Thermal Pad, GND | Should be closely grounded with other <br> ASIC grounds. |

### 2.6. Panel control block

The following controls are performed by the CX93310.

- Operation panel key scanning
- Operation panel LCD display
- Sense the Original sensor, Front sensor and Door sensor.


### 2.7. Mechanism/recording control block

- Recording control block diagram


Fig. 4

## [3] Circuit description of LIU PWB

## 1. LIU block operational description

### 1.1. Block diagram



Fig. 5

### 1.2. Circuit description

The LIU PWB is composed of the following 7 blocks.

1. Speech circuit section
2. Dial transmission section
3. Speaker amplifier section
4. Ringer circuit section
5. Cl detection circuit
6. Signal/DTMF transmission level \& receiving level
7. Power supply and bias circuit

### 1.3. Block description

1. Speech circuit section

- The receiver volume is an electronic volume type, this model is switched in 3 steps.

2. Dial transmission section

- D.P. transmission: The CML relay is turned on and off for control in the DP calling system. (Refer to the attached sheet.)
- DTMF transmission: It is formed in the modem, and is output.

3. Speaker amplifier section

- Ringer volume: It is controlled by the combination of the attenuator value of the SPEAKER DRIVER in the modem and the ringer sending level sent from the modem.
- Speaker volume: It is controlled by the attenuator value of the SPEAKER DRIVER in the modem.

4. Ringer circuit section

- The ringer sound is formed in the tone of modem when Cl signal is detected. The amplifier circuit drives the speaker of the main body.

5. Cl detection circuit

- Cl is detected by the photo coupler which is integrated in series in the primary side TEL circuit well proven in the existing unit.

6. Signal/DTMF transmission level \& receiving level

- Signal transmission level setting: According to soft switch list.
- DTMF transmission level setting: According to soft switch list

7. Power supply and bias circuit

- The voltage of +24 V are supplied from the control PWB unit.


## UX-B800A

### 1.4. Signal selection

The following signals are used to control the transmission line of TEL/ FAX signal. For details, refer to the signal selector matrix table.
[Control signals from output port]

| Signal Name | Description |
| :---: | :--- |
| CML | Line connecting relay and DP generating relay <br> H: Line make <br> L: Line break |
| SP MUTE | Speaker tone mute control signal <br> H: Muting (Power down mode) <br> L: Muting cancel (Normal operation) |
| TELMUTE | Handset reception mute control signal <br> H: Muting <br> L: Muting cancel |

[Signals for status recognition according to input signals]

| Signal Name | Function |
| :--- | :--- |
| RHS- | H: The handset is in the on-hook state. <br> L: The handset is in the off-hook state. |
| Cl- | Incoming call $(\mathrm{Cl})$ detection signal. |

(Example: SENDING/RECEIVING)


Fig. 6

## 1. Block diagram

This power supply unit has the function to convert the $A C 220-230 \mathrm{~V}(50 / 60 \mathrm{~Hz})$ to DC 5 V , and provide these outputs to the equipment. The following explains the function of each block.


## 2. Filter circuit block

This circuit reduces the outgoing noise through the input lines which is generated in the power supply unit, and prevents the invasion of the noise from the lines.(the excessive surge such as the thunder is prevented by the varistor(Z1).)

## 3. Rectification and smoothing circuit block

This circuit rectifies and smoothes the AC input, and provides the DC voltage to the switching circuit block.

## 4. Switching circuit block

This circuit converts the DC voltage (provided from the Rectification and smoothing circuit block) to the high frequency pulse voltage by FET(Q1)'s switching (on/off repeat), and provides the energy to the transformer(T1). It discharges the energy (charged during the FET ON time) to the secondary side during the FET OFF time through the secondary windings. The output voltage on the secondary side provided by the energy depend on the ratio of the winding turns (primary: secondary) etc.

## [5] Circuit description of CIS unit

## 1. CIS

CIS is an image sensor which puts the original paper in close contact with the full-size sensor for scanning, being a monochromatic type with the pixel number of 1,728 dots and the main scanning density of 8 dots/mm

It is composed of sensor, rod lens, LED light source, light-conductive plate, control circuit and so on, and the reading line and focus are previously adjusted as the unit.

Due to the full-size sensor, the focus distance is so short that the set is changed from the light weight type to the compact type.

## 2. Waveforms

The following clock is supplied from CX93310 of the control board, and VO is output.

## 5. Control circuit block

This circuit block controls the output voltage by transmitting the detected +5 V voltage to the primary control circuit through the photocoupler(PC1). In case of the over-current, this circuit reduces providing the energy to the transformer. In case of the over-voltage, this circuit reduces providing the energy to the transformer by letting the Power-Zener(D104; connected between the +30 V output voltage and GND) into the short mode and letting the over-current protection circuit work.

## 6. +30 V circuit block

This circuit block rectifies and smoothes the high-frequency pulse voltage provided by the transformer, and provides the DC +30 V output to the equipment.

## 7. +5 V circuit block

This circuit block rectifies and smoothes the high-frequency pulse voltage provided by the transformer, and provides about DC +5 V output to the equipment. The output voltage is adjusted by the variable resistor (VR101)


Fig. 8

## [1] Control PWB circuit

## 1. Main Control Block (1a/10)




## 3. Memory Block (2/10)







## 8. LAN Control Block (7/10)


9. Scanner I/F Block (8/10)

10. Sensor/Power Supply Block (9/10)




[2] LIU PWB circuit

1. LIU PWB Circuit (1/3)


## 2. LIU PWB Circuit (2/3)


3. LIU PWB Circuit (3/3)




1. Power Supply PWB circuit



## 3. Power Supply PWB parts layout (Bottom side)



## 1. Operation Panel PWB circuit (1/2)



## 2. Operation Panel PWB circuit (2/2)





| [FFa |
| :---: |
| Sn-AG-Cu | The OPERATION PANEL PWB of the model employs lead-free solder.

Note: Since the parts of PWB cannot be supplied, change it as a unit.

UX-B800A
CHAPTER 7. OTHER
[1] Protocol


## [2] Power on sequence



UX-B800A
[3] Service tools

## 1. List

| NO. | PARTS CODE | DESCRIPTION | REF NO. | QTY | PRICE <br> RANK |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | DUNT-355DSC01 | EXTENSION CONTROL PWB UNIT AND EXTENSION CABLE | - | 1 | CD |

### 1.1. Extension board unit



| NO. | PARTS CODE | DESCRIPTION | REF NO. | QTY | PRICE <br> RANK |
| :---: | :--- | :--- | :--- | :---: | :---: |
| 1 | QCNWG239DSCZZ | ENCODER EXTENSION FCC CABLE (4PIN) | - | 1 | AQ |
| 2 | QCNWG240DSCZZ | FEED MOTOR EXTENSION CABLE (2PIN) | - | 1 | AS |
| 3 | QCNWG241DSCZZ | POWER SUPPLY EXTENSION CABLE (8PIN) | - | 1 |  |
| 4 | QCNWG242DSCZZ | DECT EXTENSION CABLE (9PIN) | - | BA |  |
| 5 | QCNWG243DSCZZ | PANEL EXTENSION FCC CABLE (14PIN) | - | 1 |  |
| 6 | QCNWG244DSCZZ | TX MOTOR EXTENSION CABLE (4PIN) | - | 1 | AQ |
| 7 | QCNWG245DSCZZ | CIS EXTENSION CABLE (7PIN) | - | 1 | AW |
| 8 | QCNWG247DSCZZ | SPEAKER EXTENSION CABLE (2PIN) | - | 1 | AZ |
| 9 | QCNWG248DSCZZ | PRINTER1 EXTENSION FCC CABLE (14PIN) | - | 1 | AS |
| 10 | QCNWG249DSCZZ | CARRIER MOTOR EXTENSION CABLE (2PIN) | - | 1 | AT |
| 11 | QCNWG257DSCZZ | PIN EXTENSION CABLE (3PIN) | - | 1 | AS |
| 12 | QCNWG258DSCZZ | PRINTER2 EXTENSION FCC CABLE (14PIN) | - | 1 | AX |
| 13 | QCNWG321DSCZZ | LIU EXTENSION CABLE (15PIN) | - | 1 | AT |
| 14 | QCNCM705GAF04 | CONNECTOR (7PIN) | CNCIS | 1 | BD |
| 15 | QCNCM7014SC0B | CONNECTOR (2PIN) | CNCRMT | 2 | AD |
| 16 | QCNCW2556SC0D | CONNECTOR (4PIN) | CNENC | 2 | AD |
| 17 | QCNCM7014SC0B | CONNECTOR (2PIN) | CNFDMT | 2 | AD |
| 18 | QCNCM705RAF04 | CONNECTOR (16PIN) | CNLIU | 2 | AE |
| 19 | QCNCW2556SC1I | CONNECTOR (19PIN) | CNPN | 2 | AG |
| 20 | QCNCW2556SC1D | CONNECTOR (14PIN) | CNPRT1/2 | 4 | AH |
| 21 | QCNCM705HAF04 | CONNECTOR (8PIN) | CNPW | 2 | AD |
| 22 | QCNCM7014SC1C | CONNECTOR (13PIN) | CNDECT | 2 | AC |


| NO. | PARTS CODE | DESCRIPTION | REF NO. | QTY | PRICE <br> RANK |
| :---: | :--- | :--- | :--- | :---: | :---: |
| 23 | QCNCM705DAF04 | CONNECTOR(4PIN) | CNSCANMT | 2 | AC |
| 24 | QCNCM705BAF04 | CONNECTOR (2PIN) | CNSP | 2 | AB |
| 25 | QCNCM705CAF04 | CONNECTOR (3PIN) | CNPIN | 1 | AC |
| 26 | VHPGP1S094HCZ | PHOTO TRANSISTOR | PH900 | 1 | AG |
| 27 | VRS-TV2AB221J | RESISTOR $(1 / 10 W 220 \Omega \pm 5 \%)$ | R905 | 1 | AA |

## 2. Relay board unit

1. Remove the Control PWB from this unit, and mount the board unit instead.

Before connecting the wiring to the relay board unit, set the test PWB switches to the fixed position.
2. The setting is as follows.


PIN EXTENSION CABLE (QCNWG257DSCZZ)
TO EXTENSION PWB
(EXT_PINCN)


TO CHECK CONTROL PWB
(PIN)

## UX-B800A

## 3. Shading paper

- The white and black basis is applied to remember the shading waveform. Be sure to perform this operation when replacing the battery or replacing the control PWB. Execute in the shading mode of DIAG mode.

SHADING WAVE MEMORY STANDARD PAPER (PSHEZ3579SCZZ)

## [4] Rewriting version up the FLASH ROM

## 1. Preparations

Before updating the firmware, please make sure that you can browse the embedded Web Page by input the device IP address into the URL box of the browser.

The example of connection is below:


## 2. Operations

1. Press Function key, $*$ key, 8 key, 7 key and \# key. "FIRMWARE VERSION UP" is displayed on the upper LCD.

Then press 2 key. "FIRMWARE UPDATE/PRESS START KEY" is displayed on the LCD.
Then press START key. "FIRMWARE UPDATE/WATING FOR DATA" is displayed on the LCD.
2. Open Web browser and enter http://IP address of the machine/maint.htm in the edit box for URL and press Enter key.

Then the below page will be shown.
(Sampling: UX-B800DE)

```
$1 UX-B800 [Maintenance] - Microsoft Internet Explorer - (BrowseUI UNI)
Eile Edit Yiew Favorites Iools Help
```




```
Address e] http://192.168.1.13/maint.htm
    - MAINTENANCE
    UX-B800
```

Firmware Information
Firmware Update
Go to Main Page

## UX-B800A

3. Click Firmware Update in the menu pane. Then the below page will be shown. If it says that the machine is not ready, please retry from step 1.

4. Click Browse button, choice the new firmware file and click Open button. Then the below page will b shown.

5. Click Update button. Then "DO NOT POWER DOWN!/UPDATING" is displayed on the LCD. A few minutes later, the below page will be shown.

6. A few minutes later, the device will restart automatically.
7. After the machine restarts, the below page will be shown.


UX-B800A

- MEMO -

7-9

## SHARP PARTS GUIDE



## FACSIMILE model UX-B800A

| MODEL | SELECTION CODE | DESTINATION |
| :---: | :---: | :---: |
| UX-B800 | A | Australia |

[1] Cabinet,etc.
[2] Operation panel unit/Document guide upper
[3] Paper hopper unit
[4] Packing material \& Accessories
[5] Control PWB unit
[6] LIU PWB unit
[7] Power supply PWB unit
[8] Operation panel PWB unit

- INDEX

Parts marked with " $\AA$ " are important for maintaining the safety of the set. Be sure to replace these parts with specified ones for maintaining the safety and performance of the set.
[1] Cabinet,etc.


| NO. | PARTS CODE | PRICE <br> RANK | MEW | PART <br> RANK | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |

## [1] Cabinet,etc.



UX-B800A

| NO. | PARTS CODE | PRICE | NEW | PART | RANK |
| :--- | :---: | :---: | :---: | :---: | :---: |
| MARK | RANK | DESCRIPTION |  |  |  |

[1] Cabinet,etc.
W1
[2] Operation panel unit/Document guide upper

$\begin{array}{|c|c|c|c|c|c|}\hline \text { NO. } & \text { PARTS CODE } & \text { PRICE } & \text { NEW } & \text { PART } & \text { RANK }\end{array}$ MARK $\left.\begin{array}{l}\text { RANK }\end{array}\right] \quad$ DESCRIPTION
[2] Operation panel unit/Document guide upper

| 1 | GCASP2197XHSA | BA |  | D | Panel case |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 2 | GCASP2198XHSA | BA |  | D | Sub panel case |
| 3 | JBTN-2537XHSA | AS |  | C | 12 key |
| 4 | JBTN-2538XHSA | AS |  | C | Start key |
| 5 | JBTN-2539XHSA | AS |  | C | Copy/Stop key |
| 6 | JBTN-2541XHSA | AS |  | C | Cursol key |
| 7 | JBTN-2542XHSC | AS |  | C | E-mail key |
| 8 | JBTN-2544XHSA | AS |  | C | Qwerty key |
| 9 | DCEKP343DXH01 | AU |  | E | Operation panel PWB unit |
| 10 | QCNTM2054XHZZ | AP |  | C | 12 key rubber |
| 11 | QCNTM2055XHZZ | AP |  | C | Function key rubber |
| 12 | QCNTM2056XHZZ | AT |  | C | Qwerty key rubber |
| 13 | VVLLMG2025TPR | BA |  | B | LCD |
| 14 | MSPRC3630XHZZ | AE |  | C | Pinion gear spring |
| 15 | NGERP2318XHZZ | AD |  | C | Pinion gear |
| 16 | PGiDM2724XHSA | AT |  | C | Hopper guide,left |
| 17 | PGiDM2725XHSA | AT |  | C | Hopper guide,right |
| 18 | LPLTG3429XHZZ | AF |  | C | Separate rubber |
| 19 | LPLTP3257XHZL | AE |  | C | Sub feed plate,left |
| 20 | LPLTP3257XHZR | AE |  | C | Sub feed plate,right |
| 21 | LPLTP3430XHZZ | AW |  | C | Separation plate |
| 22 | MARMP 2034 XHZA | AF |  | C | Paper feed plate |
| 23 | MLEVP2426XHZZ | AH |  | C | Front sensor lever |
| 24 | MLEVP2427XHZZ | AK |  | C | Original sensor lever |
| 25 | MSPRD3398XHZZ | AE |  | C | Separate spring |
| 26 | MSPRD3399XHZZ | AE |  | C | Paper feed spring |
| 27 | MSPRD3656XHZZ | AE |  | C | Sub feed spring,left |
| 28 | MSPRD3657XHZZ | AE |  | C | Sub feed spring, right |
| 29 | MSPRD3495XHZZ | AE |  | C | PO pinch roller spring |
| 30 | MSPRD3632XHZZ | AE |  | C | Front sensor lever spring |
| 31 | MSPRD3633XHZZ | AE |  | C | Original sensor lever spring |
| 32 | NROLLP2332XHZA | AL |  | C | Pinch roller |
| 33 | PGiDM2726XHZZ | AT |  | C | Document guide upper |
| 34 | PSHEF3928XHZZ | AN |  | C | Static brush sheet |
| 35 | PSHEP3939XHZZ | AG |  | C | Separation sheet |
| 36 | QCNWN499CXHZZ | AP |  | C | Panel cable |
| 37 | HPNLH2458XHSD | AP |  | D | Decoration panel |
| 38 | PCUSU2238XHZZ | AE |  | C | Separate cushion |
| B1 | XEBS726P08000 | AE |  | C | Screw(2.6x8) |
| B2 | LX-BZ2222XHZ7 | AD |  | C | Screw(3x10) |
| B3 | XEBS726P10000 | AC |  | C | Screw(2.6x10) |
|  | (Unit) |  |  |  |  |
| 901 | DCEKP341DXH07 | BQ | N | E | Operation panel unit |

## [3] Paper hopper unit



| NO. | PARTS CODE | $\begin{aligned} & \hline \text { PRICE } \\ & \text { RANK } \end{aligned}$ | NEW MARK | PART RANK | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
| [3] Paper hopper unit |  |  |  |  |  |
| 1 | LPLTM2924XHFW | AQ |  | C | Rotation plate |
| 2 | LPLTP3263XHSJ | AP |  | C | RP release plate |
| 3 | MSPRC3407XHZZ | AE |  | C | Coil spring |
| 4 | MSPRT2932XHFJ | AC |  | C | RP release spring |
| 5 | NGERH2365AXZZ | AD |  | C | RP release gear,left |
| 6 | NGERH2366AXZZ | AD |  | C | RP release gear, right |
| 7 | PHOP-2095XHVD | AU |  | C | Paper hopper |
| 8 | PSEL-2015XHZZ | AB |  | C | Paper pad |
| 9 | PGiDM2493XHVB | AL |  | C | A4 paper guide |
| B1 | LX-BZ2222XHZ7 | AD |  | C | Screw( $3 \times 10$ ) |
| B2 | XEBS730P10000 | AC |  | C | Screw( $3 \times 10$ ) |



| NO. | PARTS CODE | $\begin{aligned} & \hline \text { PRICE } \\ & \text { RANK } \end{aligned}$ | NEW MARK | $\begin{aligned} & \hline \text { PART } \\ & \text { RANK } \end{aligned}$ | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
| [4] Packing material \& Accessories |  |  |  |  |  |
| 1 | T iNSE4562XHTZ | AM | N | D | Operation manual |
| 2 | TCADH3939XHZZ | AF | N | D | Setup guide |
| 6 | TLABH415KXHSA | AK |  | D | Rapid key labels |
| 7 | CPLTP3434XH01 | BB |  | C | Document exit tray/paper tray ass'y |
| 9 | LPLTP3442XHSA | AQ |  | C | Received document tray |
| 10 | SPAKA275HXHZZ | AQ |  | D | Pad A |
| 11 | CPAKC439HXH01 | BA | N | D | Packing case with label |
| 12 | DUNTK245DXHPW | AX |  | E | Handset |
| 13 | QCNWG202DXHFW | AP |  | C | Handset cord |
| 14 | QCNWG0376AFZZ | AM |  | C | Telephone line cord |
| 15 | SPAKA441HXHZZ | AG | N | D | Packing add., bottom |
| 16 | SPAKA442HXHZZ | AQ | N | D | Packing add.,top |
| 17 | SPAKA276HXHZZ | AQ |  | D | Carriage spacer |
| 18 | SPAKP274HXHZZ | AQ |  | D | Vinyl cover |
| 20 | U i NK-2046XHZZ | BN |  | A | Ink cartridge (Initial cartridge) |
| 21 | LPLTP3432XHSA | AW |  | C | Exit paper tray |
| 22 | LPLTP3433XHSA | AW |  | C | Extension exit paper tray |
| 24 | SPAKA364HXHZZ | AF |  | D | Ink cartridge protection |
| 25 | SSAKA2008XHZZ | AA |  | D | Polyethylene bag |
| 26 | SPAKP420DXHZZ | AF |  | D | Polyethylene bag |
| 27 | SSAKA3001CCZZ | AA |  | D | Polyethylene bag |
| 28 | QPLGZ9065AFZZ | AP |  | C | Adapter |


| NO. | PARTS CODE | PRICE RANK | NEW MARK | PART RANK | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |

## [5] Control PWB unit

| 1 | UBATL2232XHZZ | AF | B | Battery(CR2032T23) | [BAT1] |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 2 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4) }}$ | [BR100] |
| 3 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4 4) }}$ | [BR101] |
| 4 | VRS-CG1 JF101 J | AA | C | Block resistor(100 ${ }^{\text {4 }}$ ) | [BR102] |
| 5 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4) }}$ | [BR103] |
| 6 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4 4) }}$ | [BR104] |
| 7 | VRS-CG1 JF101 J | AA | C | Block resistor(100 ${ }^{\text {4 }}$ ) | [BR105] |
| 8 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4) }}$ | [BR106] |
| 9 | VRS-CG1 JF103J | AC | C | Block resistor( $10 \mathrm{~K} \Omega \times 4$ ) | [BR107] |
| 10 | VRS-CG1 JF271J | AD | C | Block resistor( $270 \Omega \times 4$ ) | [BR109] |
| 11 | VRS-CG1 JF271J | AD | C | Block resistor(270 $\times 4$ ) | [BR110] |
| 12 | VRS-CG1 JF330J | AC | C | Block resistor(33 $\times 4$ ) | [BR111] |
| 13 | VRS-CG1 JF330J | AC | C | Block resistor(33 $\times 4$ ) | [BR112] |
| 14 | VRS-CG1 JF330J | AC | C | Block resistor(33 $\times 4$ ) | [BR113] |
| 15 | VRS-CG1 JF271 J | AD | C | Block resistor(270 $\times 4$ ) | [BR114] |
| 16 | VRS-CG1 JF271J | AD | C | Block resistor(270 $\times 4$ ) | [BR115] |
| 17 | VRS-CG1JF101J | AA | C | Block resistor(100 ${ }^{\text {4) }}$ | [BR116] |
| 18 | VRS-CG1 JF330J | AC | C | Block resistor(33 $\times 4$ ) | [BR117] |
| 19 | VRS-CG1 JF330J | AC | C | Block resistor(33 $\times 4$ ) | [BR118] |
| 20 | VRS-CG1 JF330J | AC | C | Block resistor(33 $\times 4$ ) | [BR119] |
| 21 | VRS-CG1 JF221J | AC | C | Block resistor(220 $\times 4$ ) | [BR120] |
| 22 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4) }}$ | [BR121] |
| 23 | VRS-CG1JF101J | AA | C | Block resistor(100 ${ }^{\text {4 4) }}$ | [BR122] |
| 24 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4 4) }}$ | [BR123] |
| 25 | VRS-CG1 JF330J | AC | C | Block resistor(33 $\times 4$ ) | [BR124] |
| 26 | VRS-CG1 JF271J | AD | C | Block resistor(270 $\times 4$ ) | [BR125] |
| 27 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4 4) }}$ | [BR126] |
| 28 | VRS-CG1 JF 101 J | AA | C | Block resistor(100 ${ }^{\text {4 }}$ ) | [BR128] |
| 29 | VRS-CG1 JF 101 J | AA | C | Block resistor(100 ${ }^{\text {4) }}$ | [BR129] |
| 30 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4 4) }}$ | [BR130] |
| 31 | VRS-CG1 JF101J | AA | C | Block resistor(100 ${ }^{\text {4) }}$ | [BR132] |
| 32 | VRS-CG1 JF221J | AC | C | Block resistor(220 $\times 4$ ) | [BR300] |
| 33 | VRS-CG1 JF221J | AC | C | Block resistor(220 $\times 4$ ) | [BR704] |
| 34 | VRS-CG1 JF221J | AC | C | Block resistor(220 $\times 4$ ) | [BR708] |
| 35 | VRS-CG1 JF221J | AC | C | Block resistor(220 $\times 4$ ) | [BR709] |
| 36 | VRS-CG1 JF 103 J | AC | C | Block resistor(10K $\Omega$ x) | [BR713] |
| 37 | VRS-CG1 JF000J | AB | C | Block resistor(0 $\times 4$ ) | [BR715] |
| 38 | VRS-CG1 JF000J | AB | C | Block resistor(0 $\times 4$ ) | [BR716] |
| 39 | VRS-CG1 JF 103 J | AC | C | Block resistor( $10 \mathrm{~K} \Omega \times 4$ ) | [BR800] |
| 40 | VRS-CG1 JF330J | AC | C | Block resistor(33 $\times 4$ ) | [BRA00] |
| 41 | VRS-CG1 JF330J | AC | C | Block resistor(33@ $\times 4$ ) | BRA01] |
| 42 | VRS-CG1 JF333J | AC | C | Block resistor(33K ${ }^{\text {a }}$ 4) | [BRA02] |
| 43 | VRS-CG1 JF333 J | AC | C | Block resistor(33K $\Omega$ 4) | BRA03] |
| 44 | VRS-CG1 JF000J | AB | C | Block resistor(0 $\times 4$ ) | BRA04] |
| 45 | VRS-CG1 JF000J | AB | C | Block resistor(0 $\times 4$ ) | BRA05] |
| 46 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C100] |
| 47 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C101] |
| 48 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C102] |
| 49 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C103] |
| 50 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C104] |
| 51 | VCCCCZ1EH7R0D | AA | C | Capacitor(25WV 7PF) | [C105] |
| 52 | VCCCCZ1EH7R0D | AA | C | Capacitor(25WV 7PF) | [C106] |
| 53 | VCKYTV1AF106Z | AC | C | Capacitor(10WV 10 $\mu \mathrm{F}$ ) | [C107] |
| 54 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 1 F) | [C108] |
| 55 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C109] |
| 56 | VCKYTV1AF106Z | AC | C | Capacitor(10WV 10 $\mu \mathrm{F}$ ) | [C110] |
| 57 | VCKYCY1AF105Z | AC | C | Capacitor(10WV 1 $\mu$ F) | [C112] |
| 58 | VCCCCZ1EH270J | AA | C | Capacitor(25WV 27PF) | [C114] |
| 59 | VCCCCZ1EH270J | AA | C | Capacitor(25WV 27PF) | [C115] |
| 60 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 1 F) | [C119] |
| 61 | VCKYCZ1AB104K | AC | C | Capacitor(10WV 0.1 $\mu \mathrm{F}$ ) | [C121] |
| 62 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C122] |
| 63 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C125] |
| 64 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C126] |
| 65 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C134] |
| 66 | VCCCCZ1EH101J | AA | C | Capacitor(25WV 100PF) | [C160] |
| 67 | VCCCCZ1EH101J | AA | C | Capacitor(25WV 100PF) | [C161] |
| 68 | VCCCCZ1EH101J | AA | C | Capacitor(25WV 100PF) | [C162] |
| 69 | VCCCCZ1EH101J | AA | C | Capacitor(25WV 100PF) | [C163] |
| 70 | VCKYCZ1EB102K | AA | C | Capacitor(25WV 1000PF) | [C165] |
| 71 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C201] |
| 72 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [C203] |
| 73 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C212] |
| 74 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 1 F) | [C213] |
| 75 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C214] |
| 76 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C215] |
| 77 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C216] |
| 78 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C218] |
| 79 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C219] |
| 80 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C227] |
| 81 | VCKYCZ1CF104Z | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C301] |
| 82 | VCKYCZ1EB102K | AA | C | Capacitor(25WV 1000PF) | [C302] |
| 83 | VCKYCY1HF104Z | AA | C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C304] |
| 84 | VCCCCZ1EH221J | AB | C | Capacitor(25WV 220PF) | [C305] |
| 85 | VCCCCZ1EH221J | AB | C | Capacitor(25WV 220PF) | [C306] |


| NO. | PARTS CODE | PRICE <br> RANK | NEW <br> MARK | PAR <br> RAN |
| :---: | :---: | :---: | :---: | :---: |
| [5] Control PWB unit |  |  |  |  |

## [5] Control PWB unit

| 86 | VCCCCZ1EH221J |
| :---: | :---: |
| 87 | VCKYCZ1AB104K |
| 88 | VCKYCZ1AB104K |
| 89 | VCKYCZ1CF104Z |
| 90 | VCKYCZ1CF104Z |
| 91 | VCKYCZ1EB102K |
| 92 | VCKYCZ1CF104Z |
| 93 | VCKYTV1AF106Z |
| 94 | VCKYCZ1CF104Z |
| 95 | VCKYTV1AF106Z |
| 96 | VCKYCZ1CF104Z |
| 97 | VCKYCZ1CF104Z |
| 98 | VCKYCZ1EB102K |
| 99 | VCKYCZ1CF104Z |
| 100 | VCKYCZ1CF104Z |
| 101 | VCKYCZ1CF104Z |
| 102 | VCKYTV1AF106Z |
| 103 | VCKYCZ1CF104Z |
| 104 | VRS-CZ1JB000J |
| 105 | VCKYCZ1EB102K |
| 106 | VCKYCY1CB104K |
| 107 | VCCCCZ1EH221J |
| 108 | VCCCCZ1EH221J |
| 109 | VCCCCZ1EH221J |
| 110 | VCCCCZ1EH221J |
| 111 | VCKYCY1AB105K |
| 112 | VRS-CZ1JB202J |
| 113 | VRS-CY1 JB000J |
| 114 | VCKYCZ1CF104Z |
| 115 | VCCCCZ1EH221J |
| 116 | VCKYCY1AB105K |
| 117 | VCKYCZ1AB104K |
| 118 | VCKYCZ1AB104K |
| 119 | VCCCCZ1EH221J |
| 120 | VRS-CZ1JB000J |
| 121 | VRS-CY1JB000J |
| 122 | VCCCCZ1EH221J |
| 123 | VCCCCZ1EH221J |
| 124 | VCKYCZ1EB472K |
| 125 | VCKYCZ1EB472K |
| 126 | VCCCCZ1EH221J |
| 127 | VCCCCZ1EH221J |
| 128 | VCKYCY1AB105K |
| 129 | VCKYCZ1CF104Z |
| 130 | VCCCCZ1EH221J |
| 131 | VCKYTV1AF106Z |
| 132 | VCCCCZ1EH221J |
| 133 | VRS-CZ1JB000J |
| 134 | VCCCCZ1EH101J |
| 135 | VCEAGA1HW226M |
| 136 | VCKYCZ1EB102K |
| 137 | VCKYCZ1CF104Z |
| 138 | VCKYCZ1EB222K |
| 139 | VCKYCY1HF104Z |
| 140 | VCKYCY1AB105K |
| 141 | VCKYCY1AB105K |
| 142 | VRS-CZ1JB000J |
| 143 | VCKYCZ1EB222K |
| 144 | VCKYCY1HF104Z |
| 145 | VCEAGA1HW226M |
| 146 | VCEAZA1HW227M |
| 147 | VCKYCY1HF104Z |
| 148 | VCKYCY1HF104Z |
| 149 | VCKYCY1HF104Z |
| 150 | VCKYCY1HF104Z |
| 151 | VCKYCY1HF104Z |
| 152 | VCKYCY1HF104Z |
| 153 | VCKYCZ1CF104Z |
| 154 | VCKYCZ1CF104Z |
| 155 | VCKYCZ0JB105K |
| 156 | VCKYCZ1CB103K |
| 157 | VCKYCZ1CB103K |
| 158 | VCKYCZ1CB103K |
| 159 | VCKYCZ1AB104K |
| 160 | VCKYCZ1CB683K |
| 161 | VCKYCY1HF104Z |
| 162 | VCEAZA1HW227M |
| 163 | VCKYCY1HF104Z |
| 164 | VCKYCZ1EB102K |
| 165 | VCKYCZ1EB102K |
| 166 | VCKYCZ1CF104Z |
| 167 | VCKYCZ1CF104Z |
| 168 | VCKYCZ1CF104Z |
| 169 | VCKYCZ1CF104Z |
| 170 | VCKYCZ1CF104Z |



| C | Capacitor(25WV 220PF) | [C307] |
| :---: | :---: | :---: |
| C | Capacitor(10WV 0.1 $\mu \mathrm{F}$ ) | [C308] |
| C | Capacitor(10WV 0.1 $\mu \mathrm{F}$ ) | [C309] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C310] |
| C | Capacitor (16WV 0.1 $\mu \mathrm{F}$ ) | [C311] |
| C | Capacitor(25WV 1000PF) | [C312] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C313] |
| C | Capacitor(10WV 10 $\mu \mathrm{F}$ ) | [C314] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C315] |
| C | Capacitor(10WV 10 $\mu \mathrm{F}$ ) | [C316] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C317] |
| C | Capacitor (16WV 0.1 $\mu \mathrm{F}$ ) | [C318] |
| C | Capacitor(25WV 1000PF) | [C319] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C320] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C321] |
| C | Capacitor (16WV 0.1 $\mu \mathrm{F}$ ) | [C322] |
| C | Capacitor(10WV 10 $\mu \mathrm{F}$ ) | [C323] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C324] |
| C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [C334] |
| C | Capacitor(25WV 1000PF) | [C354] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C403] |
| C | Capacitor(25WV 220PF) | [C404] |
| C | Capacitor(25WV 220PF) | [C405] |
| C | Capacitor(25WV 220PF) | [C406] |
| C | Capacitor(25WV 220PF) | [C409] |
| C | Capacitor(10WV 1 $\mu \mathrm{F}$ ) | [C413] |
| C | Resistor(1/16W $2 \mathrm{~K} \Omega \pm 5 \%$ ) | [C414] |
| C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [C415] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C416] |
| C | Capacitor(25WV 220PF) | [C417] |
| C | Capacitor(10WV 1 $\mu \mathrm{F}$ ) | [C422] |
| C | Capacitor(10WV 0.1 $\mu \mathrm{F}$ ) | [C423] |
| C | Capacitor(10WV 0.1 $\mu \mathrm{F}$ ) | [C426] |
| C | Capacitor(25WV 220PF) | [C431] |
| C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [C432] |
| C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [C435] |
| C | Capacitor(25WV 220PF) | [C436] |
| C | Capacitor(25WV 220PF) | [C437] |
| C | Capacitor(25WV 4700PF) | [C438] |
| C | Capacitor(25WV 4700PF) | [C439] |
| C | Capacitor(25WV 220PF) | [C441] |
| C | Capacitor(25WV 220PF) | [C442] |
| C | Capacitor(10WV $1 \mu \mathrm{~F}$ ) | [C443] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C444] |
| C | Capacitor(25WV 220PF) | [C445] |
| C | Capacitor(10WV 10 $\mu$ F) | [C447] |
| C | Capacitor(25WV 220PF) | [C448] |
| C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [C449] |
| C | Capacitor(25WV 100PF) | [C450] |
| C | Capacitor(50WV 22 $\mu$ F) | [C453] |
| C | Capacitor(25WV 1000PF) | [C462] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C463] |
| C | Capacitor(25WV 2200PF) | [C464] |
| C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C467] |
| C | Capacitor(10WV 1 $\mu \mathrm{F}$ ) | [C504] |
| C | Capacitor(10WV 1 $\mu \mathrm{F}$ ) | [C505] |
| C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [C514] |
| C | Capacitor(25WV 2200PF) | [C515] |
| C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C601] |
| C | Capacitor(50WV 220 $\mu$ F) | [C602] |
| C | Capacitor(50WV 220 $\mu \mathrm{F}$ ) | [C603] |
| C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C604] |
| C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C606] |
| C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C607] |
| C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C608] |
| C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C609] |
| C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C610] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C611] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C612] |
| C | Capacitor(6.3WV $1 \mu \mathrm{~F}$ ) | [C614] |
| C | Capacitor(16WV 0.01 $\mu \mathrm{F}$ ) | [C620] |
| C | Capacitor(16WV 0.01 $\mu \mathrm{F}$ ) | [C621] |
| C | Capacitor(16WV 0.01 $\mu \mathrm{F}$ ) | [C622] |
| C | Capacitor(10WV 0.1 $\mu \mathrm{F}$ ) | [C623] |
| C | Capacitor(16WV 0.068 $\mu \mathrm{F}$ ) | [C624] |
| C | Capacitor(50WV 0.1 1 F) | [C625] |
| C | Capacitor(50WV $22 \mu \mathrm{~F}$ ) | [C626] |
| C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C627] |
| C | Capacitor(25WV 1000PF) | [C702] |
| C | Capacitor(25WV 1000PF) | [C703] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C704] |
| C | Capacitor (16WV 0.1 $\mu \mathrm{F}$ ) | [C705] |
| C | Capacitor (16WV 0.1 $\mu \mathrm{F}$ ) | [C706] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C709] |
| C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C710] |


| NO. | PARTS CODE | PRICE <br> RANK | NEW <br> MARK | PART <br> RANK | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
| [5] Control PWB unit |  |  |  |  |  |



| NO. | PARTS CODE | PRICE RANK | $\begin{aligned} & \hline \text { NEW } \\ & \text { MARK } \end{aligned}$ | PART RANK | DESCRIPTION |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| [5] Control PWB unit |  |  |  |  |  |  |
| 256 | VRS-CY1 JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [L601] |
| 257 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [L801] |
| 258 | VRS-CY1 JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [L901] |
| 259 | VRS-TV2AB000J | AA |  | C | Resistor(1/10W $0 \Omega \pm 5 \%$ ) | [LA01] |
| 260 | VRS-TV2AB000J | AA |  | C | Resistor(1/10W $0 \Omega \pm 5 \%$ ) | [LA02] |
| 261 | VHPGP1S094HCZ | AG |  | B | Photo transistor(GP1S094HCZ) | [PH900] |
| 262 | VS2SA2059++-1 | AH |  | B | Transistor(2SA2059) | [Q101] |
| 263 | VSRN1402F++-1 | AF |  | B | Transistor(RN1402) | [Q200] |
| 264 | VSRN1402F++-1 | AF |  | B | Transistor(RN1402) | [Q401] |
| 265 | VSRN1406F++-1 | AF |  | B | Transistor(RN1406) | [Q402] |
| 266 | VSRN $1402 \mathrm{~F}++$ - 1 | AF |  | B | Transistor(RN1402) | [Q403] |
| 267 | VSRN1406F + +-1 | AF |  | B | Transistor(RN1406) | [Q405] |
| 268 | VS2SC2412K/-1 | AB |  | B | Transistor(2SC2412K) | [Q802] |
| 269 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R103] |
| 270 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R104] |
| 271 | VRS-CZ1 JB225J | AA |  | C | Resistor(1/16W 2.2M $2 \pm 5 \%$ ) | [R105] |
| 272 | VRS-CZ1 JB104J | AA |  | C | Resistor(1/16W 100K $2 \pm 5 \%$ ) | [R107] |
| 273 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R110] |
| 274 | VRS-TV2AB000J | AA |  | C | Resistor(1/10W $0 \Omega \pm 5 \%$ ) | [R112] |
| 275 | VRS-TV2AB000J | AA |  | C | Resistor(1/10W $0 \Omega \pm 5 \%$ ) | [R113] |
| 276 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R114] |
| 277 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R115] |
| 278 | VRS-CZ1JB105J | AD |  | C | Resistor(1/16W 1M $2 \pm 5 \%$ ) | [R116] |
| 279 | VRS-CZ1 JB122J | AA |  | C | Resistor(1/16W 1.2K $\Omega \pm 5 \%$ ) | [R117] |
| 280 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R118] |
| 281 | VRS-CZ1 JB104J | AA |  | C | Resistor(1/16W 100K $2 \pm 5 \%$ ) | [R119] |
| 282 | VRS-CZ1JB221J | AD |  | C | Resistor(1/16W 200 $\pm$ 5\%) | [R121] |
| 283 | VRSCZ1JB4991F | AG |  | C | Resistor(1/16W 4.99K ( 1\%) | [R130] |
| 284 | VRS-CZ1JB101J | AA |  | C | Resistor(1/16W 100 $\pm 5 \%$ ) | [R131] |
| 285 | VRS-CZ1JB101J | AA |  | C | Resistor(1/16W 100 $\pm$ +5\%) | [R132] |
| 286 | VRS-CZ1JB101J | AA |  | C | Resistor(1/16W 100 $\times 5 \%$ ) | [R133] |
| 287 | VRS-CZ1JB330J | AA |  | C | Resistor(1/16W 33 $\pm \pm 5 \%$ ) | [R137] |
| 288 | VRS-CZ1JB104J | AA |  | C | Resistor(1/16W 100K $\Omega \pm 5 \%$ ) | [R147] |
| 289 | VRS-CZ1JB104J | AA |  | C | Resistor(1/16W 100K $\Omega \pm 5 \%$ ) | [R154] |
| 290 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R155] |
| 291 | VRS-CZ1JB271J | AA |  | C | Resistor(1/16W $270 \Omega \pm 5 \%)$ ) | [R156] |
| 292 | VRS-CZ1JB471J | AA |  | C | Resistor(1/16W 470 $\pm 5 \%$ ) | [R157] |
| 293 | VRS-CZ1JB101J | AA |  | C | Resistor(1/16W 100 $\pm$ 5\%) | [R173] |
| 294 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R177] |
| 295 | VRS-CZ1JB101J | AA |  | C | Resistor(1/16W 100 $\pm$ 5\%) | [R180] |
| 296 | VRS-CZ1JB104J | AA |  | C | Resistor(1/16W $100 \mathrm{~K} \Omega \pm 5 \%$ ) | [R189] |
| 297 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R191] |
| 298 | VRS-CZ1JB271J | AA |  | C | Resistor(1/16W 270 $\pm$ \% ${ }^{\text {\% }}$ ) | [R197] |
| 299 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R200] |
| 300 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R201] |
| 301 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R203] |
| 302 | VRS-CZ1JB472J | AA |  | C | Resistor(1/16W 4.7K $\Omega \pm 5 \%$ ) | [R205] |
| 303 | VRS-CZ1 JB103J | AA |  | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R206] |
| 304 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R208] |
| 305 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R219] |
| 306 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R222] |
| 307 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R224] |
| 308 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R226] |
| 309 | VRS-CZ1 JB133J | AC |  | C | Resistor(1/16W 13K $2 \pm 5 \%$ ) | [R301] |
| 310 | VRS-CZ1JB332J | AA |  | C | Resistor(1/16W $3.3 \mathrm{~K} \Omega \pm 5 \%$ ) | [R302] |
| 311 | VRS-CZ1 JB133J | AC |  | C | Resistor(1/16W 13K $2 \pm 5 \%$ ) | [R303] |
| 312 | VRS-CZ1 JB103J | AA |  | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R305] |
| 313 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R306] |
| 314 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R310] |
| 315 | VRS-CZ1JB103J | AA |  | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R312] |
| 316 | VRS-CZ1JB104J | AA |  | C | Resistor(1/16W $100 \mathrm{~K} \Omega \pm 5 \%$ ) | [R313] |
| 317 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R316] |
| 318 | VRS-CZ1JB393J | AD |  | C | Resistor(1/16W 39K $\Omega \pm 5 \%$ ) | [R401] |
| 319 | VRS-CZ1 JB103J | AA |  | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R402] |
| 320 | VRS-CZ1JB332J | AA |  | C | Resistor(1/16W 3.3K $2 \pm 5 \%$ ) | [R403] |
| 321 | VRS-CZ1JB202J | AD |  | C | Resistor(1/16W $2 \mathrm{~K} \Omega \pm 5 \%$ ) | [R406] |
| 322 | VRS-CZ1 JB473J | AA |  | C | Resistor(1/16W 47K . $\pm 5 \%$ ) | [R407] |
| 323 | VRS-CZ1JB472J | AA |  | C | Resistor(1/16W 4.7K $2 \pm 5 \%$ ) | [R410] |
| 324 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R411] |
| 325 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R415] |
| 326 | VRS-CZ1 JB203J | AD |  | C | Resistor(1/16W 20K $2 \pm 5 \%$ ) | [R416] |
| 327 | VRS-CZ1JB102J | AA |  | C | Resistor(1/16W 1K $2 \pm 5 \%$ ) | [R417] |
| 328 | VRS-CZ1JB183J | AD |  | C | Resistor(1/16W 18K $2 \pm 5 \%$ ) | [R418] |
| 329 | VRS-CZ1 JB164J | AD |  | C | Resistor(1/16W 160K $2 \pm 5 \%$ ) | [R419] |
| 330 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R428] |
| 331 | VRS-CZ1 JB104J | AA |  | C | Resistor(1/16W 100K $\Omega \pm 5 \%$ ) | [R429] |
| 332 | VRS-CZ1JB224J | AA |  | C | Resistor(1/16W 220K $2 \pm 5 \%$ ) | [R430] |
| 333 | VRS-CZ1JB332J | AA |  | C | Resistor(1/16W $3.3 \mathrm{~K} \Omega \pm 5 \%$ ) | [R431] |
| 334 | VRS-CZ1JB224J | AA |  | C | Resistor(1/16W $220 \mathrm{~K} \Omega \pm 5 \%$ ) | [R432] |
| 335 | VRS-CZ1JB102J | AA |  | C | Resistor(1/16W 1K $2 \pm 5 \%$ ) | [R433] |
| 336 | VRS-CZ1 JB823J | AD |  | C | Resistor(1/16W 82K $2 \pm 5 \%$ ) | [R434] |
| 337 | VRS-CZ1 JB472 J | AA |  | C | Resistor(1/16W 4.7K $\Omega \pm 5 \%$ ) | [R435] |
| 338 | VRS-CZ1JB912F | AG |  | C | Resistor(1/16W 9.1K ${ }^{\text {( }}$ (1\%) | [R445] |
| 339 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R501] |
| 340 | VRS-CZ1JB332J | AA |  | C | Resistor(1/16W 3.3K $2 \pm 5 \%$ ) | [R515] |


| NO. | PARTS CODE | PRICE RANK | NEW MARK | PART RANK | DESCRIPTION |
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## [5] Control PWB unit

| 341 | VRS-CZ1 J 3 033 J | AD | C | Resistor(1/16W 30K $\Omega \pm 5 \%$ ) | [R516] |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 342 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R519] |
| 343 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R521] |
| 344 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R522] |
| 345 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R523] |
| 346 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R527] |
| 347 | VRS-CZ1JB620J | AA | C | Resistor(1/16W $62 \Omega \pm 5 \%$ ) | [R600] |
| 348 | VRS-CZ1 JB152J | AA | C | Resistor(1/16W 1.5K $\Omega \pm 5 \%$ ) | [R601] |
| 349 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R602] |
| 350 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R604] |
| 351 | VRS-CZ1JB562J | AA | C | Resistor(1/16W 5.6K $2 \pm 5 \%$ ) | [R605] |
| 352 | VRS-HT3AA1R3J | AE | C | Resistor(1W $1.3 \Omega \pm 5 \%$ ) | [R606] |
| 353 | VRS-HT3AA1R3J | AE | C | Resistor(1W 1.3 ${ }^{\text {a }}$ ( 5\%) | [R607] |
| 354 | VRS-CZ1JB152J | AA | C | Resistor(1/16W 1.5K $2 \pm 5 \%$ ) | [R608] |
| 355 | VRS-CZ1JB363F | AG | C | Resistor(1/16W $36 \mathrm{~K} \Omega \pm 1 \%$ ) | [R609] |
| 356 | VRSCZ1JB5490F | AG | C | Resistor(1/16W 549 ${ }^{\text {a }}$ (1\%) | [R610] |
| 357 | VRSCZ1JB3650F | AG | C | Resistor(1/16W 365 $\pm$ 1\%) | [R611] |
| 358 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R613] |
| 359 | VRS-CZ1JB124F | AD | C | Resistor(1/16W 120K $2 \pm 1 \%$ ) | [R614] |
| 360 | VRS-CZ1JB363F | AG | C | Resistor(1/16W $36 \mathrm{~K} \Omega \pm 1 \%$ ) | [R615] |
| 361 | VRS-CZ1JB124F | AD | C | Resistor(1/16W 120K $\Omega \pm 1 \%$ ) | [R616] |
| 362 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R618] |
| 363 | VRSCZ1JB5490F | AG | C | Resistor(1/16W 549 $\pm \pm 1 \%$ ) | [R619] |
| 364 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R623] |
| 365 | VRSCZ1JB6041F | AG | C | Resistor(1/16W 6.04K $2 \pm 1 \%$ ) | [R626] |
| 366 | VRS-CZ1JB104J | AA | C | Resistor(1/16W 100K $\Omega \pm 5 \%$ ) | [R627] |
| 367 | VRSCZ1JB7321F | AG | C | Resistor(1/16W 7.32K $2 \pm 1 \%$ ) | [R628] |
| 368 | VRS-CZ1JB222F | AG | C | Resistor(1/16W 2.2K $2 \pm 1 \%$ ) | [R629] |
| 369 | VRS-CY1 JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R631] |
| 370 | VRS-CY1 JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R633] |
| 371 | VRS-CZ1 JB103J | AA | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R634] |
| 372 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R635] |
| 373 | VRSCZ1JB4991F | AG | C | Resistor(1/16W 4.99K $\pm$ 1\%) | [R636] |
| 374 | VRSCZ1JB4991F | AG | C | Resistor(1/16W 4.99K $2 \pm 1 \%$ ) | [R637] |
| 375 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R638] |
| 376 | VRS-CZ1JB330J | AA | C | Resistor(1/16W $33 \Omega \pm 5 \%$ ) | [R700] |
| 377 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R701] |
| 378 | VRS-CZ1JB471J | AA | C | Resistor(1/16W 470 $\pm 5 \%$ ) | [R702] |
| 379 | VRS-TV2AB103J | AA | C | Resistor(1/10W 10K $2 \pm 5 \%$ ) | [R703] |
| 380 | VRS-TV2AB103J | AA | C | Resistor(1/10W 10K $\Omega \pm 5 \%$ ) | [R704] |
| 381 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R705] |
| 382 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R707] |
| 383 | VRS-CZ1JB221J | AD | C | Resistor(1/16W $200 \Omega \pm 5 \%$ ) | [R710] |
| 384 | VRS-TV2AB000J | AA | C | Resistor(1/10W $0 \Omega \pm 5 \%$ ) | [R711] |
| 385 | VRSCY1JB75R0F | AD | C | Resistor(1/16W 75S $\pm$ 1\%) | [R712] |
| 386 | VRSCY1JB75R0F | AD | C | Resistor(1/16W 75 $\times$ 1\%) | [R713] |
| 387 | RR-SZ3047SCZZ | AA | C | Resistor(24.9 $\pm 1 \%$ ) | [R716] |
| 388 | RR-SZ3047SCZZ | AA | C | Resistor(24.9 ${ }^{\text {a }}$ (1\%) | [R717] |
| 389 | RR-SZ3047SCZZ | AA | C | Resistor(24.9 ${ }^{\text {a }}$ (1\%) | [R718] |
| 390 | RR-SZ3047SCZZ | AA | C | Resistor(24.9 $\pm 1 \%$ ) | [R719] |
| 391 | RR-SZ3048SCZZ | AA | C | Resistor(49.9 $\pm$ 1\%) | [R720] |
| 392 | RR-SZ3048SCZZ | AA | C | Resistor( $49.9 \Omega \pm 1 \%$ ) | [R721] |
| 393 | VRS-CZ1 JB105 J | AD | C | Resistor(1/16W 1M $2 \pm 5 \%$ ) | [R722] |
| 394 | VRS-CZ1JB681J | AC | C | Resistor(1/16W $680 \Omega \pm 5 \%$ ) | [R723] |
| 395 | VRS-CZ1 JB470J | AA | C | Resistor(1/16W 47 $\Omega \pm 5 \%$ ) | [R724] |
| 396 | VRS-TV2AB113F | AA | C | Resistor(1/10W $11 \mathrm{~K} \Omega \pm 1 \%$ ) | [R725] |
| 397 | VRSCY1JB49R9F | AG | C | Resistor(1/16W 49.9 $\pm$ ¢ 1\%) | [R730] |
| 398 | VRSCY1JB49R9F | AG | C | Resistor(1/16W 49.9 ${ }^{\text {a }}$ (1\%) | [R731] |
| 399 | VRSCY1JB49R9F | AG | C | Resistor(1/16W 49.9 ${ }^{\text {a }}$ (1\%) | [R732] |
| 400 | VRSCY1JB49R9F | AG | C | Resistor(1/16W 49.9 $\pm$ 土 1\%) | [R733] |
| 401 | VRSCY1JB49R9F | AG | C | Resistor(1/16W 49.9 $\pm$ 1\%) | [R734] |
| 402 | VRSCY1JB49R9F | AG | C | Resistor(1/16W 49.9 $\pm$ ¢ 1\%) | [R735] |
| 403 | VRS-CZ1 JB103J | AA | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R738] |
| 404 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R739] |
| 405 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [R740] |
| 406 | VRS-CZ1 JB221J | AD | C | Resistor(1/16W $200 \Omega \pm 5 \%$ ) | [R741] |
| 407 | VRS-HT3DA221J | AB | C | Resistor(2W $220 \Omega \pm 5 \%$ ) | [R800] |
| 408 | VRS-CZ1JB102J | AA | C | Resistor(1/16W 1K | [R821] |
| 409 | VRS-CZ1JB103J | AA | C | Resistor(1/16W 10K $2 \pm 5 \%$ ) | [R822] |
| 410 | VRS-CY1 JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R825] |
| 411 | VRS-CY1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%)$ | [R826] |
| 412 | VRS-CZ1JB100J | AA | C | Resistor(1/16W 10 $\pm$ \% ${ }^{\text {\% }}$ ) | [R841] |
| 413 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R844] |
| 414 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R848] |
| 415 | VRS-CY1 JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R849] |
| 416 | VRS-CZ1JB562J | AA | C | Resistor(1/16W 5.6K $\Omega \pm 5 \%$ ) | [R900] |
| 417 | VRS-CZ1JB104J | AA | C | Resistor(1/16W 100K $2 \pm 5 \%$ ) | [R902] |
| 418 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R903] |
| 419 | VRS-TV2AB221J | AA | C | Resistor(1/10W $220 \Omega \pm 5 \%$ ) | [R905] |
| 420 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R911] |
| 421 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [R913] |
| 422 | VRS-HT3DA110J | AE | C | Resistor(2W 11 ${ }^{\text {a }}$ (5\%) | [R916] |
| 423 | VRS-CZ1JB271J | AA | C | Resistor(1/16W $270 \Omega \pm 5 \%$ ) | [RA00] |
| 424 | VRS-CZ1JB271J | AA | C | Resistor(1/16W $270 \Omega \pm 5 \%$ ) | [RA01] |
| 425 | VRS-CZ1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [RA07] |


| NO. | PARTS CODE | PRICE RANK | NEW MARK | PART RANK | DESCRIPTION |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| [5] Control PWB unit |  |  |  |  |  |  |
| 426 | VRS-CZ1 JB103J | AA |  | C | Resistor(1/16W 10K $\Omega \pm 5 \%$ ) | [RA08] |
| 427 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [RA12] |
| 428 | VRS-CZ1JB271J | AA |  | C | Resistor(1/16W $270 \Omega \pm 5 \%$ ) | [RA15] |
| 429 | VRS-CZ1 JB104J | AA |  | C | Resistor(1/16W 100K $2 \pm 5 \%$ ) | [RA16] |
| 430 | VRS-CZ1JB104J | AA |  | C | Resistor(1/16W 100K $2 \pm 5 \%$ ) | [RA17] |
| 431 | VRS-CZ1JB104J | AA |  | C | Resistor(1/16W $100 \mathrm{~K} \Omega \pm 5 \%$ ) | [RA18] |
| 432 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [RA19] |
| 433 | VRS-CZ1JB000J | AA |  | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [RA20] |
| 434 | VHiLD111733-1 | AN |  | B | IC(LD1117AL-33-TN3-A-R) | [REG900] |
| 435 | VHi BA178M24-1 | AL |  | B | IC(BA178M24FP-E2) | [REG901] |
| 436 | VHHTBPS $33 \mathrm{H}-1$ | AF |  | B | Thermistor(33K $\Omega$ ) | [THERM2] |
| 437 | RCRSP2236XHZZ | AK |  | B | Crystal(12MHz) | [X1] |
| 438 | RCRSA2233XHPZ | AG |  | B | Crystal(32.768kHz) | [X2] |
| 439 | RCRSP2237XHZZ | AK |  | B | Crystal(25MHz) | [X3] |
| 440 | VHDB340A+++-1 | AH |  | B | Diode(B340A) | [ZD600] |
| 441 | VHEPTZ12B++-1 | AG |  | B | Zener diode(PTZ12B) | [ZD601] |
|  | (Unit) |  |  |  |  |  |
| 901 | DCEKC481XXHS 1 | BW | N | E | Control PWB unit (within ROM) |  |
| [6] LIU PWB unit |  |  |  |  |  |  |


| 4 | 1 | VHVDSS301L/-U | AF | B | Varistor(DSS-301L) | [AR1] |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 4 | 2 | VHVRA501PC6-1 | AG | B | Varistor(RA501P-C6) | [AR2] |
| 4 | 3 | VHVRA501PC6-1 | AG | B | Varistor(RA501P-C6) | [AR3] |
|  | 4 | QCNWN205CXHZZ | AF | C | ARG earth cable | [ARG] |
|  | 5 | VCEAGA1HW106M | AA | C | Capacitor( $50 \mathrm{WV} 10 \mu \mathrm{~F}$ ) | [C1] |
|  | 6 | RC-FZ3079SCZZ | AG | C | Capacitor(250WV $1 \mu \mathrm{~F}$ ) | [C4] |
|  | 7 | RC-FZ3078SCZZ | AF | C | Capacitor(250WV 0.56 $\mu \mathrm{F}$ ) | [C5] |
|  | 8 | VCEAGA1HW226M | AB | C | Capacitor(50WV $22 \mu \mathrm{~F}$ ) | [C7] |
|  | 9 | VCEAGA1HW107M | AA | C | Capacitor(50WV 100 $\mu$ F) | [C8] |
|  | 10 | VCEAGA1CW476M | AB | C | Capacitor(16WV 47 $\mu$ F) | [C9] |
|  | 11 | VCEAGA1HW106M | AA | C | Capacitor( 50 WV 10 $\mu \mathrm{F}$ ) | [C10] |
|  | 12 | VCFYDA1HA334J | AC | C | Capacitor(50WV 0.33 F ) | [C13] |
|  | 13 | VCEAGA1HW107M | AA | C | Capacitor(50WV 100 $\mu$ F) | [C15] |
|  | 14 | VCEAEA1CW226M | AA | C | Capacitor(16WV 22 2 F ) | C16] |
|  | 15 | VCEAEA1EW475M | AA | C | Capacitor(25WV 4.7 $\mu \mathrm{F}$ ) | C17] |
|  | 16 | VCKYCY1HF104Z | AA | C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C101] |
|  | 17 | VCKYCY1HB563K | AC | C | Capacitor(50WV 0.056 $\mu \mathrm{F}$ ) | [C102] |
|  | 18 | VRS-CY1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [C105] |
|  | 19 | VCKYCY1CB104K | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C106] |
|  | 20 | VRS-CY1JB000J | AA | C | Resistor(1/16W $0 \Omega \pm 5 \%$ ) | [C107] |
|  | 21 | VCKYCY1HB183K | AB | C | Capacitor(50WV 0.018 $\mu \mathrm{F}$ ) | [C108] |
|  | 22 | VCKYCY1HB102K | AA | C | Capacitor(50WV 1000PF) | [C109] |
|  | 23 | VCKYCY1HB102K | AA | C | Capacitor(50WV 1000PF) | [C110] |
|  | 24 | VCKYCY1HF104Z | AA | C | Capacitor(50WV 0.1 $\mu \mathrm{F}$ ) | [C111] |
|  | 25 | VCKYCY1HB152K | AB | C | Capacitor(50WV 1500PF) | [C113] |
|  | 26 | VCCCCY1HH221J | AA | C | Capacitor(50WV 220PF) | [C114] |
|  | 27 | VCKYCY1HB102K | AA | C | Capacitor(50WV 1000PF) | [C115] |
|  | 28 | VCKYCY1CB104K | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C116] |
|  | 29 | VCKYCY1HB223K | AC | C | Capacitor( $50 \mathrm{WV} 0.022 \mu \mathrm{~F}$ ) | [C118] |
|  | 30 | VCKYCY1HB683K | AC | C | Capacitor(50WV 0.068 $\mu \mathrm{F}$ ) | [C119] |
|  | 31 | VCKYCY1CB104K | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C122] |
|  | 32 | VCCCCY1HH680J | AA | C | Capacitor(50WV 68PF) | [C123] |
|  | 33 | VCKYCY1HB102K | AA | C | Capacitor(50WV 1000PF) | [C124] |
|  | 34 | VCKYCY1HB821K | AA | C | Capacitor(50WV 820PF) | [C125] |
|  | 35 | VCKYCY1CB104K | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C126] |
|  | 36 | VCKYCY1HB103K | AA | C | Capacitor(50WV 0.01 $\mu \mathrm{F}$ ) | [C132] |
|  | 37 | VCKYCY1CB104K | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C134] |
|  | 38 | VCKYCY1HB222K | AA | C | Capacitor(50WV 2200PF) | [C135] |
|  | 39 | VCKYCY1HB222K | AA | C | Capacitor(50WV 2200PF) | [C136] |
|  | 40 | VCKYCY1CB104K | AB | C | Capacitor(16WV 0.1 $\mu \mathrm{F}$ ) | [C143] |
| 4 | 41 | RRLYD3434XHZZ | AP | B | Relay(OUAZ-SH-124DZ) | [CML1] |
|  | 42 | QJAKZ2079XH0D | AD | C | Jack | [CNHJ] |
|  | 43 | QCNCM2666XH1E | AF | C | Connector(15pin) | [CNLIU] |
|  | 44 | QJAKZ2087XH0D | AE | C | Jack | [CNLNJ] |
|  | 45 | QJAKZ2087XH0B | AD | C | Jack | [CNTLJ] |
|  | 46 | VHD1N4148//-1 | AA | B | Diode(1N4148) | [D1] |
|  | 47 | VHD1N4148//-1 | AA | B | Diode(1N4148) | [D2] |
|  | 48 | VHD1SS355//-1 | AB | B | Diode(1SS355) | [D101] |
|  | 49 | QCNWN205CXHZZ | AF | C | FG earth cable | [FG] |
|  | 50 | RH-iX2383XHZZ | AG | B | IC(LM2902NS) | [IC101] |
| 4 | 51 | RFiLN2027XHZZ | AC | C | Coil(R-5C) | [L1] |
| 4 | 52 | RFiLN2027XHZZ | AC | C | Coil(R-5C) | [L2] |
|  | 53 | RCiLF2125SCZZ | AF | C | Coil( 4.7 mH ) | [L5] |
| 令 | 54 | RFiLN2027XHZZ | AC | C | Coil(R-5C) | [L6] |
| 4 | 55 | RFiLN2027XHZZ | AC | C | Coil(R-5C) | [L7] |
|  | 56 | VHPPS2535-1// | AH | B | Photo transistor(PS2535-1) | [PC2] |
|  | 57 | VHPPS2561L-1/ | AG | B | Photo transistor(PS2561L-1) | [PC3] |
|  | 58 | VHPSG206S//-1 | AG | B | Photo transistor(SG206S) | [PH1] |
|  | 59 | VSBS108////-1 | AE | B | FET(BS108) | [Q1] |
|  | 60 | VSKTC3198GR-1 | AD | B | Transistor(KTC3198GR) | [Q2] |
|  | 61 | VSRN1406F++-1 | AF | B | Transistor(RN1406) | [Q101] |
|  | 62 | VSRN1406F++-1 | AF | B | Transistor(RN1406) | [Q102] |
|  | 63 | VSRN1406F++-1 | AF | B | Transistor(RN1406) | [Q104] |
|  | 64 | VSRN1406F++-1 | AF | B | Transistor(RN1406) | Q105] |


| NO. | PARTS CODE | PRICE <br> RANK | NEW <br> MARK | PART <br> RANK | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |

## [6] LIU PWB unit



UX-B800A


| PARTS CODE | No． | PRICE RANK | NEW | PART RANK |
| :---: | :---: | :---: | :---: | :---: |
| 【 C 】 |  |  |  |  |
| CCNWN495CXH01 | 1－28 | AP |  | C |
| CFRM－2277XH01 | 1－45 | BD |  | C |
| CFRM－2278XH01 | 1－8 | BD |  | C |
| CPAKC439HXH01 | 4－11 | BA | N | D |
| CPLTP3434XH01 | 4－7 | BB |  | C |
| CRŌLP2499XH01 | 1－29 | AM |  | C |
| CROLR2362AX01 | 1－18 | AN |  | C |
| CROLLR2548XH01 | 1－27 | AT |  | C |
| 【 D 】 |  |  |  |  |
| DCEKC481XXHS1 | 1－52 | BW | N | E |
| ＂ | 5－901 | BW | N | E |
| DCEKL 259 DXH 14 | 1－30 | BE | N | E |
| ＂ | 6－901 | BE | N | E |
| DCEKP341DXH07 | 2－901 | BQ | N | E |
| DCEKP343DXH01 | 2－9 | AU |  | E |
| ＂ | 8－901 | AU |  | E |
| DUNTK245DXHPW | 4－12 | AX |  | E |
| DUNTM342DXH01 | 1－53 | BR |  | E |
| 【 G 】 |  |  |  |  |
| GCABB2468XHSB | 1－31 | BC |  | D |
| GCASP2197XHSA | 2－1 | BA |  | D |
| GCASP2198XHSA | 2－2 | BA |  | D |
| GCŌVA2538XHSA | 1－68 | BC |  | C |
| GCOVA2539XHSA | 1－69 | BC |  | C |
| GCŌVA2540XHSA | 1－1 | BC |  | C |
| GLEGG2078XHZZ | 1－32 | AD |  | C |
| 【 H 】 |  |  |  |  |
| HPNLH2458XHSD | 2－37 | AP |  | D |
| 【 J 】 |  |  |  |  |
| JBTN－2537XHSA | 2－3 | AS |  | C |
| JBTN－2538XHSA | 2－4 | AS |  | C |
| JBTN－2539XHSA | 2－5 | AS |  | C |
| JBTN－2541XHSA | 2－6 | AS |  | C |
| JBTN－2542XHSC | 2－7 | AS |  | C |
| JBTN－2544XHSA | 2－8 | AS |  | C |
| 【 L 】 |  |  |  |  |
| LBNDJ2006XHZZ | 1－56 | AA |  | C |
| LBSHP 2169 XHZZ | 1－5 | AF |  | C |
| LFRM－2277XHZZ | 1－46 | AT |  | C |
| LFRM－2278XHZZ | 1－9 | AT |  | C |
| LHLDZ2322XHZZ | 1－64 | AK |  | C |
| LHLDZ2323XHZZ | 1－65 | AK |  | C |
| LPLTG3429XHZZ | 2－18 | AF |  | C |
| LPLTM2924XHFW | 3－1 | AQ |  | C |
| LPLTM3317XHZZ | 1－33 | AL |  | C |
| LPLTM3431XHZZ | 1－47 | AG |  | C |
| LPLTM3436XHZZ | 1－10 | AG |  | C |
| LPLTM3437XHZZ | 1－57 | AG |  | C |
| LPLTM3441 XHZZ | 1－55 | AL |  | C |
| LPLTP2884AXZA | 1－22 | AP |  | C |
| LPLTP3257XHZL | 2－19 | AE |  | C |
| LPLTP3257XHZR | 2－20 | AE |  | C |
| LPLTP3263XHSJ | 3－2 | AP |  | C |
| LPLTP3430XHZZ | 2－21 | AW |  | C |
| LPLTP3432XHSA | 1－34 | AW |  | C |
| ＂ | 4－21 | AW |  | C |
| LPLTP3433XHSA | 1－35 | AW |  | C |
| ＂ | 4－22 | AW |  | C |
| LPLTP3442XHSA | 4－9 | AQ |  | C |
| LX－BZ2138XHZ7 | 1－B6 | AD |  | C |
| LX－BZ2222XHZ7 | 2－B2 | AD |  | C |
| ＂ | 3－B1 | AD |  | C |
| LX－BZ2234XHZ7 | 1－B7 | AA |  | C |
| LX－BZ2282XHZ7 | 1－B3 | AE |  | C |
| LX－BZ2286XHZ7 | 1－B5 | AE |  | C |
| 【 M 】 |  |  |  |  |
| MARMP 2034 XHZA | 2－22 | AF |  | C |
| MARMP 2042 XHZA | 1－70 | AF |  | C |
| MARMP 2046XHZZ | 1－48 | AH |  | C |
| MLEVP2426XHZZ | 2－23 | AH |  | C |
| MLEVP2427XHZZ | 2－24 | AK |  | C |
| MLEVP2428XHZZ | 1－2 | AK |  | C |
| MSPRC3387XHZZ | 1－49 | AD |  | C |
| MSPRC3406XHZZ | 1－23 | AD |  | C |
| MSPRC3407XHZZ | 3－3 | AE |  | C |
| MSPRC3470XHZZ | 1－36 | AF |  | C |
| MSPRC3488XHZZ | 1－58 | AE |  | C |
| MSPRC3489XHZZ | 1－59 | AE |  | C |


| PARTS CODE | No． | PRICE RANK | NEW MARK | PART RANK |
| :---: | :---: | :---: | :---: | :---: |
| MSPRC3630XHZZ | 2－14 | AE |  | C |
| MSPRC3634XHZZ | 1－3 | AE |  | C |
| MSPRD3398XHZZ | 2－25 | AE |  | C |
| MSPRD3399XHZZ | 2－26 | AE |  | C |
| MSPRD3495XHZZ | 2－29 | AE |  | C |
| MSPRD3632XHZZ | 2－30 | AE |  | C |
| MSPRD3633XHZZ | 2－31 | AE |  | C |
| MSPRD3636XHZZ | 1－11 | AE |  | C |
| MSPRD3637XHZZ | 1－60 | AE |  | C |
| MSPRD3656XHZZ | 2－27 | AE |  | C |
| MSPRD3657XHZZ | 2－28 | AE |  | C |
| MSPRP3119XHZZ | 1－4 | AC |  | C |
| MSPRT2932XHFJ | 3－4 | AC |  | C |
| 【 N 】 |  |  |  |  |
| NGERH2365AXZZ | 3－5 | AD |  | C |
| NGERH2366AXZZ | 3－6 | AD |  | C |
| NGERH2611XHZZ | 1－12 | AE |  | C |
| ＂ | 1－50 | AE |  | C |
| NGERH2636XHZZ | 1－37 | AF |  | C |
| NGERH2639XHZZ | 1－13 | AF |  | C |
| NGERH2641XHZZ | 1－6 | AE |  | C |
| NGERH2717XHZZ | 1－51 | AP |  | C |
| NGERH2718XHZZ | 1－14 | AP |  | C |
| NGERH2719XHZZ | 1－15 | AP |  | C |
| NGERH2720XHZZ | 1－16 | AP |  | C |
| NGERH2721XHZZ | 1－19 | AP |  | C |
| NGERP2318XHZZ | 2－15 | AD |  | C |
| NRŌLP2332XHZA | 2－32 | AL |  | C |
| NRŌLR2549XHZZ | 1－7 | AU |  | C |
| NSFTP2413XHZZ | 1－61 | AL |  | C |
| NSFTZ2367XHZZ | 1－20 | AG |  | C |
| 【 P 】 |  |  |  |  |
| PCOVVA146XHSA | 1－71 | AY |  | C |
| PCUSU2238XHZZ | 2－38 | AE |  | C |
| PFLT－2039XHZZ | 1－62 | AK |  | C |
| PGidM2493XHVB | 3－9 | AL |  | C |
| PGiDM2723XHSA | 1－72 | AT |  | C |
| PGiDM2724XHSA | 2－16 | AT |  | C |
| PGiDM2725XHSA | 2－17 | AT |  | C |
| PGiDM2726XHZZ | 2－33 | AT |  | C |
| PGiDM2727XHZZ | 1－63 | AT |  | C |
| PGidM2728XHZZ | 1－21 | AT |  | C |
| PGiDM2729XHSA | 1－24 | AT |  | C |
| PGUMM2225XHZZ | 1－73 | AG |  | C |
| PHŌP－2095XHVD | 3－7 | AU |  | C |
| PSEL－2015XHZZ | 3－8 | AB |  | C |
| PSHEF3928XHZZ | 2－34 | AN |  | C |
| PSHEP3939XHZZ | 2－35 | AG |  | C |
| PSHEZ3293XHZZ | 1－76 | AH |  | C |
| PSHEZ3344XHZZ | 1－77 | AD |  | C |
| PSHEZ3410XHZZ | 1－83 | AB |  | C |
| PSHEZ3760XHZZ | 1－25 | AE |  | C |
| PSHEZ3770XHZZ | 1－26 | AG |  | C |
| 【 Q 】 |  |  |  |  |
| QACCL2102XHZZ | 1－40 | AY |  | B |
| QCNCM2666XHOB | 5－227 | AD |  | C |
| QCNCM2666XH0D | 5－226 | AD |  | C |
| QCNCM2666XH0G | 5－216 | AE |  | C |
| QCNCM2666XHOH | 5－225 | AE |  | C |
| QCNCM2666XH1E | 5－221 | AF |  | C |
| ＂ | 6－43 | AF |  | C |
| QCNCM7014SC0B | 5－217 | AD |  | C |
| ＂ | 5－219 | AD |  | C |
| QCNCW2556SC0D | 5－218 | AH |  | C |
| QCNCW2556SC1D | 5－223 | AH |  | C |
| ＂ | 5－224 | AH |  | C |
| QCNCW2556SC1i | 5－222 | AG |  | C |
| QCNTM2054XHZZ | 2－10 | AP |  | C |
| QCNTM2055XHZZ | 2－11 | AP |  | C |
| QCNTM2056XHZZ | 2－12 | AT |  | C |
| QCNWG0376AFZZ | 4－14 | AM |  | C |
| QCNWG202DXHFW | 4－13 | AP |  | C |
| QCNWN200CXHZZ | 1－41 | AL |  | C |
| QCNWN205CXHZZ | 6－4 | AF |  | C |
| ＂ | 6－49 | AF |  | C |
| QCNWN253DXHZZ | 1－66 | AP |  | C |
| QCNWN263DXHZZ | 1－54 | AC |  | C |
| QCNWN294DXHZZ | 1－42 | AQ | N | C |
| QCNWN499CXHZZ | 1－75 | AP |  | C |


| PARTS CODE | No． | PRICE | NEW MARK | PART RANK |
| :---: | :---: | :---: | :---: | :---: |
| ＂ | 2－36 | AP |  | C |
| QFS－L2016XHZZ | 5－233 | AD |  | A |
| QJAKT2089XHZZ | 5－220 | AT |  | C |
| QJAKZ2079XHOD | 6－42 | AD |  | C |
| QJAKZ2087XHOB | 6－45 | AD |  | C |
| QJAKZ2087XHOD | 6－44 | AE |  | C |
| QPLGZ9065AFZZ | 4－28 | AP |  | C |
| 【 R 】 |  |  |  |  |
| RC－FZ3078SCZZ | 6－7 | AF |  | C |
| RC－FZ3079SCZZ | 6－6 | AG |  | C |
| RCiLF2125SCZZ | 6－53 | AF |  | C |
| RCiLZ2200XHZZ | 5－255 | AP |  | C |
| RC－KZ3122SCZZ | 5－174 | AL |  | C |
| ＂ | 5－181 | AL |  | C |
| RCORF 2137 XHZZ | 1－80 | AF |  | B |
| RCŌRF 2145 XHZZ | 1－43 | AF |  | B |
| RCRSA2233XHPZ | 5－438 | AG |  | B |
| RCRSP2236XHZZ | 5－437 | AK |  | B |
| RCRSP2237XHZZ | 5－439 | AK |  | B |
| RDENT2228XHZZ | 1－44 | BF |  | E |
| ＂ | 7－901 | BF |  | E |
| RFiLN2027XHZZ | 6－51 | AC |  | C |
| ＂ | 6－52 | AC |  | C |
| ＂ | 6－54 | AC |  | C |
| ＂ | 6－55 | AC |  | C |
| RH－DX2007SCZZ | 6－108 | AC |  | B |
| RH－iX2346XHZZ | 5－239 | AG |  | B |
| ＂ | 5－241 | AG |  | B |
| RH－iX2383XHZZ | 6－50 | AG |  | B |
| RH－iX2505XHPZ | 5－234 | BG |  | B |
| RMOTS2209XHZZ | 1－17 | AV |  | B |
| RRLYD3434XHZZ | 6－41 | AP |  | B |
| RR－SZ3047SCZZ | 5－387 | AA |  | C |
| ＂ | 5－388 | AA |  | C |
| ＂ | 5－389 | AA |  | C |
| ＂ | 5－390 | AA |  | C |
| RR－SZ3048SCZZ | 5－391 | AA |  | C |
| ＂ | 5－392 | AA |  | C |
| RTRNi2165XHZZ | 6－110 | AG |  | B |
| RTRNL2178SCZZ | 5－248 | AA |  | B |
| RUNTZ2145XHZZ | 1－67 | BN |  | B |
| 【 S 】 |  |  |  |  |
| SPAKA275HXHZZ | 4－10 | AQ |  | D |
| SPAKA276HXHZZ | 4－17 | AQ |  | D |
| SPAKA364HXHZZ | 4－24 | AF |  | D |
| SPAKA441HXHZZ | 4－15 | AG | N | D |
| SPAKA442HXHZZ | 4－16 | AQ | N | D |
| SPAKP 274 HXHZZ | 4－18 | AQ |  | D |
| SPAKP420DXHZZ | 4－26 | AF |  | D |
| SSAKA2008XHZZ | 4－25 | AA |  | D |
| SSAKA3001CCZZ | 4－27 | AA |  | D |
| 【 T 】 |  |  |  |  |
| TCADH3939XHZZ | 4－2 | AF | N | D |
| TiNSE4562XHTZ | 4－1 | AM | N | D |
| TLABH415KXHSA | 4－6 | AK |  | D |
| TLABH545KXHZZ | 1－82 | AE |  | D |
| 【 U 】 |  |  |  |  |
| UBATL2232XHZZ | 5－1 | AF |  | B |
| U i NK－2046XHZZ | 4－20 | BN |  | A |
| \ V 】 |  |  |  |  |
| VCCCCY1HH221J | 6－26 | AA |  | C |
| VCCCCY1HH470J | 5－205 | AA |  | C |
| ＂ | 5－206 | AA |  | C |
| ＂ | 5－207 | AA |  | C |
| ＂ | 5－208 | AA |  | C |
| ＂ | 5－212 | AA |  | C |
| ＂ | 5－213 | AA |  | C |
| ＂ | 5－214 | AA |  | C |
| ＂ | 5－215 | AA |  | C |
| VCCCCY1HH560J | 5－203 | AA |  | C |
| ＂ | 5－204 | AA |  | C |
| VCCCCY1HH680J | 6－32 | AA |  | C |
| VCCCCZ1EH101J | 5－66 | AA |  | C |
| ＂ | 5－67 | AA |  | C |
| ＂ | 5－68 | AA |  | C |
| ＂ | 5－69 | AA |  | C |
| ＂ | 5－134 | AA |  | C |
| VCCCCZ1EH180J | 5－176 | AA |  | C |
| ＂ | 5－177 | AA |  | C |
| VCCCCZ1EH221J | 5－84 | AB |  | C |
| ＂ | 5－85 | AB |  | C |
| ＂ | 5－86 | AB |  | C |


| PARTS CODE | No． | PRICE RANK | $\begin{array}{\|c\|} \text { NEW } \\ \text { MARK } \end{array}$ | PART RANK |
| :---: | :---: | :---: | :---: | :---: |
| ＂ | 5－107 | AB |  | C |
| ＂ | 5－108 | AB |  | C |
| ＂ | 5－109 | AB |  | C |
| ＂ | 5－110 | AB |  | C |
| ＂ | 5－115 | AB |  | C |
| ＂ | 5－119 | AB |  | C |
| ＂ | 5－122 | AB |  | C |
| ＂ | 5－123 | AB |  | C |
| ＂ | 5－126 | AB |  | C |
| ＂ | 5－127 | AB |  | C |
| ＂ | 5－130 | AB |  | C |
| ＂ | 5－132 | AB |  | C |
| VCCCCZ1EH270J | 5－58 | AA |  | C |
| ＂ | 5－59 | AA |  | C |
| VCCCCZ1EH7R0D | 5－51 | AA |  | C |
| ＂ | 5－52 | AA |  | C |
| VCEAEA1CW226M | 6－14 | AA |  | C |
| VCEAEA1EW475M | 6－15 | AA |  | C |
| VCEAGA1CW476M | 6－10 | AB |  | C |
| VCEAGA1HW106M | 6－5 | AA |  | C |
| ＂ | 6－11 | AA |  | C |
| VCEAGA1HW107M | 6－9 | AA |  | C |
| ＂ | 6－13 | AA |  | C |
| VCEAGA1HW226M | 5－135 | AB |  | C |
| ＂ | 5－145 | AB |  | C |
| ＂ | 6－8 | AB |  | C |
| VCEAZA1HW227M | 5－146 | AG |  | C |
| ＂ | 5－162 | AG |  | C |
| VCFYDA1HA334J | 6－12 | AC |  | C |
| VCKYCY1AB105K | 5－111 | AB |  | C |
| ＂ | 5－116 | AB |  | C |
| ＂ | 5－128 | AB |  | C |
| ＂ | 5－140 | AB |  | C |
| ＂ | 5－141 | AB |  | C |
| ＂ | 5－198 | AB |  | C |
| ＂ | 6－88 | AB |  | C |
| VCKYCY1AF105Z | 5－57 | AC |  | C |
| ＂ | 5－192 | AC |  | C |
| VCKYCY1CB104K | 5－106 | AB |  | C |
| ＂ | 6－19 | AB |  | C |
| ＂ | 6－28 | AB |  | C |
| ＂ | 6－31 | AB |  | C |
| ＂ | 6－35 | AB |  | C |
| ＂ | 6－37 | AB |  | C |
| ＂ | 6－40 | AB |  | C |
| VCKYCY1HB102K | 5－199 | AA |  | C |
| ＂ | 5－200 | AA |  | C |
| ＂ | 5－201 | AA |  | C |
| ＂ | 5－202 | AA |  | C |
| ＂ | 6－22 | AA |  | C |
| ＂ | 6－23 | AA |  | C |
| ＂ | 6－27 | AA |  | C |
| ＂ | 6－33 | AA |  | C |
| VCKYCY1HB103K | 6－36 | AA |  | C |
| VCKYCY1HB152K | 6－25 | AB |  | C |
| VCKYCY1HB183K | 6－21 | AB |  | C |
| VCKYCY1HB222K | 6－38 | AA |  | C |
| ＂ | 6－39 | AA |  | C |
| VCKYCY1HB223K | 6－29 | AC |  | C |
| VCKYCY1HB563K | 6－17 | AC |  | C |
| VCKYCY1HB683K | 6－30 | AC |  | C |
| VCKYCY1HB821K | 6－34 | AA |  | C |
| VCKYCY1HF104Z | 5－83 | AA |  | C |
| ＂ | 5－139 | AA |  | C |
| ＂ | 5－144 | AA |  | C |
| ＂ | 5－147 | AA |  | C |
| ＂ | 5－148 | AA |  | C |
| ＂ | 5－149 | AA |  | C |
| ＂ | 5－150 | AA |  | C |
| ＂ | 5－151 | AA |  | C |
| ＂ | 5－152 | AA |  | C |
| ＂ | 5－161 | AA |  | C |
| ＂ | 5－163 | AA |  | C |
| ＂ | 5－193 | AA |  | C |
| ＂ | 5－194 | AA |  | C |
| ＂ | 5－195 | AA |  | C |
| ＂ | 6－16 | AA |  | C |
| ＂ | 6－24 | AA |  | C |
| VCKYCZ0JB105K | 5－155 | AB |  | C |
| VCKYCZ1AB104K | 5－61 | AC |  | C |
| ＂ | 5－87 | AC |  | C |
| ＂ | 5－88 | AC |  | C |


| PARTS CODE | No. | PRICE <br> RANK | NEW MARK | PART RANK | PARTS CODE | No. | PRICE <br> RANK | NEW MARK | PART <br> RANK |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| / | 5-117 | AC |  | C | " | 5-102 | AC |  | C |
| " | 5-118 | AC |  | C | " | 5-131 | AC |  | C |
| " | 5-159 | AC |  | C | " | 5-190 | AC |  | C |
| VCKYCZ1CB103K | 5-156 | AA |  | C | VHD1N4148//-1 | 6-46 | AA |  | B |
| " | 5-157 | AA |  | C | " | 6-47 | AA |  | B |
| " | 5-158 | AA |  | C | VHD1SS352F+-1 | 5-228 | AF |  | B |
| " | 5-175 | AA |  | C | " | 5-230 | AF |  | B |
| " | 5-191 | AA |  | C | " | 5-231 | AF |  | B |
| VCKYCZ1CB683K | 5-160 | AE |  | C | VHD1SS355//-1 | 6-48 | AB |  | B |
| VCKYCZ1CF104Z | 5-46 | AB |  | C | VHDB340A+++-1 | 5-440 | AH |  | B |
| / | 5-47 | AB |  | C | VHDRB715F//-1 | 5-229 | AF |  | B |
| " | 5-48 | AB |  | C | VHEHZ2A1///-1 | 6-113 | AC |  | B |
| " | 5-49 | AB |  | C | " | 6-114 | AC |  | B |
| " | 5-50 | AB |  | C | VHEMTZJ100B-1 | 6-115 | AC |  | B |
| " | 5-54 | AB |  | C | VHEMTZJ200B-1 | 6-117 | AC |  | B |
| " | 5-55 | AB |  | C | VHEMTZJ300B-1 | 6-116 | AA |  | B |
| " | 5-60 | AB |  | C | VHEMTZJ6R8B-1 | 6-112 | AC |  | B |
| " | 5-62 | AB |  | C | VHEPTZ12B++-1 | 5-441 | AG |  | B |
| " | 5-63 | AB |  | C | VHHTBPS333H-1 | 5-436 | AF |  | B |
| " | 5-6 4 | AB |  | C | VHi 14D0830+-1 | 5-244 | AU |  | B |
| " | 5-65 | AB |  | C | VHi 25V016CS-1 | 5-235 | AX |  | B |
| " | 5-71 | AB |  | C | VHiBA178M24-1 | 5-435 | AL |  | B |
| " | 5-73 | AB |  | C | VHiCX20452+-1 | 5-240 | AY |  | B |
| " | 5-74 | AB |  | C | VHiCX20556+-1 | 5-251 | AU |  | B |
| " | 5-75 | AB |  | C | VHiF016+TF64B | 5-238 | AZ | N | B |
| " | 5-76 | AB |  | C | VHiKiA78L05BP | 6-109 | AF |  | B |
| " | 5-77 | AB |  | C | VHiLD111733-1 | 5-434 | AN |  | B |
| " | 5-78 | AB |  | C | VHiLN91C113-1 | 5-246 | BA |  | B |
| " | 5-79 | AB |  | C | VHiLV125AT+-1 | 5-237 | AN |  | B |
| " | 5-80 | AB |  | C | VHiLVC2G14D-1 | 5-247 | AN |  | B |
| " | 5-81 | AB |  | C | VHiNJM2113M-1 | 5-242 | AG |  | B |
| " | 5-89 | AB |  | C | VHiS64AH75E-1 | 5-236 | BA |  | B |
| " | 5-90 | AB |  | C | VHiTC7PA04+-1 | 5-245 | AG |  | B |
| " | 5-92 | AB |  | C | VHiTC7PA53F-1 | 5-243 | AG |  | B |
| " | 5-9 4 | AB |  | C | " | 5-249 | AG |  | B |
| " | 5-96 | AB |  | C | " | 5-250 | AG |  | B |
| " | 5-97 | AB |  | C | VHPGP1S094HCZ | 5-261 | AG |  | B |
| " | 5-99 | AB |  | C | VHPPS2535-1// | 6-56 | AH |  | B |
| " | 5-100 | AB |  | C | VHPPS2561L-1/ | 6-57 | AG |  | B |
| " | 5-101 | AB |  | C | VHPSG206S//-1 | 6-58 | AG |  | B |
| " | 5-103 | AB |  | C | " | 8-1 | AG |  | B |
| " | 5-114 | AB |  | C | " | 8-2 | AG |  | B |
| " | 5-129 | AB |  | C | " | 8-3 | AG |  | B |
| " | 5-137 | AB |  | C | VHVDSS301L/-U | 6-1 | AF |  | B |
| " | 5-153 | AB |  | C | VHVRA501PC6-1 | 6-2 | AG |  | B |
| " | 5-154 | AB |  | C | " | 6-3 | AG |  | B |
| " | 5-166 | AB |  | C | VHVTN07G101-1 | 6-111 | AB |  | B |
| " | 5-167 | AB |  | C | VRD-HT2EY101J | 6-71 | AA |  | C |
| " | 5-168 | AB |  | C | " | 6-72 | AA |  | C |
| " | 5-169 | AB |  | C | VRD-HT2EY103J | 6-67 | AA |  | C |
| " | 5-170 | AB |  | C | VRD-HT2EY150J | 6-66 | AA |  | C |
| " | 5-171 | AB |  | C | " | 6-68 | AA |  | C |
| " | 5-172 | AB |  | C | VRD-HT2EY151J | 6-73 | AA |  | C |
| " | 5-173 | AB |  | C | VRD-HT2EY221J | 6-69 | AA |  | C |
| " | 5-178 | AB |  | C | VRD-HT2EY433J | 6-70 | AA |  | C |
| " | 5-179 | AB |  | C | VRD-HT2HY223J | 6-74 | AA |  | C |
| " | 5-180 | AB |  | C | VRS-CG1JF000J | 5-37 | AB |  | C |
| " | 5-182 | AB |  | C | " | 5-38 | AB |  | C |
| " | 5-183 | AB |  | C | " | 5-44 | AB |  | C |
| " | 5-184 | AB |  | C | " | 5-45 | AB |  | C |
| " | 5-185 | AB |  | C | VRS-CG1JF101J | 5-2 | AA |  | C |
| " | 5-186 | AB |  | C | " | 5-3 | AA |  | C |
| " | 5-187 | AB |  | C | " | 5-4 | AA |  | C |
| " | 5-189 | AB |  | C | " | 5-5 | AA |  | C |
| " | 5-196 | AB |  | C | " | 5-6 | AA |  | C |
| " | 5-197 | AB |  | C | " | 5-7 | AA |  | C |
| VCKYCZ1EB102K | 5-70 | AA |  | C | " | 5-8 | AA |  | C |
| " | 5-82 | AA |  | C | " | 5-17 | AA |  | C |
| " | 5-91 | AA |  | C | " | 5-22 | AA |  | C |
| " | 5-98 | AA |  | C | " | 5-23 | AA |  | C |
| " | 5-105 | AA |  | C | " | 5-24 | AA |  | C |
| " | 5-136 | AA |  | C | " | 5-27 | AA |  | C |
| " | 5-164 | AA |  | C | " | 5-28 | AA |  | C |
| " | 5-165 | AA |  | C | " | 5-29 | AA |  | C |
| VCKYCZ1EB222K | 5-138 | AB |  | C | " | 5-30 | AA |  | C |
| " | 5-143 | AB |  | C | " | 5-31 | AA |  | C |
| VCKYCZ1EB472K | 5-124 | AA |  | C | VRS-CG1JF103J | 5-9 | AC |  | C |
| " | 5-125 | AA |  | C | " | 5-36 | AC |  | C |
| VCKYTV1AF106Z | 5-53 | AC |  | C | " | 5-39 | AC |  | C |
| " | 5-56 | AC |  | C | VRS-CG1JF221J | 5-21 | AC |  | C |
| " | 5-93 | AC |  | C | " | 5-32 | AC |  | C |
| " | 5-95 | AC |  | C | " | 5-33 | AC |  | C |


| PARTS CODE | No. | PRICE <br> RANK | $\begin{array}{\|c\|} \text { NEW } \\ \text { MARK } \end{array}$ | PART RANK | PARTS CODE | No. | PRICE RANK | NEW MARK | PART RANK |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| " | 5-34 | AC |  | C | " | 5-297 | AA |  | C |
| " | 5-35 | AC |  | C | " | 5-306 | AA |  | C |
| VRS-CG1JF271J | 5-10 | AD |  | C | " | 5-307 | AA |  | C |
| " | 5-11 | AD |  | C | " | 5-308 | AA |  | C |
| " | 5-15 | AD |  | C | " | 5-314 | AA |  | C |
| " | 5-16 | AD |  | C | " | 5-317 | AA |  | C |
| " | 5-26 | AD |  | C | " | 5-324 | AA |  | C |
| VRS-CG1JF330J | 5-12 | AC |  | C | " | 5-325 | AA |  | C |
| " | 5-13 | AC |  | C | " | 5-330 | AA |  | C |
| " | 5-14 | AC |  | C | " | 5-339 | AA |  | C |
| " | 5-18 | AC |  | C | " | 5-342 | AA |  | C |
| " | 5-19 | AC |  | C | " | 5-343 | AA |  | C |
| " | 5-20 | AC |  | C | " | 5-344 | AA |  | C |
| " | 5-25 | AC |  | C | " | 5-345 | AA |  | C |
| " | 5-40 | AC |  | C | " | 5-346 | AA |  | C |
| " | 5-41 | AC |  | C | " | 5-377 | AA |  | C |
| VRS-CG1JF333J | 5-42 | AC |  | C | " | 5-413 | AA |  | C |
| " | 5-43 | AC |  | C | " | 5-414 | AA |  | C |
| VRS-CY1JB000J | 5-113 | AA |  | C | " | 5-418 | AA |  | C |
| " | 5-121 | AA |  | C | " | 5-420 | AA |  | C |
| " | 5-232 | AA |  | C | " | 5-421 | AA |  | C |
| " | 5-252 | AA |  | C | " | 5-425 | AA |  | C |
| " | 5-253 | AA |  | C | " | 5-427 | AA |  | C |
| " | 5-254 | AA |  | C | " | 5-432 | AA |  | C |
| " | 5-256 | AA |  | C | " | 5-433 | AA |  | C |
| " | 5-258 | AA |  | C | VRS-CZ1JB100J | 5-412 | AA |  | C |
| " | 5-369 | AA |  | C | VRS-CZ1JB101J | 5-284 | AA |  | C |
| " | 5-370 | AA |  | C | " | 5-285 | AA |  | C |
| " | 5-410 | AA |  | C | " | 5-286 | AA |  | C |
| " | 5-411 | AA |  | C | " | 5-293 | AA |  | C |
| " | 5-415 | AA |  | C | " | 5-295 | AA |  | C |
| " | 6-18 | AA |  | C | VRS-CZ1JB102J | 5-327 | AA |  | C |
| " | 6-20 | AA |  | C | " | 5-335 | AA |  | C |
| " | 6-96 | AA |  | C | " | 5-408 | AA |  | C |
| VRS-CY1JB102J | 6-91 | AA |  | C | VRS-CZ1JB103J | 5-72 | AA |  | C |
| " | 6-93 | AA |  | C | " | 5-273 | AA |  | C |
| " | 6-98 | AA |  | C | " | 5-276 | AA |  | C |
| " | 6-106 | AA |  | C | " | 5-290 | AA |  | C |
| VRS-CY1JB103J | 6-92 | AA |  | C | " | 5-299 | AA |  | C |
| " | 6-94 | AA |  | C | " | 5-300 | AA |  | C |
| VRS-CY1JB113J | 6-99 | AA |  | C | " | 5-301 | AA |  | C |
| VRS-CY1JB152J | 6-105 | AA |  | C | " | 5-303 | AA |  | C |
| VRS-CY1JB154J | 6-95 | AA |  | C | / | 5-304 | AA |  | C |
| " | 6-101 | AA |  | C | " | 5-305 | AA |  | C |
| VRS-CY1JB162J | 6-82 | AA |  | C | " | 5-312 | AA |  | C |
| VRS-CY1JB203J | 6-90 | AA |  | C | " | 5-313 | AA |  | C |
| VRS-CY1JB222J | 6-104 | AA |  | C | " | 5-315 | AA |  | C |
| VRS-CY1JB223J | 6-83 | AA |  | C | " | 5-319 | AA |  | C |
| " | 6-84 | AA |  | C | " | 5-349 | AA |  | C |
| VRS-CY1JB224J | 6-97 | AA |  | C | " | 5-350 | AA |  | C |
| VRS-CY1JB271J | 6-80 | AA |  | C | " | 5-358 | AA |  | C |
| VRS-CY1JB332J | 6-87 | AA |  | C | " | 5-362 | AA |  | C |
| " | 6-89 | AA |  | C | " | 5-364 | AA |  | C |
| " | 6-102 | AA |  | C | " | 5-371 | AA |  | C |
| VRS-CY1JB333J | 6-85 | AA |  | C | " | 5-372 | AA |  | C |
| " | 6-86 | AA |  | C | " | 5-375 | AA |  | C |
| VRS-CY1JB392J | 6-79 | AA |  | C | " | 5-381 | AA |  | C |
| VRSCY1JB49R9F | 5-397 | AG |  | C | " | 5-382 | AA |  | C |
| " | 5-398 | AG |  | C | " | 5-403 | AA |  | C |
| " | 5-399 | AG |  | C | " | 5-404 | AA |  | C |
| " | 5-400 | AG |  | C | " | 5-405 | AA |  | C |
| " | 5-401 | AG |  | C | " | 5-409 | AA |  | C |
| " | 5-402 | AG |  | C | " | 5-426 | AA |  | C |
| VRS-CY1JB621J | 6-81 | AA |  | C | VRS-CZ1JB104J | 5-272 | AA |  | C |
| VRSCY1JB75R0F | 5-385 | AD |  | C | " | 5-281 | AA |  | C |
| " | 5-386 | AD |  | C | " | 5-288 | AA |  | C |
| VRS-CY1JB822J | 6-100 | AA |  | C | " | 5-289 | AA |  | C |
| VRS-CY1JB913J | 6-103 | AA |  | C | " | 5-296 | AA |  | C |
| VRS-CZ1JB000J | 5-104 | AA |  | C | " | 5-316 | AA |  | C |
| " | 5-120 | AA |  | C | " | 5-331 | AA |  | C |
| " | 5-133 | AA |  | C | " | 5-366 | AA |  | C |
| " | 5-142 | AA |  | C | " | 5-417 | AA |  | C |
| " | 5-188 | AA |  | C | " | 5-429 | AA |  | C |
| " | 5-209 | AA |  | C | " | 5-430 | AA |  | C |
| " | 5-210 | AA |  | C | " | 5-431 | AA |  | C |
| " | 5-211 | AA |  | C | VRS-CZ1JB105J | 5-278 | AD |  | C |
| " | 5-257 | AA |  | C | " ${ }^{\prime \prime}$ | 5-393 | AD |  | C |
| " | 5-269 | AA |  | C | VRS-CZ1JB122J | 5-279 | AA |  | C |
| " | 5-270 | AA |  | C | VRS-CZ1JB124F | 5-359 | AD |  | C |
| " | 5-277 | AA |  | C | " | 5-361 | AD |  | C |
| " | 5-280 | AA |  | C | VRS-CZ1JB133J | 5-309 | AC |  | C |
| " | 5-294 | AA |  | C | / | 5-311 | AC |  | C |


| PARTS CODE | No． | PRICE RANK | NEW MARK | PART RANK |
| :---: | :---: | :---: | :---: | :---: |
| VRS－CZ1JB152J | 5－348 | AA |  | C |
| ＂ | 5－354 | AA |  | C |
| VRS－CZ1JB164J | 5－329 | AD |  | C |
| VRS－CZ1JB183J | 5－328 | AD |  | C |
| VRS－CZ1JB202J | 5－112 | AD |  | C |
| ＂ | 5－321 | AD |  | C |
| VRS－CZ1JB203J | 5－326 | AD |  | C |
| VRS－CZ1JB221J | 5－282 | AD |  | C |
| ＂ | 5－383 | AD |  | C |
| ／ | 5－406 | AD |  | C |
| VRS－CZ1JB222F | 5－368 | AG |  | C |
| VRS－CZ1JB224J | 5－332 | AA |  | C |
| ＂ | 5－334 | AA |  | C |
| VRS－CZ1JB225J | 5－271 | AA |  | C |
| VRS－CZ1JB271J | 5－291 | AA |  | C |
| ＂ | 5－298 | AA |  | C |
| ＂ | 5－423 | AA |  | C |
| ＂ | 5－424 | AA |  | C |
| ／ | 5－428 | AA |  | C |
| VRS－CZ1JB303J | 5－341 | AD |  | C |
| VRS－CZ1JB330J | 5－287 | AA |  | C |
| ＂ | 5－376 | AA |  | C |
| VRS－CZ1JB332J | 5－310 | AA |  | C |
| ＂ | 5－320 | AA |  | C |
| ＂ | 5－333 | AA |  | C |
| ＂ | 5－340 | AA |  | C |
| VRS－CZ1JB363F | 5－355 | AG |  | C |
| ＂ | 5－360 | AG |  | C |
| VRSCZ1JB3650F | 5－357 | AG |  | C |
| VRS－CZ1JB393J | 5－318 | AD |  | C |
| VRS－CZ1JB470J | 5－395 | AA |  | C |
| VRS－CZ1JB471J | 5－292 | AA |  | C |
| ＂ | 5－378 | AA |  | C |
| VRS－CZ1JB472J | 5－302 | AA |  | C |
| ＂ | 5－323 | AA |  | C |
| ＂ | 5－337 | AA |  | C |
| VRS－CZ1JB473J | 5－322 | AA |  | C |
| VRSCZ1JB4991F | 5－283 | AG |  | C |
| ＂ | 5－373 | AG |  | C |
| ＂ | 5－374 | AG |  | C |
| VRSCZ1JB5490F | 5－356 | AG |  | C |
| ＂ | 5－363 | AG |  | C |
| VRS－CZ1JB562J | 5－351 | AA |  | C |
| ＂ | 5－416 | AA |  | C |
| VRSCZ1JB6041F | 5－365 | AG |  | C |
| VRS－CZ1JB620J | 5－347 | AA |  | C |
| VRS－CZ1JB681J | 5－394 | AC |  | C |
| VRSCZ1JB7321F | 5－367 | AG |  | C |
| VRS－CZ1JB823J | 5－336 | AD |  | C |
| VRS－CZ1JB912F | 5－338 | AG |  | C |
| VRS－HT3AA1R3J | 5－352 | AE |  | C |
| ＂ | 5－353 | AE |  | C |
| VRS－HT3DA110J | 5－422 | AE |  | C |
| VRS－HT3DA221J | 5－407 | AB |  | C |
| VRS－RE2HA101J | 6－75 | AB |  | C |
| VRS－TS2AD000J | 6－77 | AA |  | C |
| VRS－TS2AD153J | 6－76 | AA |  | C |
| VRS－TS2AD301J | 6－107 | AA |  | C |
| VRS－TS2AD471J | 6－78 | AA |  | C |
| VRS－TV2AB000J | 5－259 | AA |  | C |
| ＂ | 5－260 | AA |  | C |
| ＂ | 5－274 | AA |  | C |
| ＂ | 5－275 | AA |  | C |
| ＂ | 5－384 | AA |  | C |
| VRS－TV2AB103J | 5－379 | AA |  | C |
| ＂ | 5－380 | AA |  | C |
| VRS－TV2AB113F | 5－396 | AA |  | C |
| VRS－TV2AB221J | 5－419 | AA |  | C |
| VS2SA2059＋＋－1 | 5－262 | AH |  | B |
| VS2SC2412K／－1 | 5－268 | AB |  | B |
| VSBS108／／／／－1 | 6－59 | AE |  | B |
| VSKTC3198GR－1 | 6－60 | AD |  | B |
| VSRN1402F＋＋－1 | 5－263 | AF |  | B |
| ＂ | 5－264 | AF |  | B |
| ＂ | 5－266 | AF |  | B |
| VSRN1406F＋＋－1 | 5－265 | AF |  | B |
| ＂ | 5－267 | AF |  | B |
| ＂ | 6－61 | AF |  | B |
| ＂ | 6－62 | AF |  | B |
| ＂ | 6－63 | AF |  | B |
| ＂ | 6－64 | AF |  | B |
| VSRN2402F＋＋－1 | 6－65 | AF |  | B |


| PARTS CODE | No． | PRICE RANK | NEW MARK | PART RANK |
| :---: | :---: | :---: | :---: | :---: |
| VVLLMG2025TPR | 2－13 | BA |  | B |
| 【 X 】 |  |  |  |  |
| XEBS726P08000 | 2－B1 | AE |  | C |
| XEBS726P10000 | 2－B3 | AC |  | C |
| XEBS730P08000 | 1－B2 | AC |  | C |
| XEBS730P10000 | 1－B1 | AC |  | C |
| ＂ | 3－B2 | AC |  | C |
| XHBS730P06000 | 1－B4 | AC |  | C |
| XWHS740－08100 | 1－W1 | AA |  | C |
| 【 0 】 |  |  |  |  |
| OKY0MPS902200 | 7－31 | AF |  | C |
| OKYC10B2SQ470 | 7－7 | AG |  | C |
| OKYC1102CC333 | 7－9 | AC |  | C |
| OKYC1102EC472 | 7－8 | AC |  | C |
| OKYC1131EC101 | 7－10 | AC |  | C |
| OKYC1132BC104 | 7－13 | AC |  | C |
| OKYC1384QS472 | 7－5 | AG |  | C |
| ／ | 7－6 | AG |  | C |
| OKYC2131QS104 | 7－3 | AG |  | C |
| 0KYC30A0BQ471 | 7－12 | AL |  | C |
| OKYC30A0EQ101 | 7－11 | AH |  | C |
| OKYC3138MS390 | 7－4 | AU |  | C |
| OKYD1057AQ006 | 7－22 | AF |  | B |
| ＂ | 7－23 | AF |  | B |
| ＂ | 7－24 | AF |  | B |
| ＂ | 7－25 | AF |  | B |
| OKYD2049BQ202 | 7－26 | AQ |  | B |
| OKYD2051AQ002 | 7－16 | AD |  | B |
| ＂ | 7－17 | AD |  | B |
| ＂ | 7－19 | AD |  | B |
| ＂ | 7－20 | AD |  | B |
| OKYD20Q0AQ003 | 7－27 | AL |  | B |
| OKYD4055AQ022 | 7－18 | AD |  | B |
| OKYD4055AQ065 | 7－21 | AE |  | B |
| OKYD4145AA006 | 7－28 | AK |  | B |
| 0KYD7114AR005 | 7－65 | AG |  | B |
| OKYH2211AR001 | 7－32 | AL |  | B |
| OKYH7152AS001 | 7－36 | AL |  | B |
| OKYK2051AQ002 | 7－14 | AG |  | C |
| OKYK2148LS002 | 7－15 | AG |  | C |
| OKYK7135AS003 | 7－29 | AM |  | A |
| ＂ | 7－30 | AM |  | A |
| OKYL1173JL553 | 7－33 | AS |  | C |
| OKYL2100DS010 | 7－63 | BA |  | B |
| OKYL5051AQ001 | 7－1 | AE |  | C |
| ＂ | 7－2 | AE |  | C |
| OKYR1053UQ470 | 7－53 | AC |  | C |
| OKYR1053UQ562 | 7－55 | AC |  | C |
| ＂ | 7－56 | AC |  | C |
| ＂ | 7－57 | AC |  | C |
| OKYR3111VC182 | 7－45 | AC |  | C |
| OKYR3111VC221 | 7－51 | AC |  | C |
| OKYR3111VC223 | 7－50 | AB |  | C |
| OKYR3111VC272 | 7－59 | AC |  | C |
| OKYR3111VC333 | 7－46 | AB |  | C |
| OKYR3111VC472 | 7－60 | AC |  | C |
| OKYR3111VC682 | 7－48 | AB |  | C |
| OKYR3114VC103 | 7－49 | AC |  | C |
| OKYR3114VC183 | 7－43 | AC |  | C |
| OKYR3114VC562 | 7－61 | AC |  | C |
| ＂ | 7－62 | AC |  | C |
| OKYR3121TC101 | 7－47 | AB |  | C |
| ＂ | 7－58 | AB |  | C |
| OKYR3121TC222 | 7－44 | AC |  | C |
| OKYR3121TC681 | 7－52 | AB |  | C |
| OKYR3126TC394 | 7－40 | AC |  | C |
| ＂ | 7－41 | AC |  | C |
| ＂ | 7－42 | AC |  | C |
| OKYR3133AC225 | 7－39 | AC |  | C |
| ＂ | 7－54 | AC |  | C |
| OKYR8054EQ102 | 7－64 | AG |  | C |
| 0KYT2718KL001 | 7－37 | AV |  | B |
| OKYT4097CC002 | 7－38 | AG |  | B |
| OKYW0000AQ005 | 7－34 | AC |  | C |
| OKYW0000AQ012 | 7－35 | AC |  | C |

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